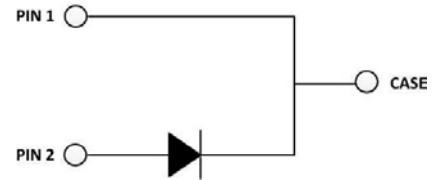


# 3<sup>rd</sup> Generation 650V/20A SiC Schottky Barrier Diode

## Features

- AEC-Q101 qualified
- Revolutionary semiconductor material - Silicon Carbide (SiC)
- No reverse recovery
- High-speed switching performance
- Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 175°C
- RoHS compliant



Package Type: TO-252-2L

## Potential Applications

- DC/DC converter for EV/HEV
- On board charger (OBC)



## Description

The ADS065J020D3 SiC Schottky Barrier Diode (SBD) has been developed using Sanan’s advanced 3<sup>rd</sup> generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 650V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components and being AEC-Q101 qualified, it is qualified for use in automotive application.

## Product Specifications

Device	V <sub>RRM</sub>	I <sub>F</sub> (135°C)	V <sub>F</sub> (25°C)	Q <sub>C</sub>	Marking
ADS065J020D3	650V	25A	1.35V	55nC	DS065020D3

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**Table 1. Maximum Ratings**

(T<sub>c</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V <sub>RRM</sub>	650	V	T <sub>C</sub> = 25°C
Surge peak reverse voltage	V <sub>RSM</sub>	650		T <sub>C</sub> = 25°C
DC reverse voltage	V <sub>DC</sub>	650		T <sub>C</sub> = 25°C
Continuous forward current	I <sub>F</sub>	51	A	T <sub>C</sub> = 25°C
		25		T <sub>C</sub> = 135°C
		20		T <sub>C</sub> = 150°C
Surge non-repetitive forward current	I <sub>FSM</sub>	110	A	T <sub>C</sub> = 25°C, t <sub>p</sub> = 10ms, half sine pulse
Non-repetitive peak forward current	I <sub>F,Max</sub>	520	A	T <sub>C</sub> = 25°C, t <sub>p</sub> = 10μs, pulse
Surge repetitive forward current	I <sub>FRM</sub>	47	A	T <sub>C</sub> = 25°C, t <sub>p</sub> = 10ms, half sine wave D = 0.1
Power dissipation	P <sub>tot</sub>	178	W	T <sub>C</sub> = 25°C
i <sup>2</sup> t value	∫i <sup>2</sup> dt	60	A <sup>2</sup> s	T <sub>C</sub> = 25°C, t <sub>p</sub> = 10ms
Operating junction temperature	T <sub>j</sub>	-55~175	°C	
Storage temperature	T <sub>stg</sub>	-55~175	°C	

**Table 2. Thermal Resistance**

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R <sub>th(j-c)</sub>	/	0.84	/	°C/W	

**Table 3. Static Electrical Characteristics**

(T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V <sub>DC</sub>	650	/	/	V	I <sub>R</sub> = 100 μA
Forward voltage	V <sub>F</sub>	/	1.35	1.50	V	I <sub>F</sub> = 20A, T <sub>j</sub> = 25°C
		/	1.70	1.90		I <sub>F</sub> = 20A, T <sub>j</sub> = 175°C
Reverse current	I <sub>R</sub>	/	2	40	μA	V <sub>R</sub> = 650V, T <sub>j</sub> = 25°C
		/	10	100		V <sub>R</sub> = 650V, T <sub>j</sub> = 175°C

**Table 4. Dynamic Electrical Characteristics**

(T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	1018	/	pF	V <sub>R</sub> = 0V, f = 1MHz
		/	104	/		V <sub>R</sub> = 200V, f = 1MHz
		/	89	/		V <sub>R</sub> = 400V, f = 1MHz
Total capacitive charge	Q <sub>C</sub>	/	55	/	nC	V <sub>R</sub> = 400V
Capacitance stored energy	E <sub>C</sub>	/	8.3	/	μJ	V <sub>R</sub> = 400V

### Electrical Characteristic Diagrams

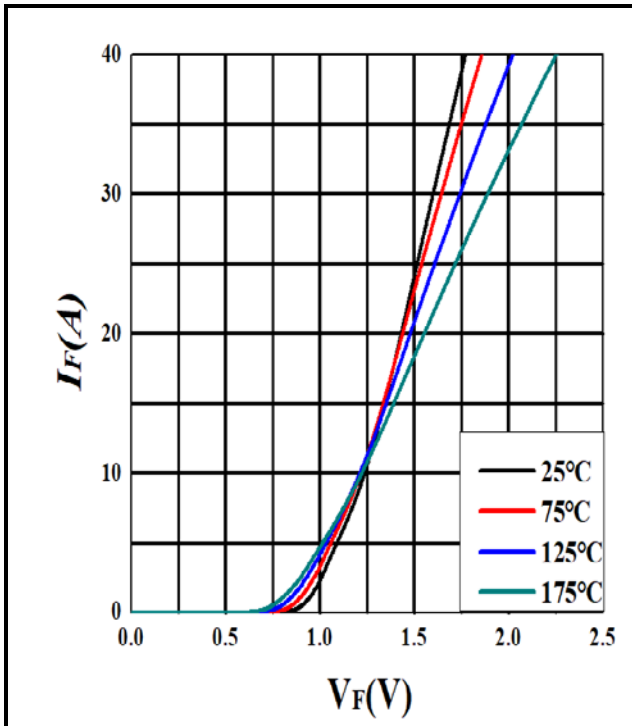


Figure 1. Forward characteristics

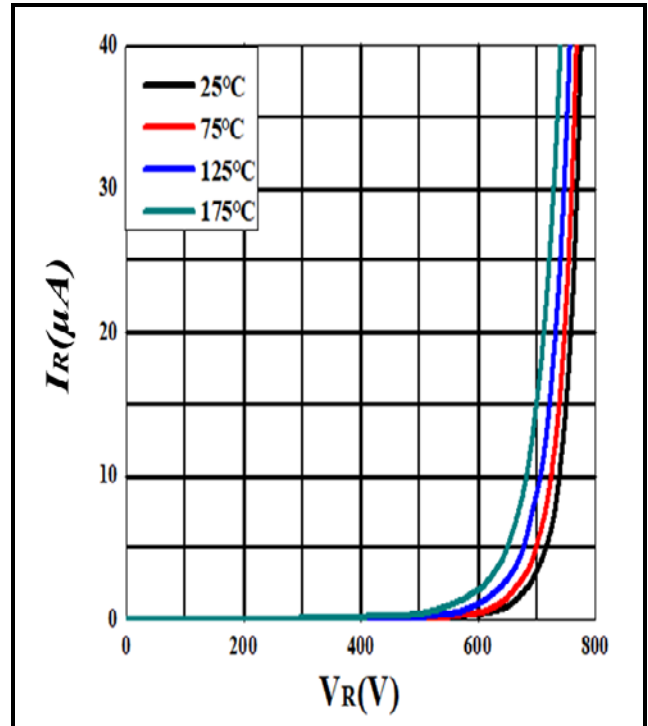


Figure 2. Reverse characteristics

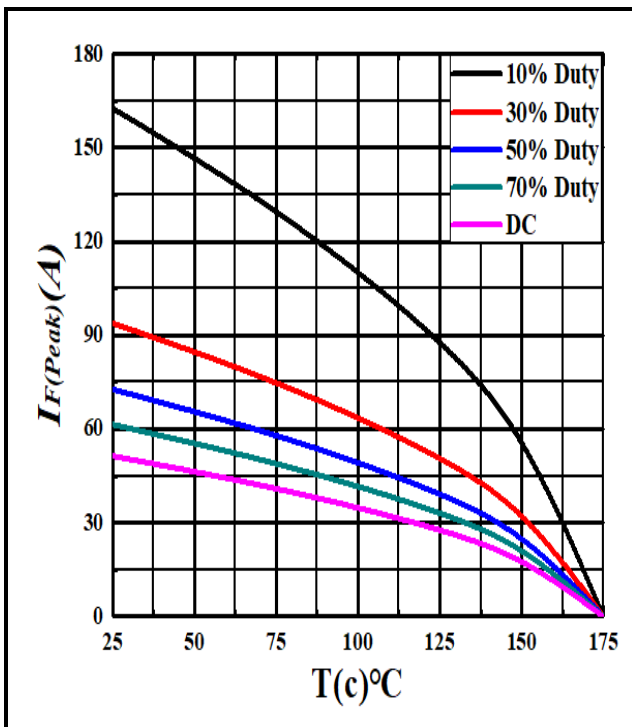


Figure 3. Current derating

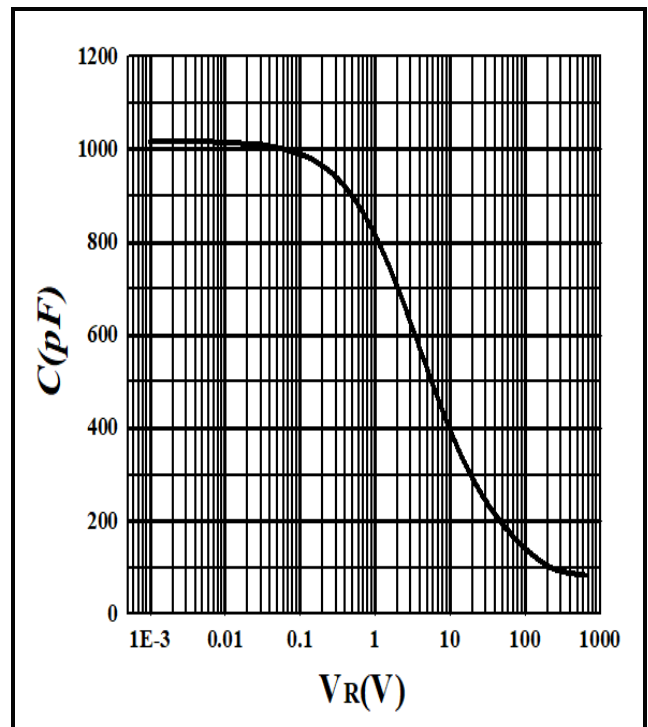


Figure 4. Capacitance vs. reverse voltage

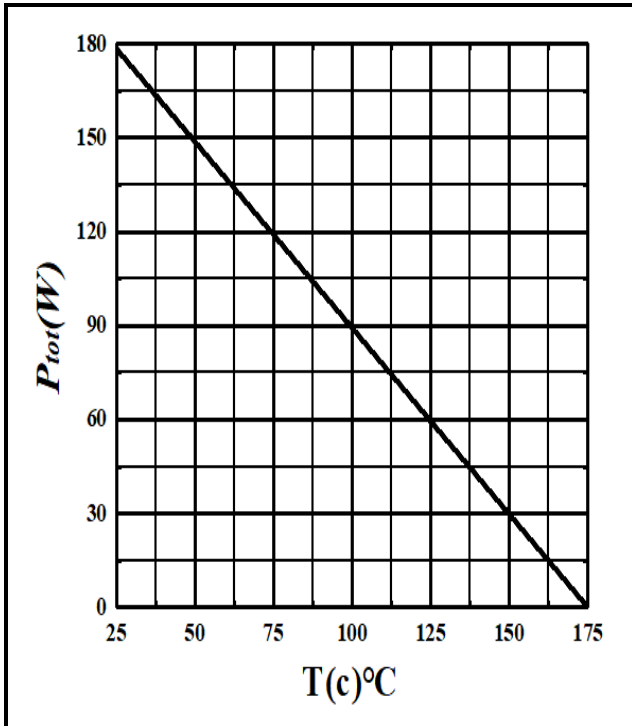


Figure 5. Power derating

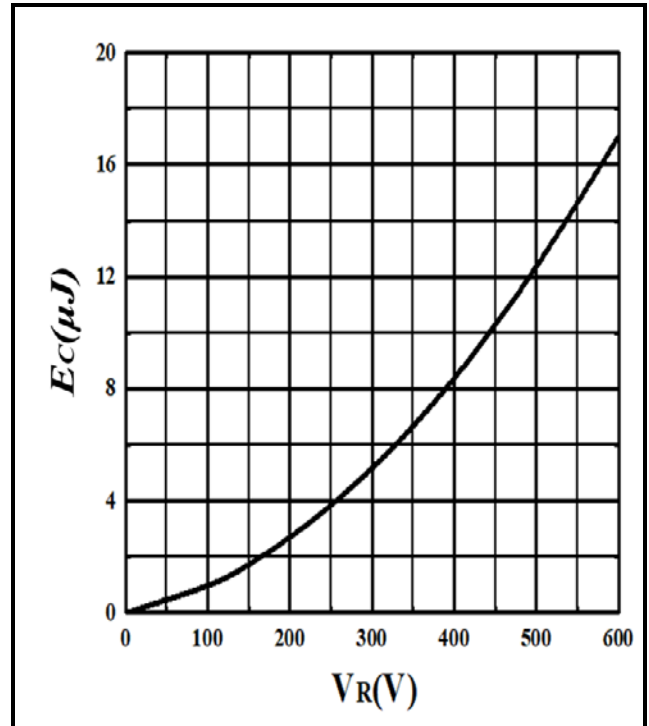


Figure 6. Capacitance stored energy

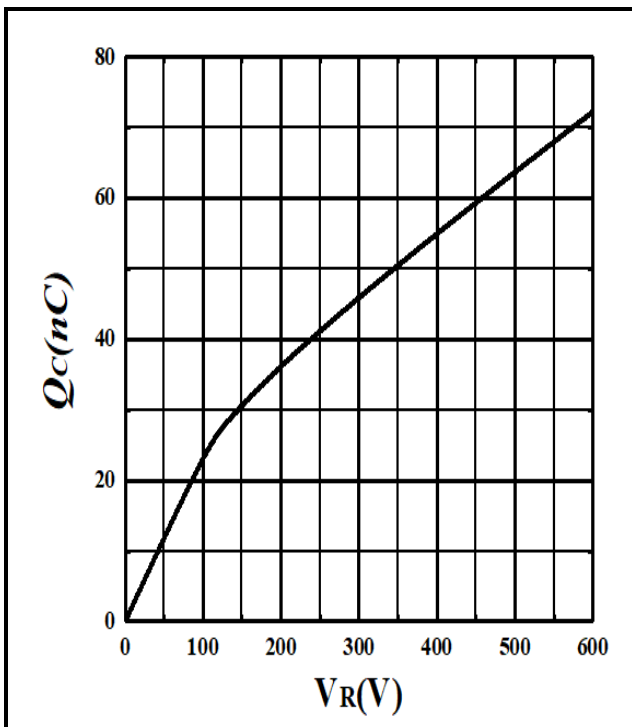


Figure 7. Total capacitance charge vs. reverse voltage

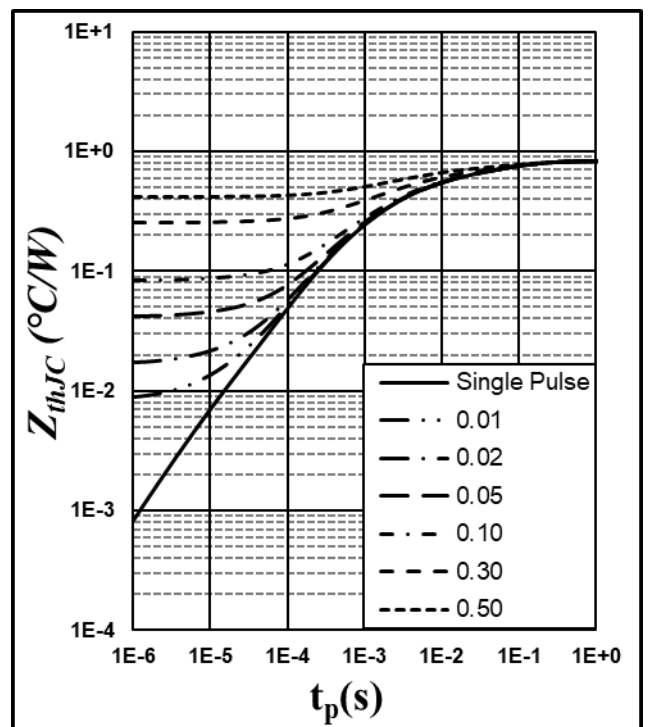
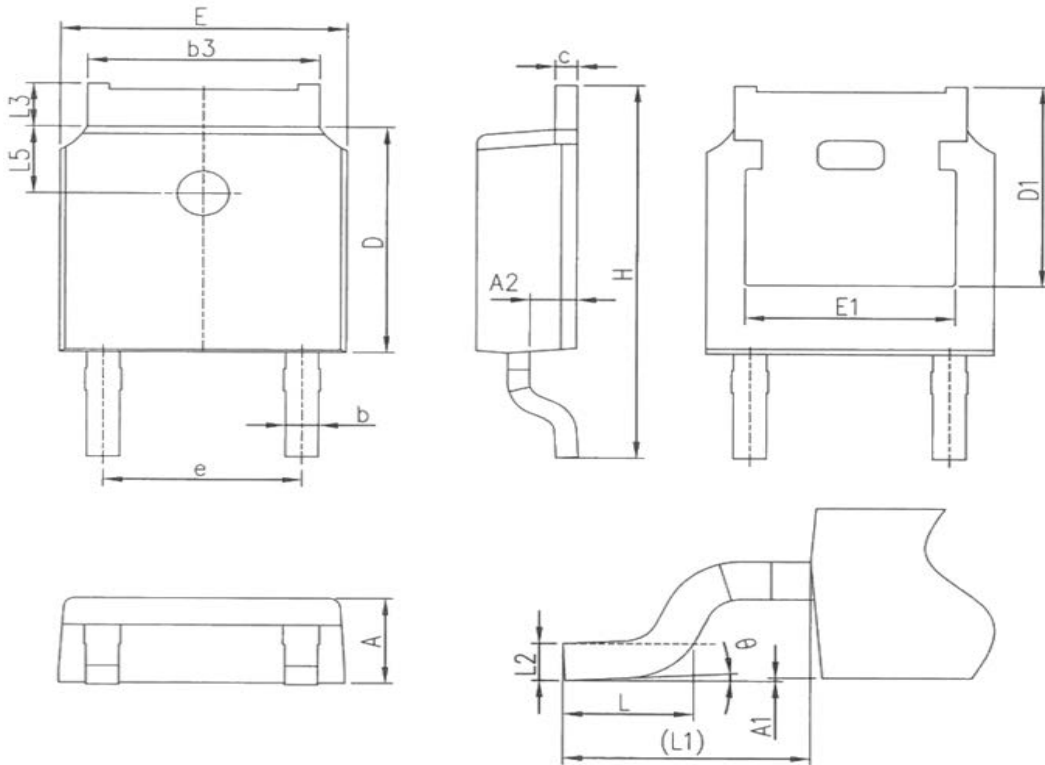


Figure 8. Transient Thermal Impedance Junction Case

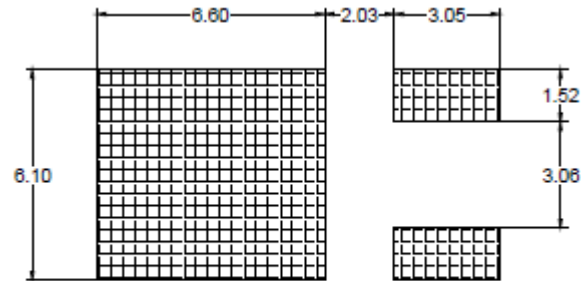
**Package Information**



SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	4.572BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L5	1.65	1.80	1.95
θ	0°	-	8°

## Recommended Solder Pad Layout

Note: All dimensions are in mm



TO-252-2L

## Ordering Information

Part number	ADS065J020D3-ASARH
Package	TO-252-2L
Unit quantity	2500 EA
Packing type	Tape & Reel

## Important Notices – Read Carefully

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