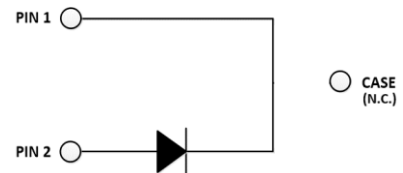


# 3<sup>rd</sup> Generation 650V/12A SiC Schottky Barrier Diode

## Features

- Ceramic Package Provides 2.5kV Isolation
- Revolutionary semiconductor material - Silicon Carbide (SiC)
- No reverse recovery
- High-speed switching performance
- Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 175°C
- RoHS compliant



## Potential Applications

- Industrial power supplies: Industrial UPS
- Battery chargers
- Solar inverters
- Switch mode power supplies

Package Type: TO-220N-2L



## Description

The SDS065J012N3 SiC Schottky Barrier Diode (SBD) has been developed using Sanan’s advanced 3<sup>rd</sup> generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 650V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

## Product Specifications

Device	V <sub>RRM</sub>	I <sub>F</sub> (135°C)	V <sub>F</sub> (25°C)	Q <sub>c</sub>	Marking
SDS065J012N3	650V	15A	1.35V	36nC	DS065012N3





**Table 3. Static Electrical Characteristics**

 (T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V <sub>DC</sub>	650	/	/	V	I <sub>R</sub> = 100 μA
Forward voltage	V <sub>F</sub>	/	1.35	1.50	V	I <sub>F</sub> = 12A, T <sub>j</sub> = 25°C
		/	1.60	1.80		I <sub>F</sub> = 12A, T <sub>j</sub> = 175°C
Reverse current	I <sub>R</sub>	/	1	36	μA	V <sub>R</sub> = 650V, T <sub>j</sub> = 25°C
		/	10	96		V <sub>R</sub> = 650V, T <sub>j</sub> = 175°C

**Table 4. Dynamic Electrical Characteristics**

 (T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	650	/	pF	V <sub>R</sub> = 0V, f = 1MHz
		/	72	/		V <sub>R</sub> = 200V, f = 1MHz
		/	66	/		V <sub>R</sub> = 400V, f = 1MHz
Total capacitive charge	Q <sub>C</sub>	/	36	/	nC	V <sub>R</sub> = 400V
Capacitance stored energy	E <sub>C</sub>	/	5.7	/	μJ	V <sub>R</sub> = 400V

### Electrical Characteristic Diagrams

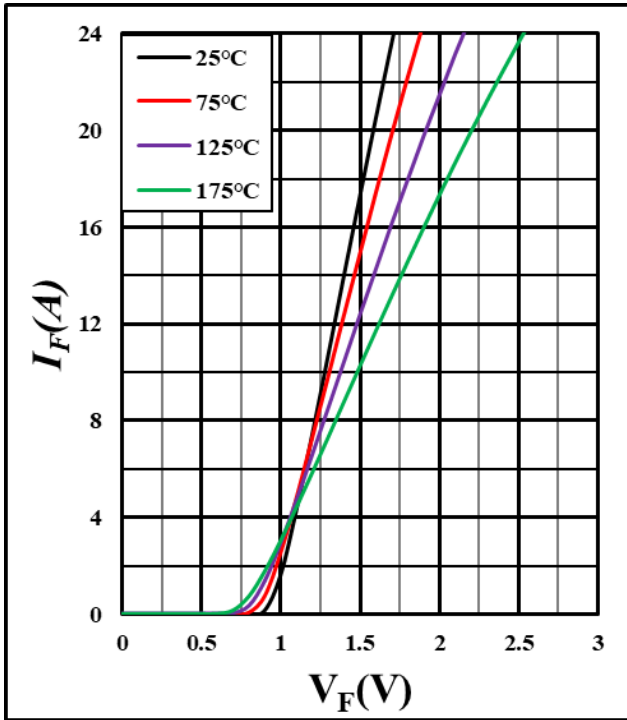


Figure 1. Forward characteristics

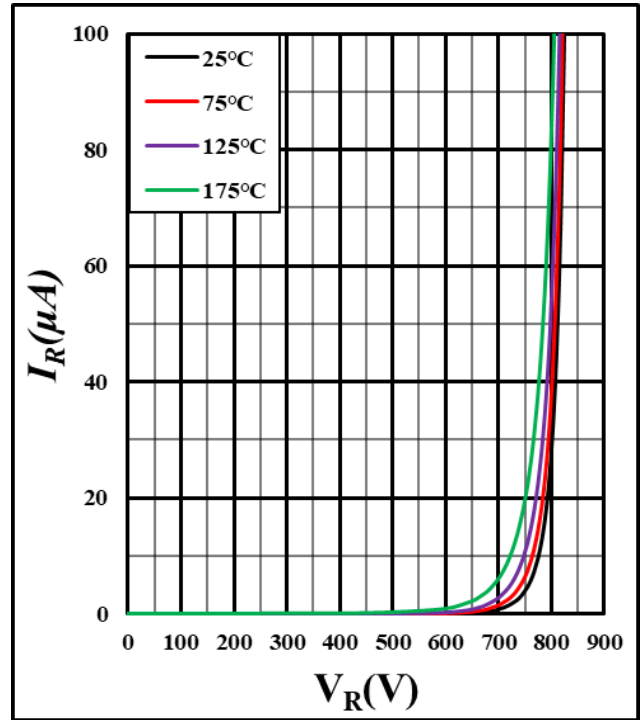


Figure 2. Reverse characteristics

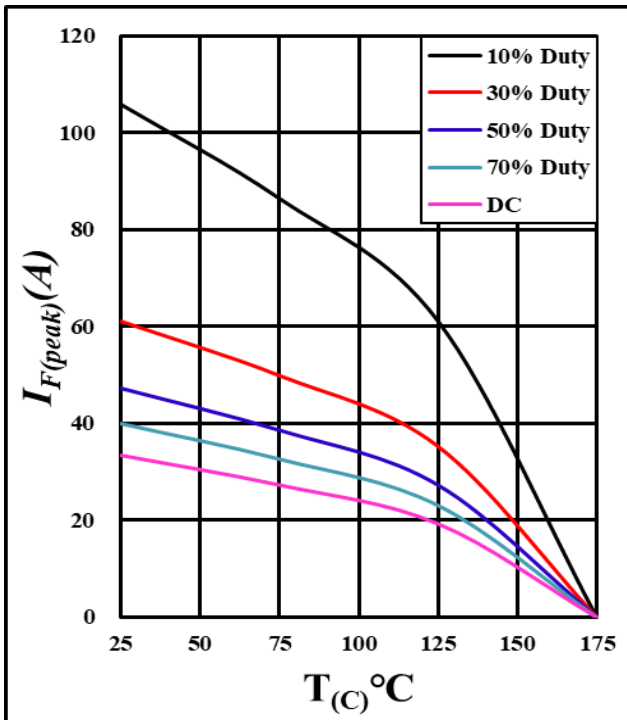


Figure 3. Current derating

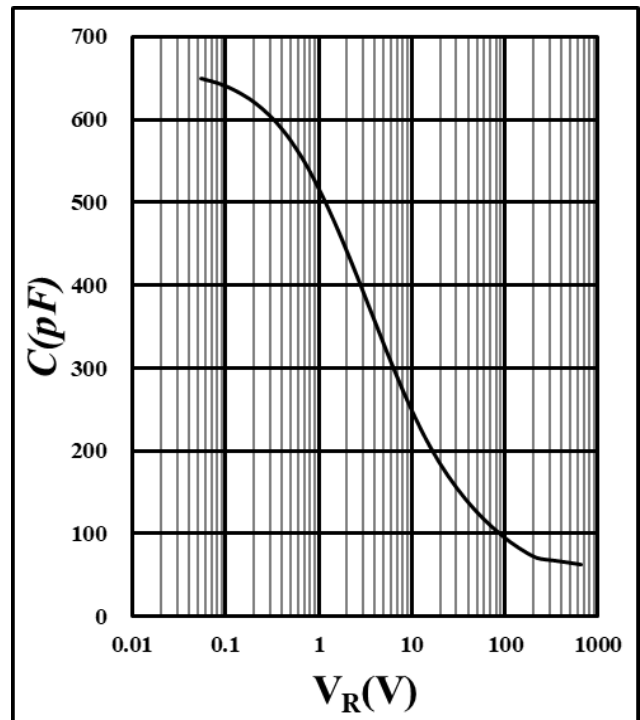


Figure 4. Capacitance vs. reverse voltage

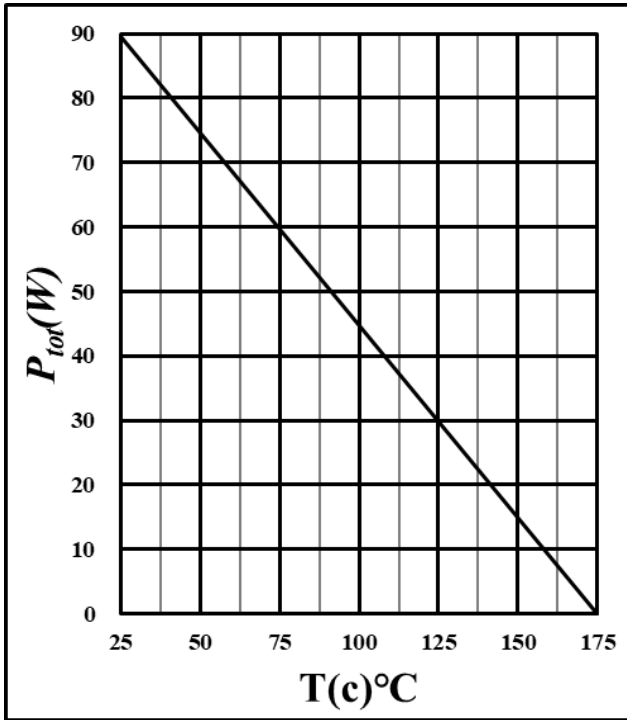


Figure 5. Power derating

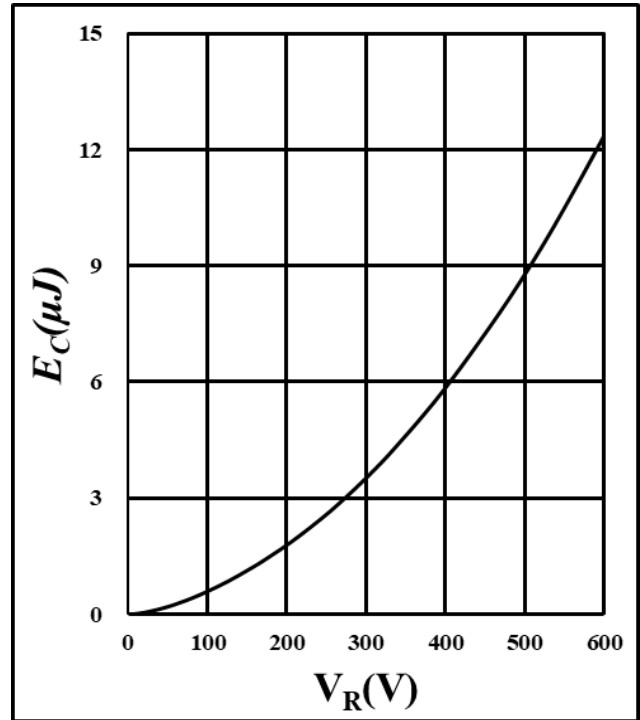


Figure 6. Capacitance stored energy

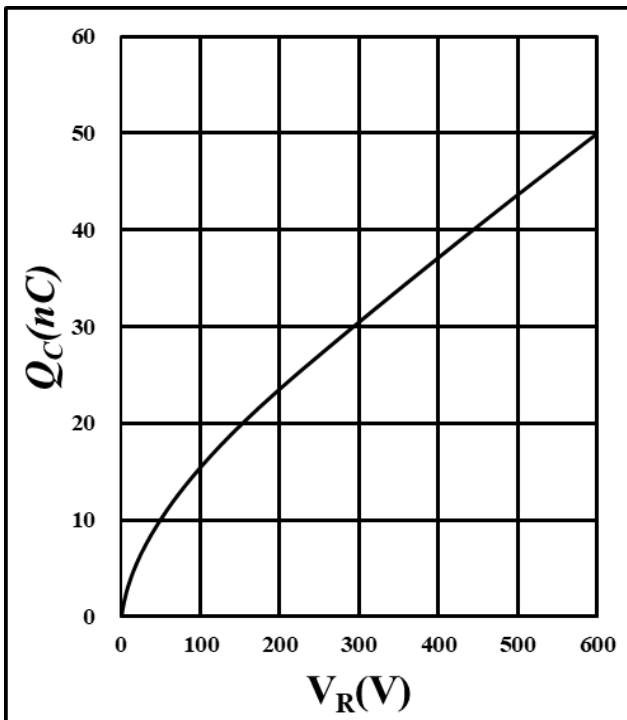
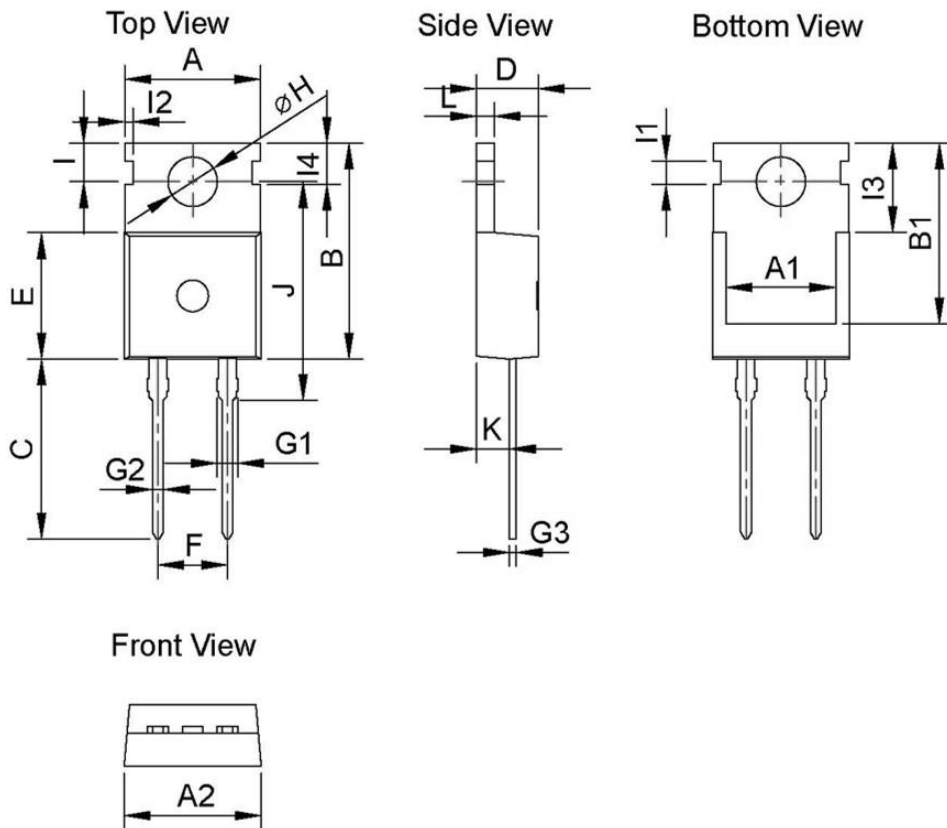


Figure 7. Total capacitance charge vs. reverse voltage

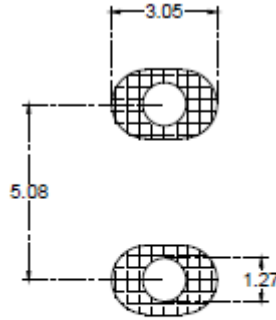
**Package Information**



Dimension unit: [mm]			
Symbol	Min	Nom	Max
A	9.66	9.90	10.28
A1	6.90	-	-
A2	9.80	10.00	10.20
B	15.60	15.70	15.80
B1	13.16 REF		
C	12.70	13.08	14.27
D	4.30	4.50	4.70
E	8.59	9.20	9.40
F	5.08 BSC		
G1	1.32	1.52	1.62
G2	0.70	0.80	0.95
G3	0.45	0.50	0.60
$\phi H$	3.53	3.60	3.70
I	2.70	2.80	2.90
I1	1.70 REF		
I2	0.59 REF		
I3	6.50 REF		
I4	3.00 REF		
J	15.70	15.90	16.25
K	2.20	2.40	2.60
L	1.15	1.30	1.40

## Recommended Solder Pad Layout

Note: All dimensions are in mm



TO-220N-2L

## Ordering Information

Part number	SDS065J012N3-ISATH
Package	TO-220N-2L
Unit quantity	1000 EA
Packing type	Tube



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