Contact Information

Sanan Mega Fab in Hunan
No.399, Changxing Road, Hi-tech Industrial Development Zone, Changsha, Hunan, PRC
E-mail: sales@sanan-ic.com
Tel: 86-731-81889206

Hong Kong，China
Unit 3805&3806, 38th Floor, Tower Two, Lippo Centre, No.89 Queensway, Hong Kong，China
E-mail: sales.hk@sanan-e.com
Tel: 852-39520301

Americas
1145 Sonora Court, Sunnyvale, California
E-mail: sanan-semi@luminus.com

Europe
Sanan Europe GmbH Max-Planck-Straße 8
85609 Aschheim Germany

Japan
4F-1, IXINAL Monzennakacho, 2-5-4, Fukuzumi, Kohto-Ward, Tokyo, Japan
E-mail: sales@sanan-jt.com

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Sanan Product Brochure SiC and GaN Material

Semi-insulating SiC Substrate

Hunan Sanan Semiconductor Co., Ltd

Company Profile

Sanan Semiconductor Co., Ltd (Sanan Semiconductor), as a wholly-owned subsidiary of the listed company Sanan Optoelectronics (600703CH), is a manufacturer focusing on the power electronics field, providing power semiconductor materials and foundry services.

We are the technology commercialization leader with the capacity and scale to bring large diameter wafers and high-quality epitaxy to the market in mass production volumes.

Sanan Semiconductor has long-proven expertise in SiC and GaN materials technology advancement, We have achieved stable supply to over 20 domestic and international customers. Our current production capacity includes 360,000 6-inch wafers and 520,000 8-inch wafers annually, with a long-term planned capacity of 1 million wafers per year. Our products primarily serve the manufacturing of SiC MOSFETs, with end applications spanning automotive, photovoltaic and energy storage systems (PV & ESS), charging infrastructure, power supplies, and home appliances.

Polytype	Single-Crystal 4H
Supported diameters	150 mm & 200 mm
Crystal structure	Hexagonal
Bandgap	3.26 eV
Thermal conductivity (n-type; 0.020 Ω•cm)	a~4.2 W/cm • K @ 298 K c~3.7 W/cm • K @ 298 K
Lattice parameters	a=3.073 Å c=10.053 Å
Mohs hardness	9

n-TYPE SiC Substrate

High Purity Semi-insulating SiC Substrate

SiC Optical Substrate

SiC Heatsink Substrate

SiC EPITAXY
n-type and p-type Epitaxial Layers

NITRIDE EPITAXY
GaN,AlGaN, AlInN Epitaxial Layers

DIMENSIONAL PROPERTIES, TERMINOLOGY AND METHODS*

DIMENSIONAL

The linear dimension across the surface of a wafer. Measurement is performed using an automated optical micrometer, providing the average value for each individual wafer.

THICKNESS,CENTER POINT

Measured with non-contact tools at the center of each individual wafer.

SURFACE ORIENTATION

Denotes the orientation of the surface of a wafer with respect to a crystallographic plane within the lattice structure. In wafers cut intentionally “off orientation,” the direction of cut is parallel to the primary flat or notch, away from the secondary flat (if present). Measured with x-ray goniometer on a sample of one wafer per boule in the center of the wafer.

ORTHOGONAL MISORIENTATION

In wafers intentionally cut “off orientation,” the angle between the projection of the surface normal onto a (0001) plane and the nearest <11-20> direction.

SUBSTRATE	EPITAXY
6’ N-Type	6’ N-Type
8’ N-Type	8’ N-Type
8’ Optical	

FLATLENGTH

Linear dimension of the flat measured with automated optical micrometer on a sample of one wafer Per boule (see Figure 1).

PRIMARY FLAT

The flat of the longest length on the wafer, oriented such that the chord is parallel with a specified Low-index crystal plane.

PRIMARY FLAT ORIENTATION

The primary flat is the (1-100) plane with the flat face parallel to the <11-20> direction. Measured with XRD back reflection technique.

NOTCH

The notch position is parallel to the <11-20> direction, with the notch bisector is in the (1-100) plane (see Figure 2).

MARKING*

For silicon face polished material, the carbon face of each individual wafer is laser-marked with OCR compatible font, similar to definitions and characteristics in SEMI M12. The laser markings are positioned upright when the major flat or notch is oriented up, making the scribe easier to read when the wafers are loaded into cassettes. This format includes a wafer supplier identification code, validating the wafer’ s authenticity. It also includes a checksum, which is an error-detection method that prevents OCR mis-read errors and reduces the instance of processing errors associated with such events.

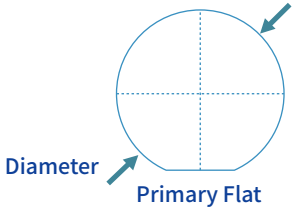

Figure1. Diameter, primary flat location and marking orientation, carbonface up for silicon face polished 150mm n-type wafers

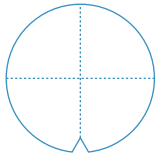

Figure2. Notch location and marking orientation, carbonface up for 200mm 200mm n-type wafers

Note：* SEMI M12 coding rule is only for 200 mm wafer.

PRODUCTS

6 Inch Semi-insulating SiC Substrate

Product Performance	P grade
1. Ingot characteristics	
Polytype	4H
Micropipe Density	⩽0.5 ea /cm²
2.Electrical characteristics	
Doping	High Purity
Resistivity	⩾1E8 Ω·cm
3.Mechanical characteristics	
Diameter	150.0 mm ±0.2 mm
Thickness	500 ± 25 μm
Surface orientation	0°towards <11-20> ±0.2°
Notch orientation	<1-100> ±5°
Notch Depth	1-1.25 mm
Notch Angle	90° +5°/-1°
TTV	⩽5 μm
LTV	⩽3 μm (10 mm X 10 mm)
Warp	⩽30 μm
Bow (absolute value)	⩽15 μm
4. Surface Parameters	
Visual Carbon inclusion	无
Hexagonal	无
Metal element content	⩽5E10 atoms/cm² (Na、K、Al、Fe、Ni、Cu、Zn、Cr、Hg、Ti、Ca、Mn)

5.Frontside quality	
Frontside	Si
Surface process	CMP
Particle(Size⩾0.3 μm)	⩽75 ea/wafer(Size⩾0.3 μm)
Scratch	Count ⩽5 e.a, Cumulative length ⩽ 150 mm Scratch detection rule (Scratch = Length >10 μm and width > 2 μm)
Si face Ra	Ra⩽0.2 nm (10 μm X 10 μm)
Edge chip	无
6.Backside quality	
Backside	C
Backside process	CMP
Edge chip	无
Backside Laser ID	SEMI 字体 / 位置与切口中心对齐
Note:None means no	