

6th Generation 650V/4A SiC Schottky Barrier Diode

Features

- Revolutionary semiconductor material - Silicon Carbide (SiC)
- No reverse recovery
- High-speed switching performance
- Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 150°C
- RoHS compliant



Package Type: SMA



Potential Applications

- Switch mode power supplies

Description

The PS6D060DA SiC Schottky Barrier Diode (SBD) has been developed using Sanan's advanced 6th generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 650V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

Product Specifications

Device	V_{RRM}	I_F (135°C)	V_F (25°C)	Q_C	Marking
PS6D060DA	650V	1.7A	1.20V	9.3nC	66DA

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Table 1. Maximum Ratings

(T_c = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V _{RRM}	650	V	T _C = 25°C
Surge peak reverse voltage	V _{RSM}	650		T _C = 25°C
DC reverse voltage	V _{DC}	650		T _C = 25°C
Continuous forward current	I _F	4.9	A	T _C = 25°C
		4		T _C = 65°C
		1.7		T _C = 135°C
Surge non-repetitive forward current	I _{FSM}	32	A	T _C = 25°C, t _p = 10ms, half sine pulse
		27		T _C = 150°C, t _p = 10ms, half sine pulse
Surge repetitive forward current	I _{FRM}	23	A	T _C = 25°C, t _p = 10ms, half sine wave D = 0.1
Power dissipation	P _{tot}	8.8	W	T _C = 25°C
i ² t value	∫i ² dt	5	A ² s	T _C = 25°C, t _p = 10ms
Operating junction temperature	T _j	-55~150	°C	
Storage temperature	T _{stg}	-55~150	°C	

Table 2. Thermal Resistance

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R _{th(j-c)}	/	14.2	/	°C/W	

Table 3. Static Electrical Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V_{DC}	650	/	/	V	$I_R = 1\text{mA}$
Forward voltage	V_F	/	1.20	1.40	V	$I_F = 4\text{A}$, $T_j = 25^\circ\text{C}$
		/	1.45	/		$I_F = 4\text{A}$, $T_j = 150^\circ\text{C}$
Reverse current	I_R	/	35	500	μA	$V_R = 650\text{V}$, $T_j = 25^\circ\text{C}$
		/	250	/		$V_R = 650\text{V}$, $T_j = 150^\circ\text{C}$

Table 4. Dynamic Electrical Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	272	/	pF	$V_R = 0\text{V}$, $f = 1\text{MHz}$
		/	17	/		$V_R = 200\text{V}$, $f = 1\text{MHz}$
		/	13	/		$V_R = 400\text{V}$, $f = 1\text{MHz}$
Total capacitive charge	Q_C	/	9.3	/	nC	$V_R = 400\text{V}$
Capacitance stored energy	E_C	/	1.3	/	μJ	$V_R = 400\text{V}$

Electrical Characteristic Diagrams

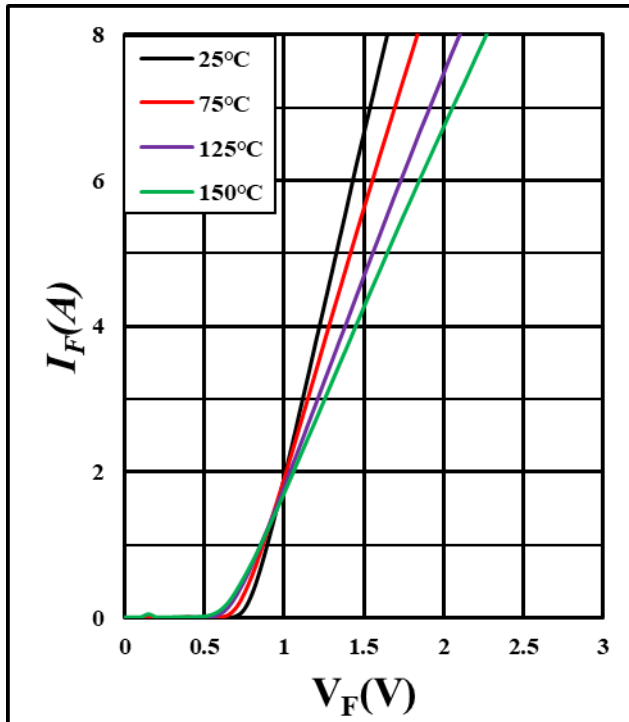


Figure 1. Forward characteristics

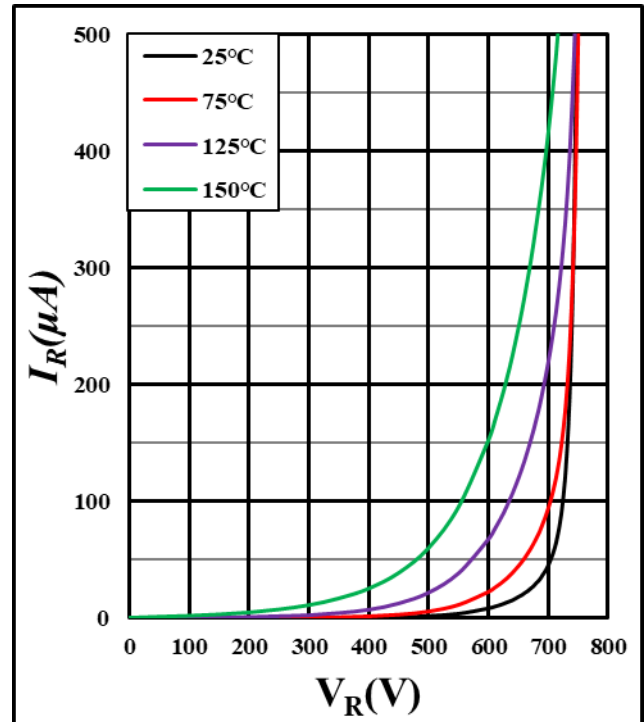


Figure 2. Reverse characteristics

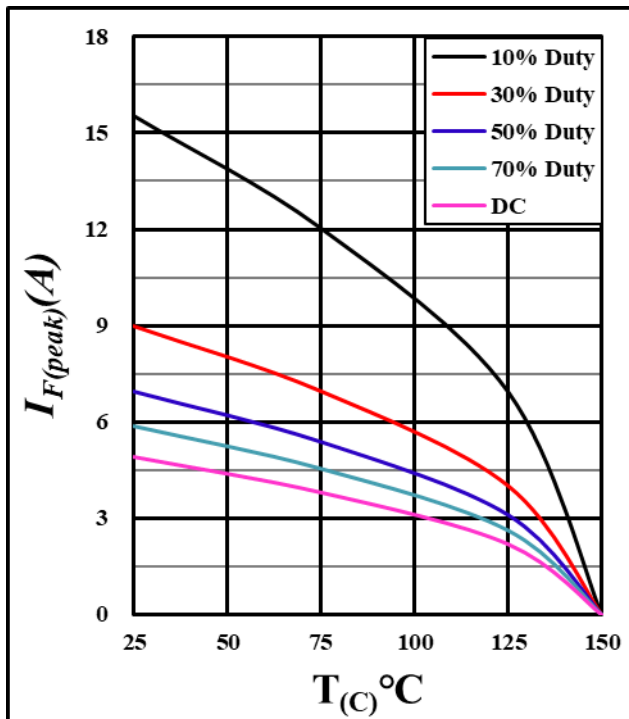


Figure 3. Current derating

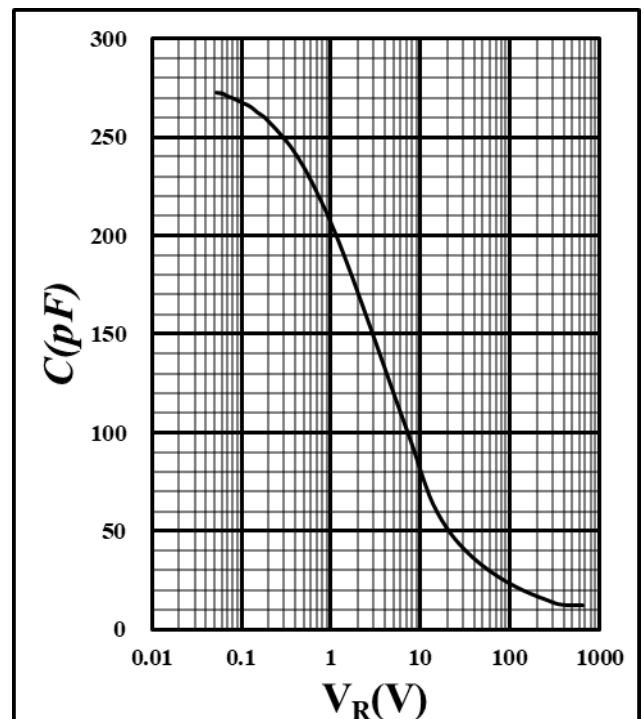


Figure 4. Capacitance vs. reverse voltage

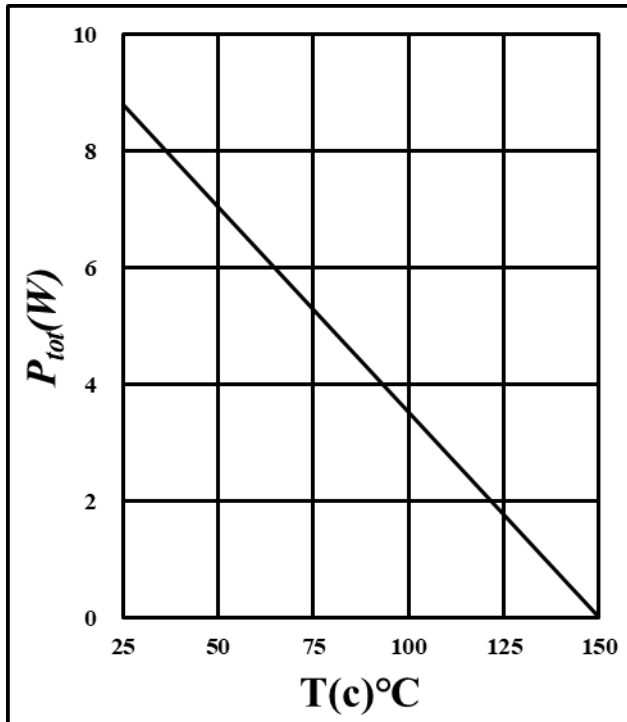


Figure 5. Power derating

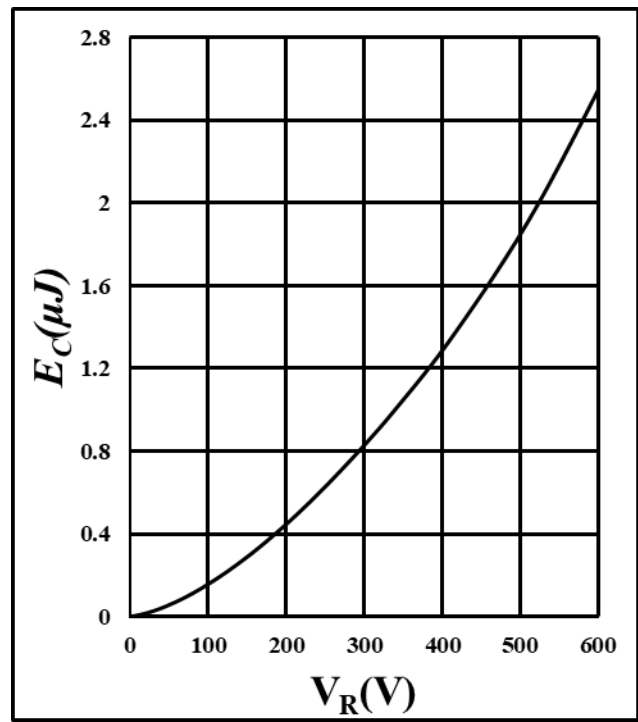


Figure 6. Capacitance stored energy

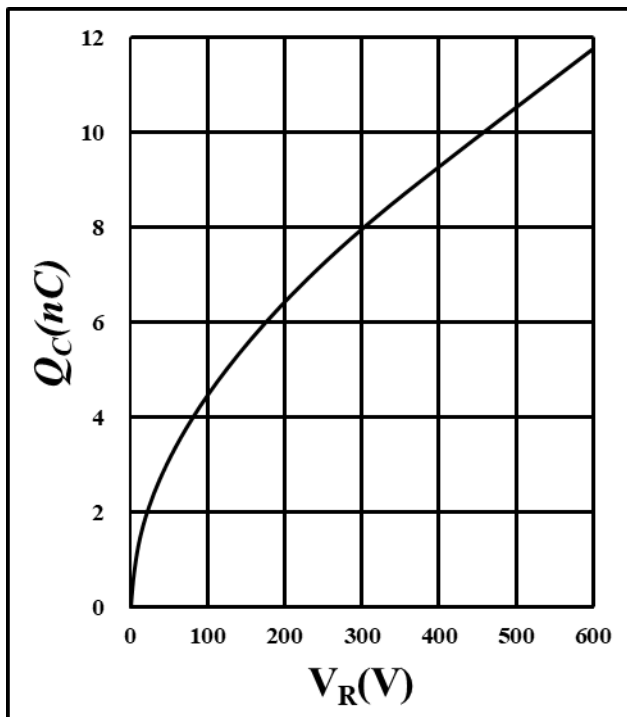
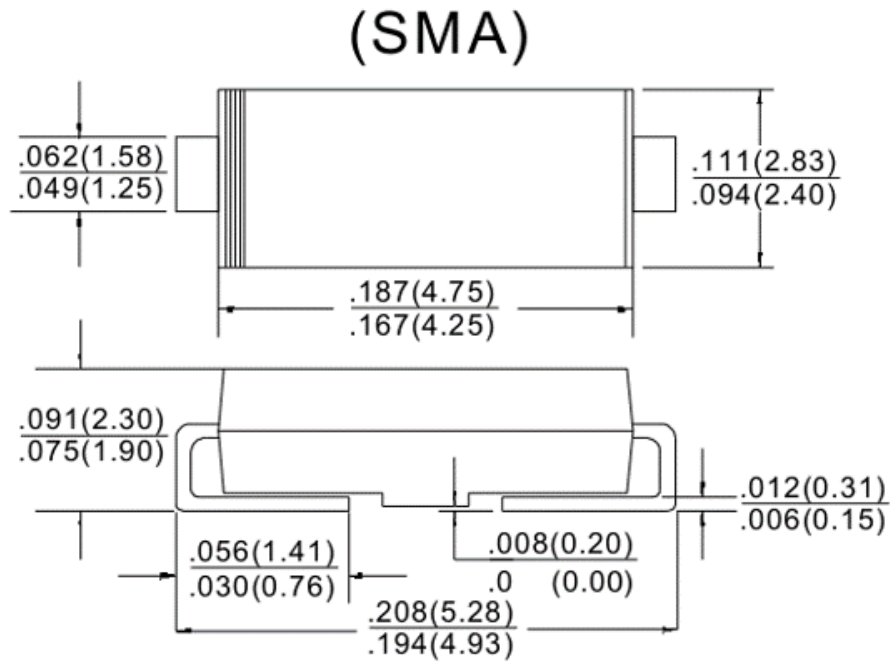


Figure 7. Total capacitance charge vs. reverse voltage

Package Information

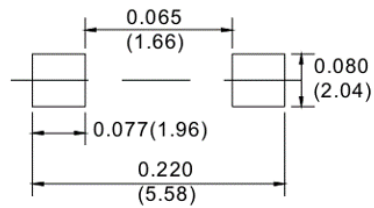


Dimensions in inches and (millimeters)

Recommended Solder Pad Layout

Note: All dimensions are in mm

Mounting Pad Layout



(SMA)

Ordering Information

Part number	PS6D060DA-CSARH
Package	SMA
Unit quantity	5000 EA
Packing type	Tape & Reel

Important Notices – Read Carefully

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