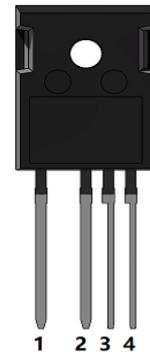


1200V 20mΩ Silicon Carbide Power MOSFET

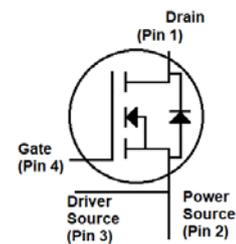
Features

- AEC-Q101 qualified
- High blocking voltage with low on-resistance
- High switching speed with low capacitance
- Very fast and robust intrinsic body diode with low reverse recovery
- Very low switching losses
- Excellent avalanche ruggedness



Benefits

- Greater system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency
- Easy to parallel and simple to drive



Potential Applications

- Solar inverters
- Uninterrupted power supplies
- Switch mode power supplies
- Motor drives

Package Type: TO-247-4LB



Description

The Sanan Semiconductor 1200V/20mΩsilicon carbide power MOSFET uses advanced SiC MOSFET technology with low on-resistance, low switching losses, and a high operation temperature of 175°C. It is suitable for use in high frequency circuits and provides a reduction in overall system size, increased efficiency and increased switching frequency. It has been widely used in applications including solar inverters, uninterrupted power supplies, switch mode power supplies, and motor drives. Using RoHS compliant components and being AEC-Q101 qualified, it is qualified for use in automotive application.

Product Specifications

Device	V _{DS}	I _D (25°C)	R _{(DS)on}	Marking
AMS1200020M2	1200V	88A	20mΩ	MS1200020M2

CONTENTS

Features.....	1
Benefits.....	1
Potential Applications.....	1
Description.....	1
Product Specifications	1
Table 1. Maximum Ratings	3
Table 2. Thermal Resistances	3
Table 3. Static Electrical Characteristics	4
Table 4. Dynamic Electrical Characteristics.....	4
Table 5. Switching Characteristics	5
Table 6. Reverse SiC Diode Characteristics.....	6
Electrical Characteristic Diagrams.....	7
Package Information.....	13
Recommended Solder Pad Layout.....	14
Ordering Information	14
Important Notices – Read Carefully.....	15
Warning.....	15

Table 1. Maximum Ratings**(T_C = 25°C, unless otherwise specified)**

Parameter	Symbol	Value	Unit	Test conditions
Drain-source voltage	V _{DSmax}	1200	V	V _{GS} = 0V, I _D = 100μA
Gate-source voltage, max. transient voltage	V _{GSmax}	-11/+25	V	t _p ≤ 0.5us, D < 1%
Gate-source voltage, max. static voltage	V _{GSmax}	-10/+22	V	
Gate-source voltage	V _{GSop}	-5/+18	V	Recommended operation values
Continuous drain current	I _D	88	A	V _{GS} = 18V
		62		V _{GS} = 18V, T _C = 100°C
Pulsed drain current	I _{D(pulse)}	220	A	Pulse width t _p limited by T _{jmax}
Power dissipation	P _{tot}	403	W	
Operating junction temperature	T _j	-55~175	°C	
Storage temperature	T _{stg}	-55~175	°C	
Soldering temperature	T _L	260	°C	1.6mm from case for 10s
Mounting torque	M	0.7	Nm	M3 screw

Table 2. Thermal Resistances

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R _{th(j-c)}	/	0.31	/	°C/W	
Thermal resistance from junction to ambient	R _{th(j-a)}	/	/	40	°C/W	

Table 3. Static Electrical Characteristics(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Drain-source breakdown voltage	V _{(BR)DSS}	1200	/	/	V	V _{GS} = 0V, I _D = 100μA
Gate threshold voltage	V _{GS(th)}	1.8	3.2	4.2		V _{DS} = V _{GS} , I _D = 18mA
		/	2.4	/		V _{DS} = V _{GS} , I _D = 18mA, T _j = 175°C
Drain-source leakage current	I _{DSS}	/	1	150	μA	V _{DS} = 1200V, V _{GS} = 0V
Gate-source leakage current	I _{GSS}	/	1	250	nA	V _{GS} = 18V, V _{DS} = 0V
Drain-source on-state resistance	R _{DS(on)}	/	23	/	mΩ	V _{GS} = 15V, I _D = 40A
		/	20	26		V _{GS} = 18V, I _D = 40A
		/	32	/		V _{GS} = 18V, I _D = 40A, T _j = 175°C
Transconductance	g _{fs}	/	26	/	S	V _{DS} = 20V, I _D = 40A
		/	27	/		V _{DS} = 20V, I _D = 40A, T _j = 175°C
Internal gate resistance	R _{g(int)}	/	2.9	/	Ω	f = 1MHz, V _{AC} = 25mV

Table 4. Dynamic Electrical Characteristics(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Input capacitance	C _{iss}	/	3533	/	pF	V _{GS} = 0V, V _{DS} = 1000V, f = 100kHz, V _{AC} = 25mV
Output capacitance	C _{oss}	/	147	/		
Reverse transfer capacitance	C _{rss}	/	9	/		
C _{oss} stored energy	E _{oss}	/	84	/	μJ	
Gate to source charge	Q _{GS}	/	50	/	nC	V _{DD} = 800V, V _{GS} = -5/+18V, I _D = 40A, I _{GS} = 1mA
Gate to drain charge	Q _{GD}	/	40	/		
Total gate charge	Q _G	/	154	/		

Table 5. Switching Characteristics**(T_j = 25°C, unless otherwise specified)**

Parameter	Symbol	Values			Unit	Test conditions	
		Min.	Typ.	Max.			
Turn-on delay time	t _{d(on)}	/	18	/	ns	V _{DD} = 800V, V _{GS} = -5/+18V, I _D = 40A, R _{G(ext)} = 2.4Ω, L = 110μH	
Rise time	t _r	/	22	/			
Turn-off delay time	t _{d(off)}	/	41	/			
Fall time	t _f	/	12	/			
Turn-on switching energy	E _{on}	/	532	/	μJ		
Turn-off switching energy	E _{off}	/	46	/			
Turn-on delay time	t _{d(on)}	/	16	/	ns		V _{DD} = 800V, V _{GS} = -5/+18V, I _D = 40A, R _{G(ext)} = 2.4Ω, L = 110μH, T _j = 175°C
Rise time	t _r	/	26	/			
Turn-off delay time	t _{d(off)}	/	52	/			
Fall time	t _f	/	13	/			
Turn-on switching energy	E _{on}	/	749	/	μJ		
Turn-off switching energy	E _{off}	/	53	/			

Table 6. Reverse SiC Diode Characteristics(T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Diode forward voltage	V _{SD}	/	4.1	/	V	V _{GS} = -5V, I _{SD} = 40A
		/	3.6	/		V _{GS} = -5V, I _{SD} = 40A, T _j = 175°C
Continuous diode forward current	I _S	/	/	88	A	T _C = 25°C
Diode pulse current	I _{S, pulse}	/	/	220	A	V _{GS} = -5V, pulse width t _p limited by T _{jmax}
Reverse recovery time	t _{rr}	/	15	/	ns	V _{GS} = -5V, I _{SD} = 40A, V _R = 800V, di _f /dt = 2.98kA/μs
Reverse recovery charge	Q _{rr}	/	0.37	/	μC	
Peak reverse recovery current	I _{rrm}	/	42	/	A	
Reverse recovery time	t _{rr}	/	26	/	ns	V _{GS} = -5V, I _{SD} = 40A, V _R = 800V, T _j = 175°C, di _f /dt = 3.12kA/μs
Reverse recovery charge	Q _{rr}	/	1.02	/	μC	
Peak reverse recovery current	I _{rrm}	/	63	/	A	

Electrical Characteristic Diagrams

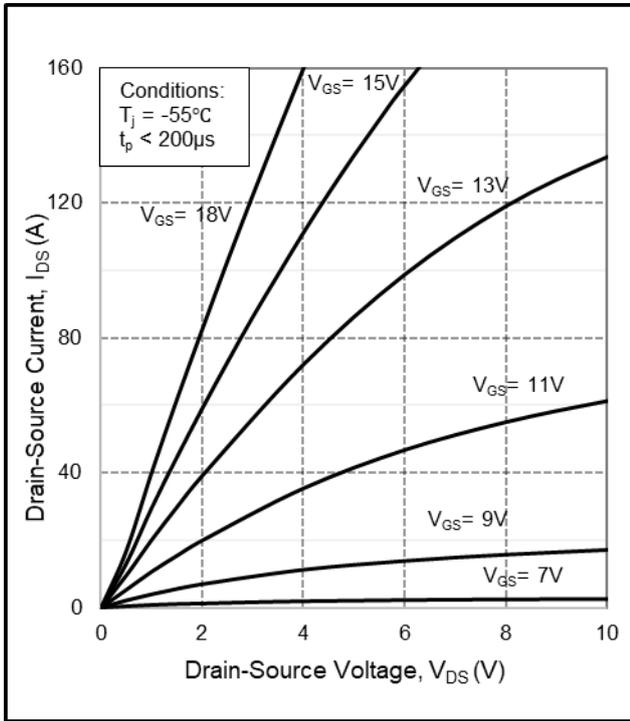


Figure 1. Output characteristics at $T_j = -55^\circ\text{C}$

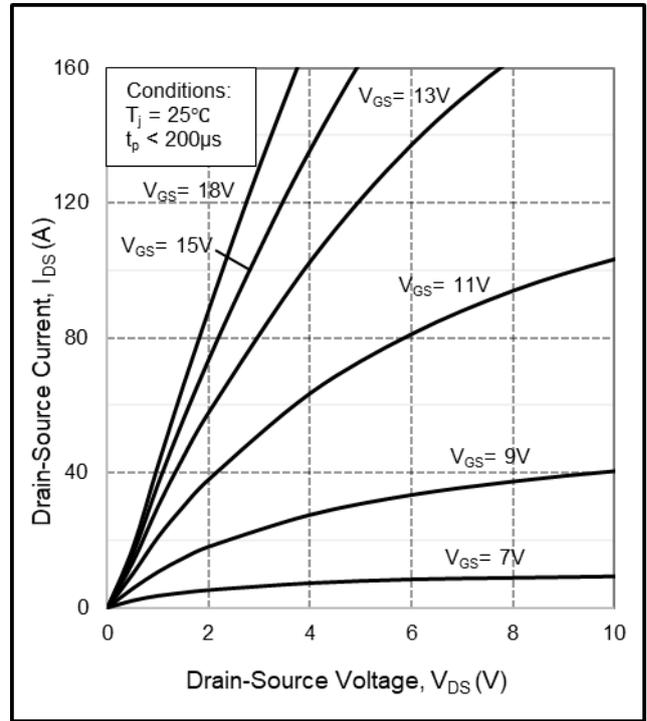


Figure 2. Output characteristics at $T_j = 25^\circ\text{C}$

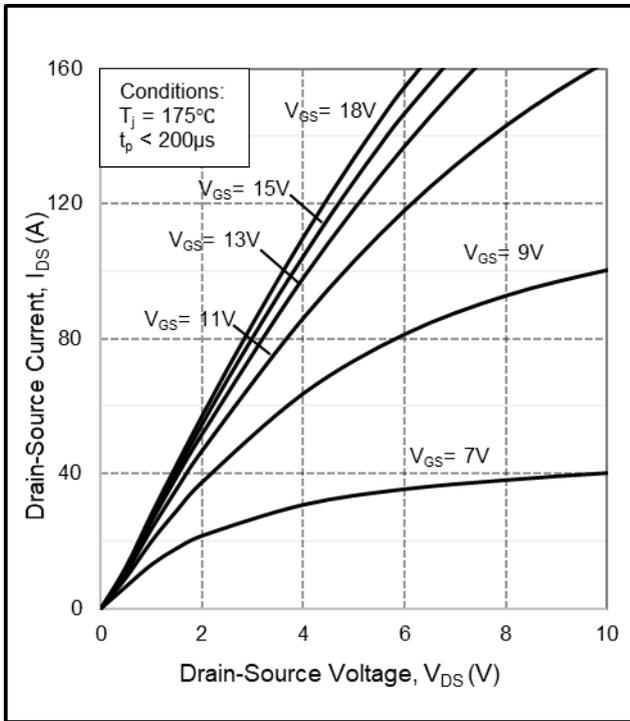


Figure 3. Output characteristics at $T_j = 175^\circ\text{C}$

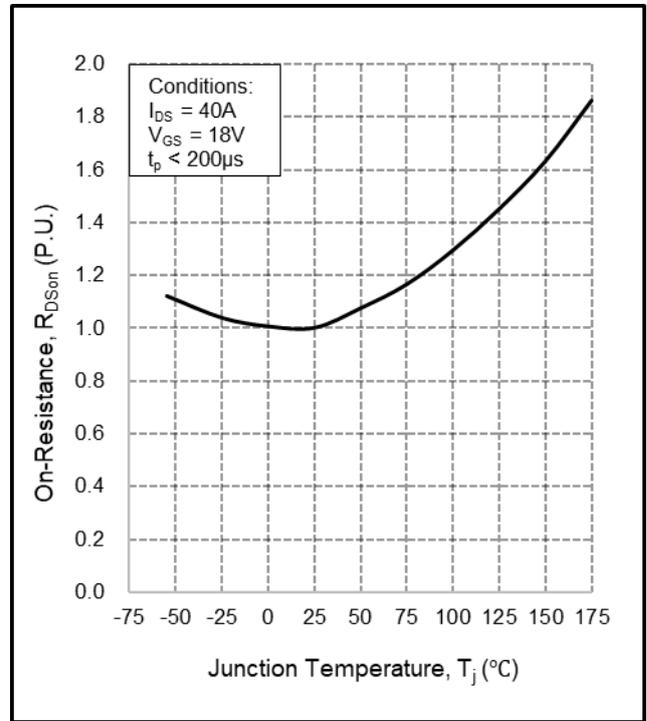


Figure 4. Normalized on-resistance vs. temperature

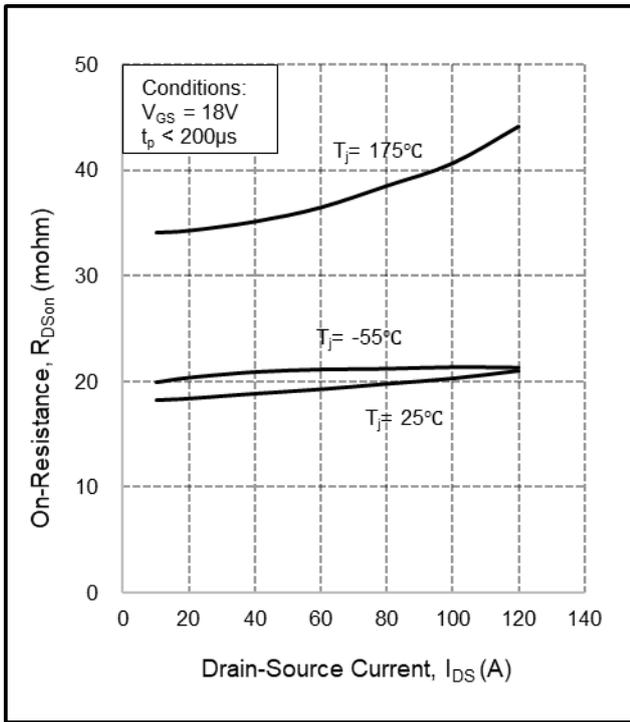


Figure 5. On-resistance vs. drain current for various temperatures

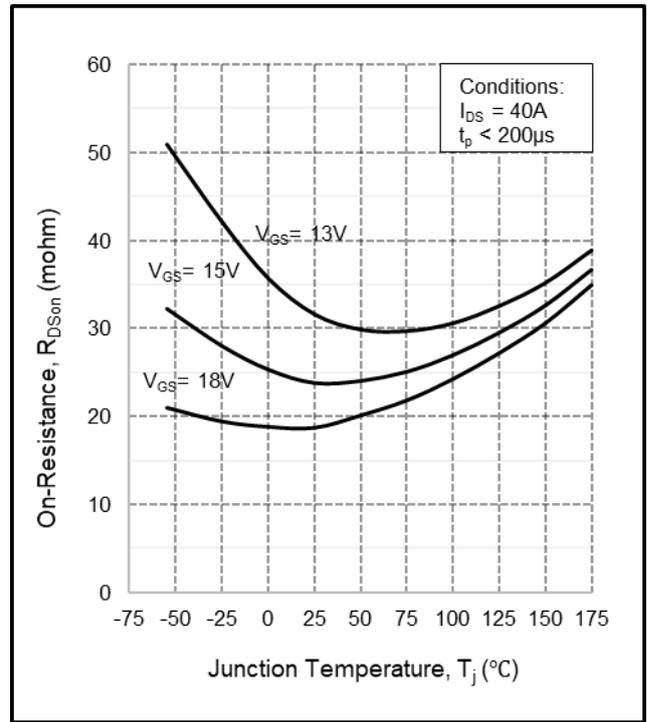


Figure 6. On-resistance vs. temperature for various gate voltages

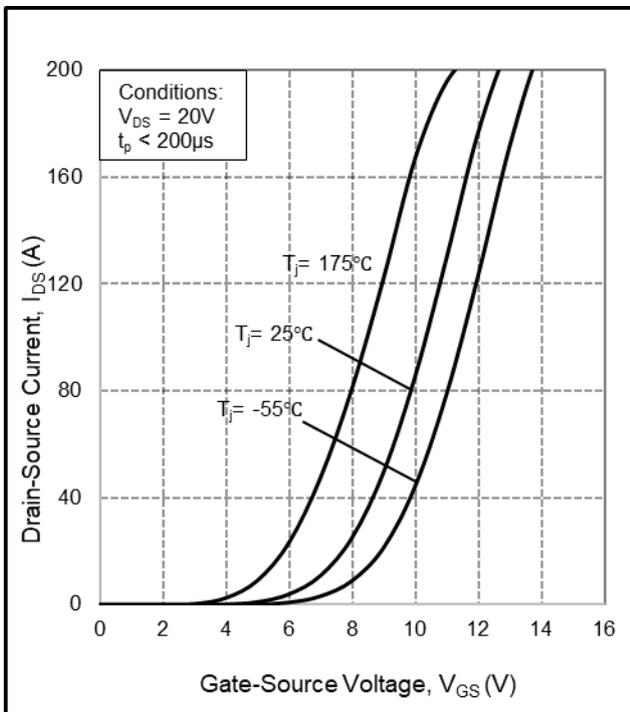


Figure 7. Transfer characteristic for various junction temperatures

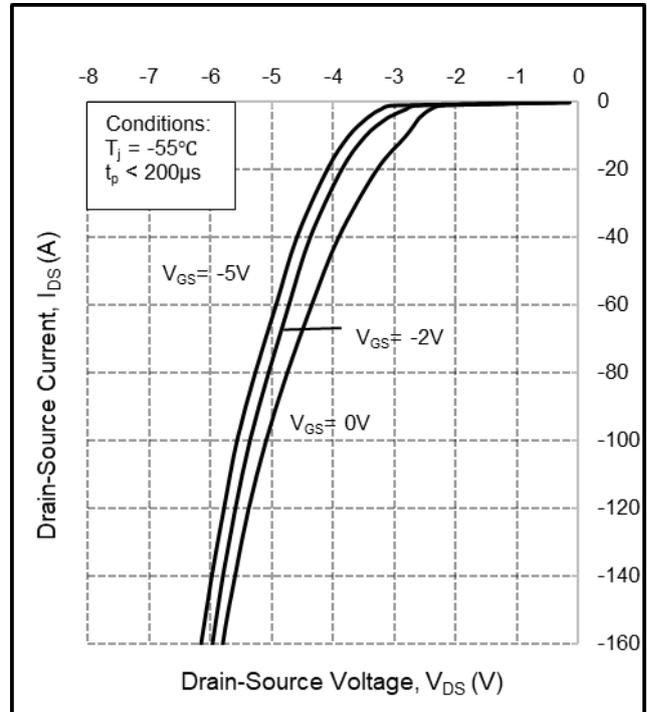


Figure 8. Body diode characteristic at $T_j = -55^\circ\text{C}$

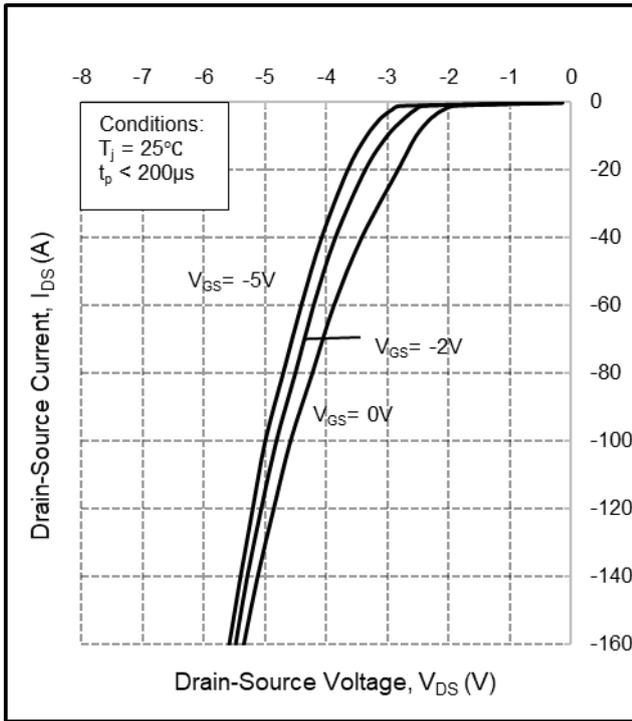


Figure 9. Body diode characteristic at $T_j = 25^\circ\text{C}$

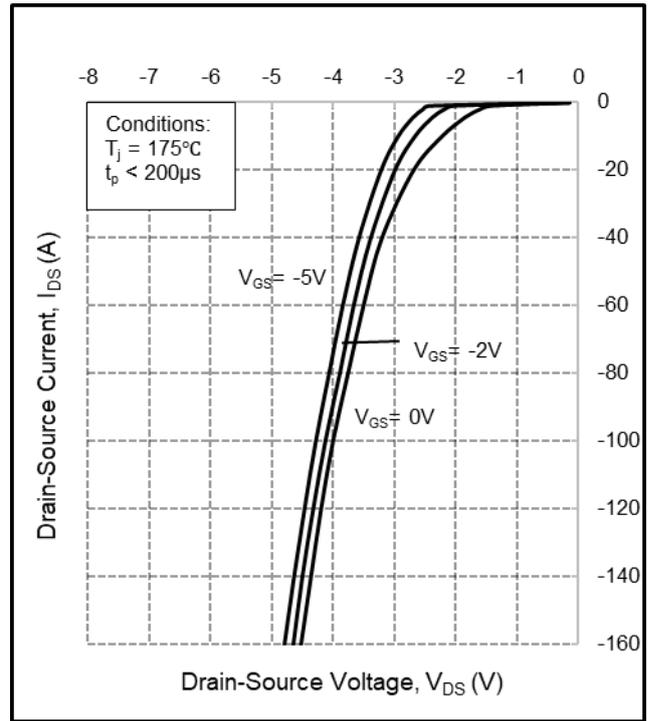


Figure 10. Body diode characteristic at $T_j = 175^\circ\text{C}$

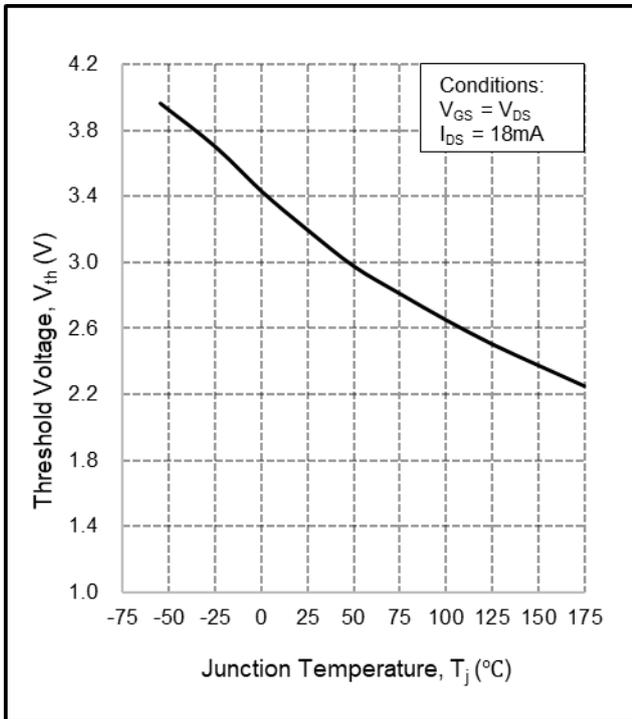


Figure 11. Threshold voltage vs. temperature

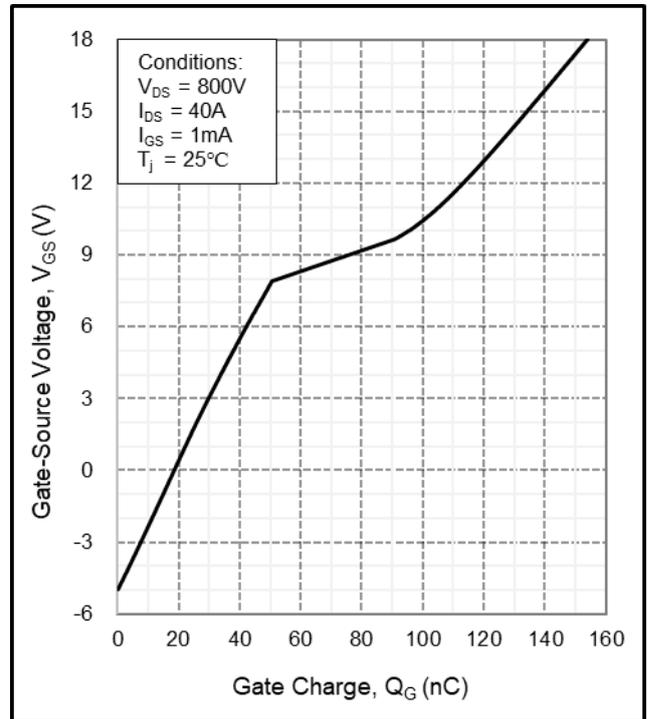


Figure 12. Gate charge characteristics

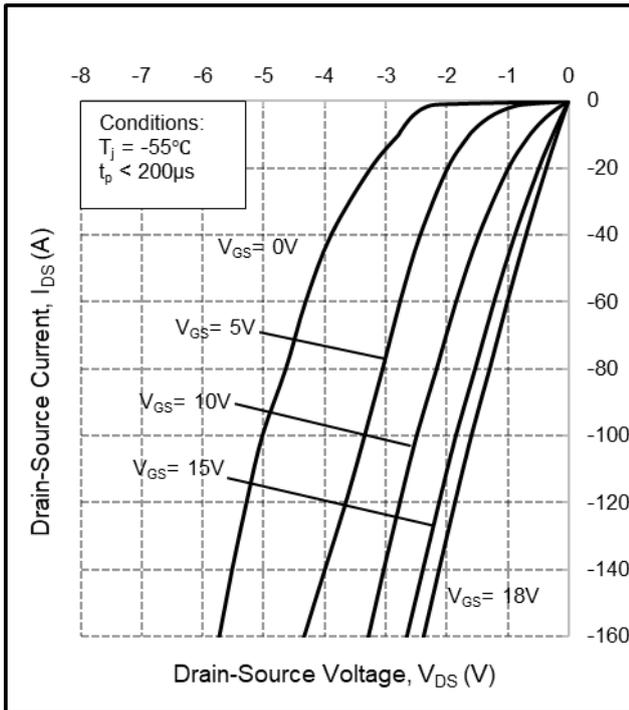


Figure 13. 3rd quadrant characteristic at $T_j = -55^\circ\text{C}$

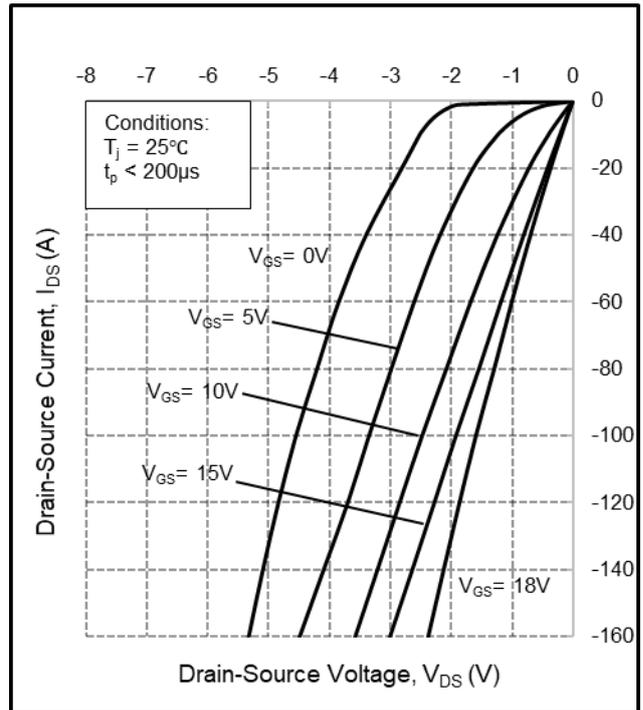


Figure 14. 3rd quadrant characteristic at $T_j = 25^\circ\text{C}$

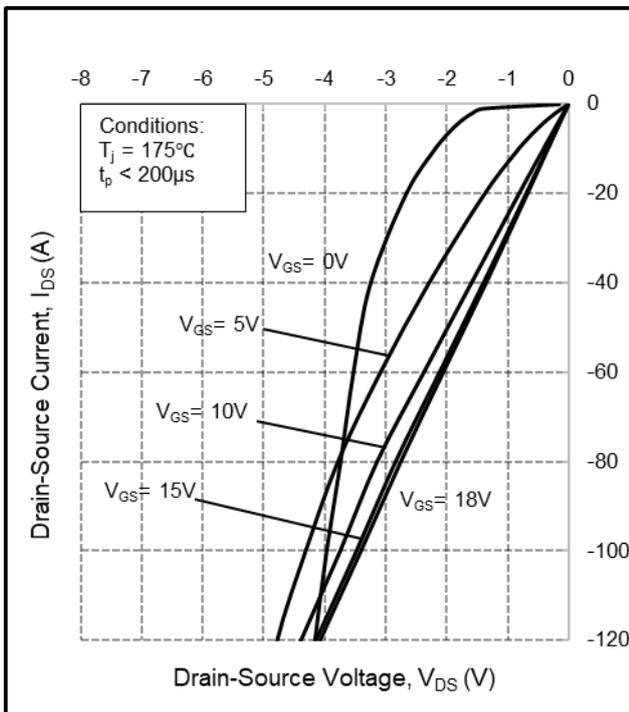


Figure 15. 3rd quadrant characteristic at $T_j = 175^\circ\text{C}$

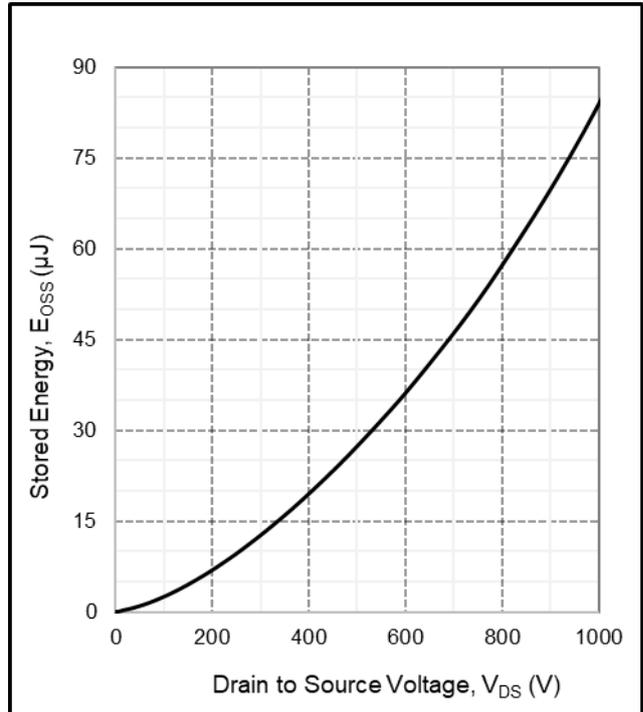


Figure 16. Output capacitor stored energy

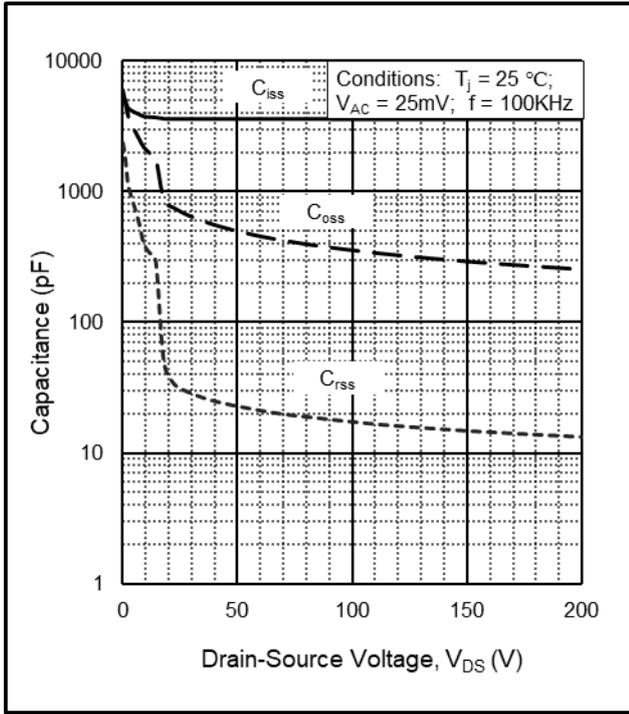


Figure 17. Capacitance vs. drain-source voltage (0 - 200V)

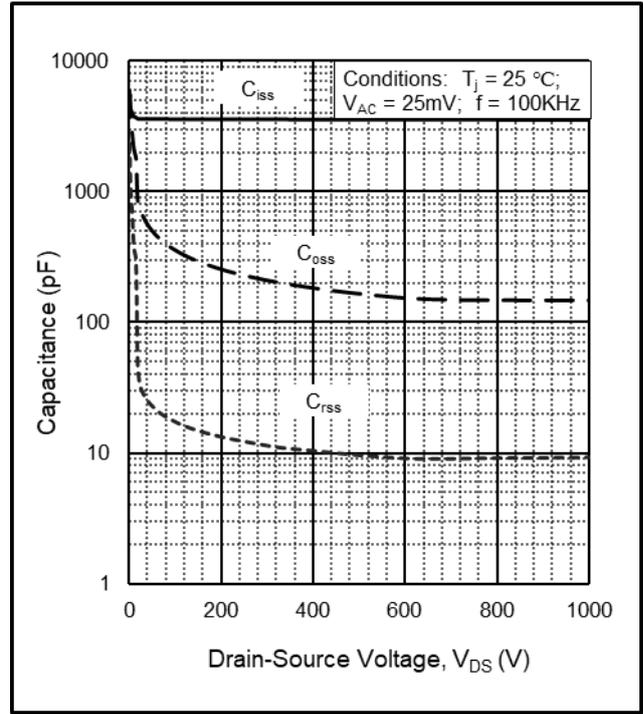


Figure 18. Capacitance vs. drain-source voltage (0 - 1000V)

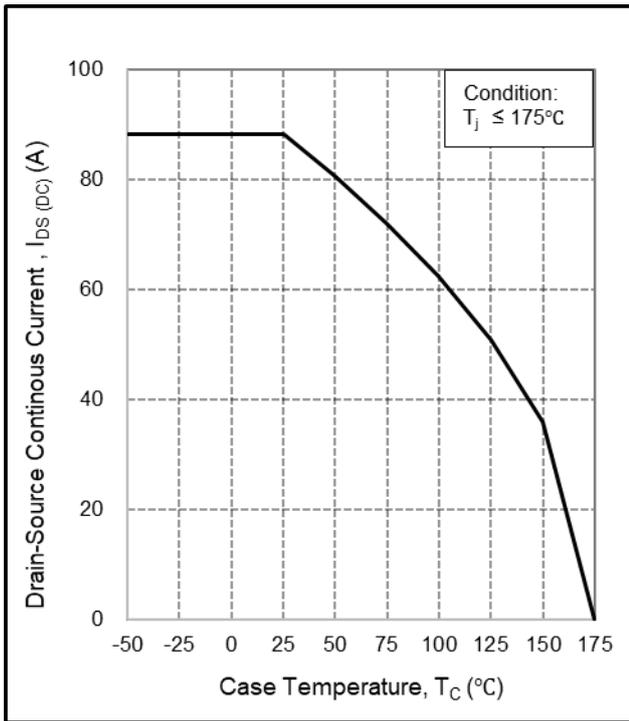


Figure 19. Continuous drain current derating vs. temperature

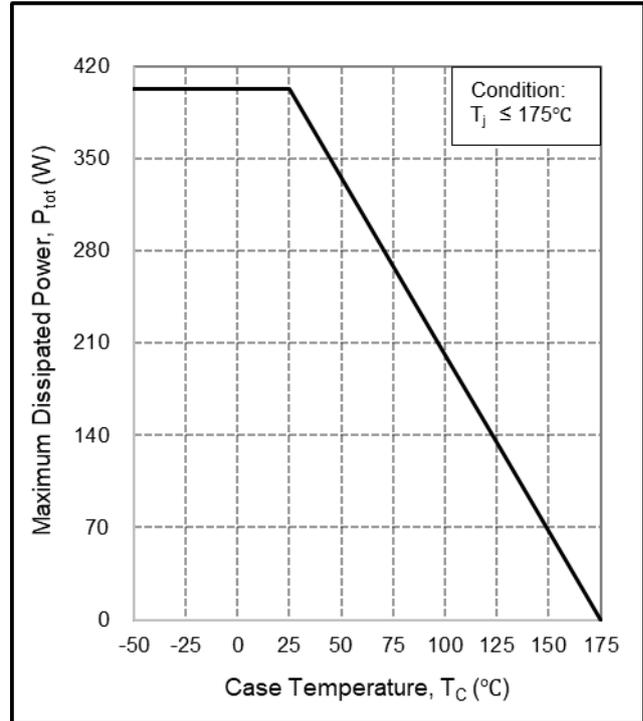


Figure 20. Maximum power dissipation derating vs. temperature

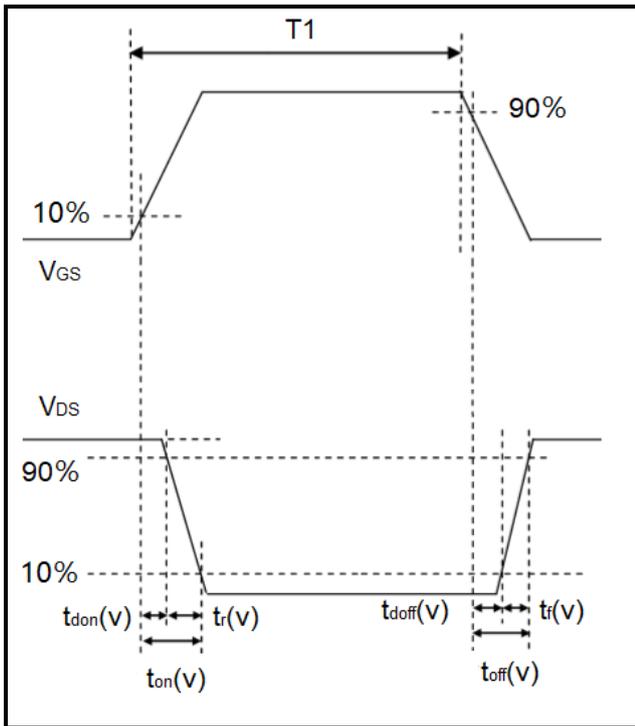


Figure 21. Switching times definition

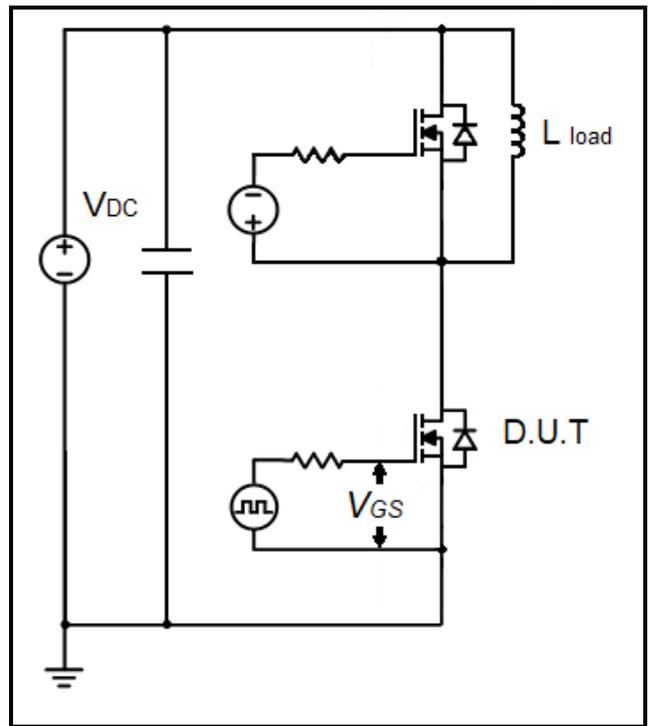
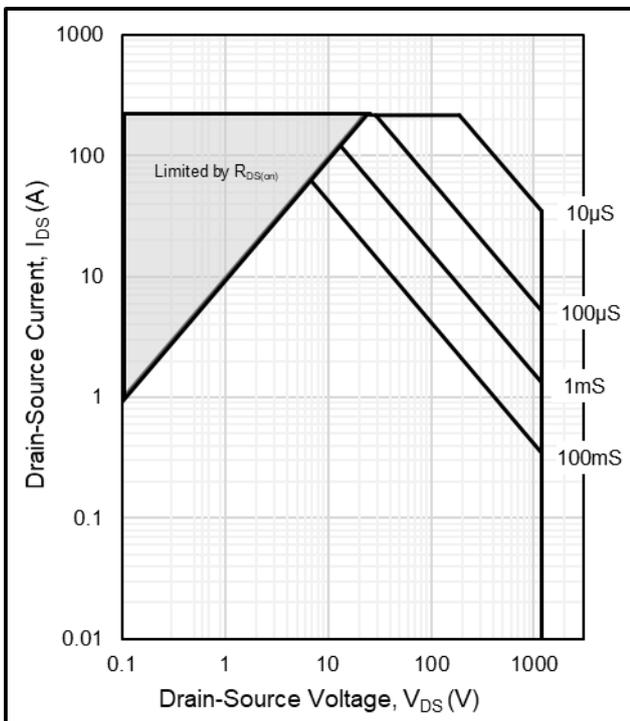
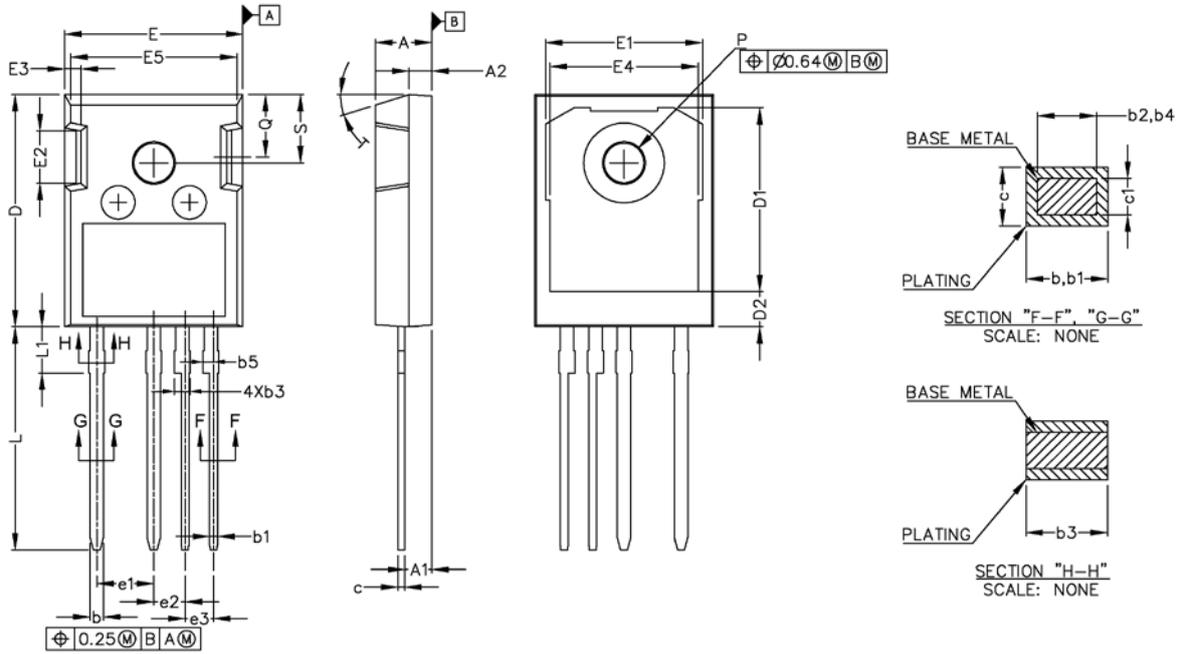


Figure 22. Clamped inductive switching waveform test circuit

Figure 23. Safe Operating Area



Package Information

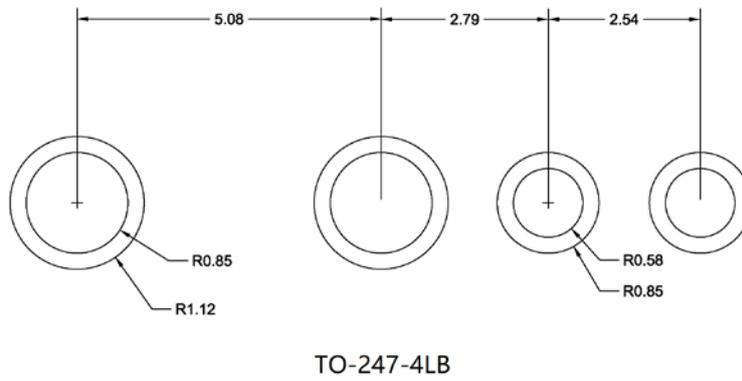


Dimension unit: [mm]		
Symbol	Min	Max
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b	1.10	1.30
b1	0.65	0.79
b2	1.10	1.25
b3	1.34	1.44
b4	0.65	0.74
b5	0.74	1.14
c	0.55	0.68
c1	0.55	0.65
D	20.80	21.10
D1	16.25	17.65
D2	2.95	3.35
E	15.75	16.13
E1	13.10	14.15
E2	4.32	5.10
E3	1.00	1.90
E4	12.38	13.43
E5	14.65	15.05

Dimension unit: [mm]		
Symbol	Min	Max
e1	5.08 BSC	
e2	2.79 BSC	
e3	2.54 BSC	
L	19.72	20.32
L1	3.97	4.37
øP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5° REF.	

Recommended Solder Pad Layout

Note: All dimensions are in mm



Ordering Information

Part number	AMS1200020M2-ASATH
Package	TO-247-4LB
Unit quantity	450 EA
Packing type	Tube

Important Notices – Read Carefully

Before you use the products that made in Sanan, you are requested to carefully read this document and fully understand its contents. Sanan Semiconductor Co., Ltd shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of products that made in Sanan.

Sanan Semiconductor Datasheets are subject to change. Information presented in this document is from the characterization of engineering lots. Sanan Semiconductor Co., Ltd reserves the right to change limits, test conditions, and dimensions without notice. Information contained in this document are typical values and shall in no event be regarded as a guarantee of characteristics. With respect to any information regarding the application of the product, Sanan hereby disclaims all warranties and liabilities of any kind. The information in this document is exclusively for trained technical staff. It is the responsibility of the customer's technical department to decide the suitability of the product in the customer's application and Sanan Semiconductor Co., Ltd assumes no responsibility or liability whatsoever for the use of the information contained in this document.

In case there is any clause in this document or in any other documents which is contradictory to this clause, this clause shall prevail. This clause shall survive after termination of this document.

Warning

Due to technical requirements, the products that made in Sanan may contain dangerous substances. For detailed information about the substance(s), please contact the Sanan office. Sanan Semiconductor Co., Ltd bears no responsibility for any damage whatsoever due to the substance(s) used in products that made in Sanan.