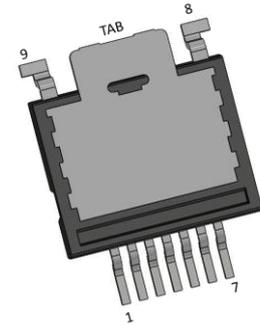


# 1200V 32mΩ Silicon Carbide Power MOSFET

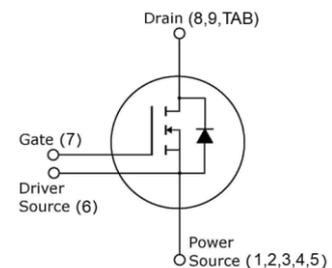
## Features

- AEC-Q101 qualified
- High blocking voltage with low on-resistance
- High switching speed with low capacitance
- Very fast and robust intrinsic body diode with low reverse recovery
- Very low switching losses
- Excellent avalanche ruggedness
- RoHS compliant



## Benefits

- Greater system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency
- Easy to parallel and simple to drive



## Potential Applications

- Solar inverters
- Uninterrupted power supplies
- Switch mode power supplies
- Motor drives

Package Type: X2PAK



## Description

The Sanan Semiconductor 1200V/32mΩ silicon carbide power MOSFET uses advanced SiC MOSFET technology with low on-resistance, low switching losses, and a high operation temperature of 175°C. It is suitable for use in high frequency circuits and provides a reduction in overall system size, increased efficiency and increased switching frequency. It has been widely used in applications including solar inverters, uninterrupted power supplies, switch mode power supplies, and motor drives. Using RoHS compliant components and being AEC-Q101 qualified, it is qualified for use in automotive application.

## Product Specifications

Device	V <sub>DS</sub>	I <sub>D</sub> (25°C)	R <sub>(DS)on</sub>	Marking
AMS1200032VA2	1200V	57A	32mΩ	MS1200032VA2

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**Table 1. Maximum Ratings**(T<sub>C</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Drain-source voltage	V <sub>DSmax</sub>	1200	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100μA
Gate-source voltage, max. transient voltage	V <sub>GSmax</sub>	-11/+25		t <sub>p</sub> ≤ 0.5us, D < 1%
Gate-source voltage, max. static voltage	V <sub>GSmax</sub>	-10/+22		
Gate-source voltage	V <sub>GSop</sub>	-5/+18		Recommended operation values,
Continuous drain current	I <sub>D</sub>	57	A	V <sub>GS</sub> = 18V
		41		V <sub>GS</sub> = 18V, T <sub>C</sub> = 100°C
Pulsed drain current	I <sub>D(pulse)</sub>	143	A	Pulse width t <sub>p</sub> limited by T <sub>jmax</sub>
Power dissipation	P <sub>tot</sub>	278	W	
Operating junction temperature	T <sub>j</sub>	-55~175	°C	
Storage temperature	T <sub>stg</sub>	-55~175	°C	
Soldering temperature	T <sub>L</sub>	260	°C	1.6mm from case for 10s

**Table 2. Thermal Resistances**

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R <sub>th(j-c)</sub>	/	0.45	/	°C/W	

**Table 3. Static Electrical Characteristics**(T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	1200	/	/	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100μA
Gate threshold voltage	V <sub>GS(th)</sub>	1.8	3.2	4.2		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 10mA
		/	2.4	/		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 10mA, T <sub>j</sub> = 175 °C
Drain-source leakage current	I <sub>DSS</sub>	/	1	150	μA	V <sub>DS</sub> = 1200V, V <sub>GS</sub> = 0V
Gate-source leakage current	I <sub>GSS</sub>	/	1	250	nA	V <sub>GS</sub> = 18V, V <sub>DS</sub> = 0V
Drain-source on-state resistance	R <sub>DS(on)</sub>	/	37	/	mΩ	V <sub>GS</sub> = 15V, I <sub>D</sub> = 40A
		/	32	42		V <sub>GS</sub> = 18V, I <sub>D</sub> = 40A
		/	56	/		V <sub>GS</sub> = 18V, I <sub>D</sub> = 40A, T <sub>j</sub> = 175 °C
Transconductance	g <sub>fs</sub>	/	24	/	S	V <sub>DS</sub> = 20V, I <sub>D</sub> = 40A
		/	20	/		V <sub>DS</sub> = 20V, I <sub>D</sub> = 40A, T <sub>j</sub> = 175 °C
Internal gate resistance	R <sub>g(int)</sub>	/	3.6	/	Ω	f = 1MHz, V <sub>AC</sub> = 25mV

**Table 4. Dynamic Electrical Characteristics**(T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Input capacitance	C <sub>iss</sub>	/	2090	/	pF	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 1000V, f = 100KHz, V <sub>AC</sub> = 25mV
Output capacitance	C <sub>oss</sub>	/	86	/		
Reverse transfer capacitance	C <sub>rss</sub>	/	5	/		
C <sub>oss</sub> stored energy	E <sub>oss</sub>	/	50	/	μJ	
Gate to source charge	Q <sub>GS</sub>	/	30	/	nC	V <sub>DD</sub> = 800V, V <sub>GS</sub> = -5/+18V, I <sub>D</sub> = 40A, I <sub>GS</sub> = 1mA
Gate to drain charge	Q <sub>GD</sub>	/	27	/		
Total gate charge	Q <sub>G</sub>	/	91	/		

**Table 5. Switching Characteristics**(T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions	
		Min.	Typ.	Max.			
Turn-on delay time	t <sub>d(on)</sub>	/	13	/	ns	V <sub>DD</sub> = 800V, V <sub>GS</sub> = -5/+18V, I <sub>D</sub> = 40A, R <sub>G(ext)</sub> = 2.4Ω, L = 100μH	
Rise time	t <sub>r</sub>	/	15	/			
Turn-off delay time	t <sub>d(off)</sub>	/	29	/			
Fall time	t <sub>f</sub>	/	8	/			
Turn-on switching energy	E <sub>on</sub>	/	286	/	μJ		
Turn-off switching energy	E <sub>off</sub>	/	70	/			
Turn-on delay time	t <sub>d(on)</sub>	/	11	/	ns		V <sub>DD</sub> = 800V, V <sub>GS</sub> = -5/+18V, I <sub>D</sub> = 40A, R <sub>G(ext)</sub> = 2.4Ω, L = 100μH, T <sub>j</sub> = 175 °C
Rise time	t <sub>r</sub>	/	15	/			
Turn-off delay time	t <sub>d(off)</sub>	/	36	/			
Fall time	t <sub>f</sub>	/	9	/			
Turn-on switching energy	E <sub>on</sub>	/	274	/	μJ		
Turn-off switching energy	E <sub>off</sub>	/	79	/			

**Table 6. Reverse SiC Diode Characteristics**(T<sub>j</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Diode forward voltage	V <sub>SD</sub>	/	4.6	/	V	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 40A
		/	4.0	/		V <sub>GS</sub> = -5V, I <sub>SD</sub> = 40A, T <sub>j</sub> = 175 °C
Continuous diode forward current	I <sub>S</sub>	/	/	57	A	T <sub>C</sub> = 25°C
Diode pulse current	I <sub>S, pulse</sub>	/	/	143	A	V <sub>GS</sub> = -5V, pulse width t <sub>p</sub> limited by T <sub>jmax</sub>
Reverse recovery time	t <sub>rr</sub>	/	11	/	ns	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 40A, V <sub>R</sub> = 800V, di <sub>r</sub> /dt = 3.13kA/μs
Reverse recovery charge	Q <sub>rr</sub>	/	0.20	/	μC	
Peak reverse recovery current	I <sub>rrm</sub>	/	30	/	A	
Reverse recovery time	t <sub>rr</sub>	/	18	/	ns	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 40A, V <sub>R</sub> = 800V, T <sub>j</sub> = 175 °C, di <sub>r</sub> /dt = 3.06kA/μs
Reverse recovery charge	Q <sub>rr</sub>	/	0.55	/	μC	
Peak reverse recovery current	I <sub>rrm</sub>	/	49	/	A	

### Electrical Characteristic Diagrams

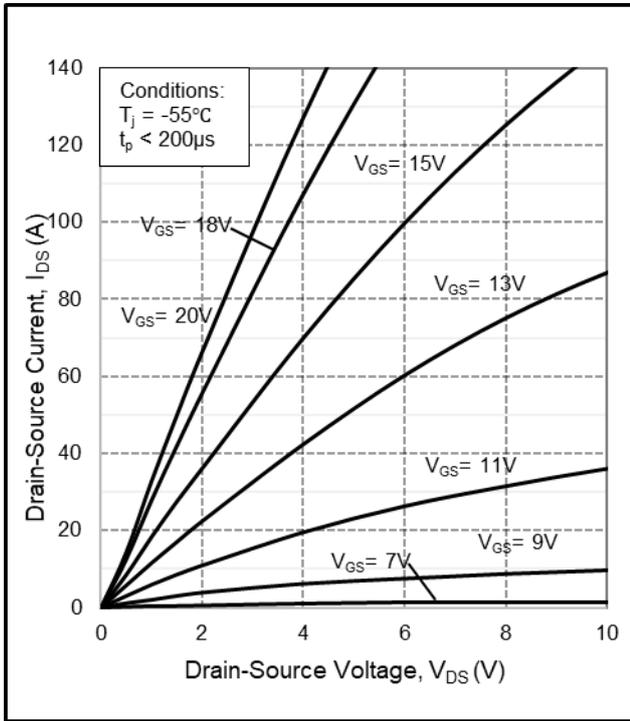


Figure 1. Output characteristics at  $T_j = -55^\circ\text{C}$

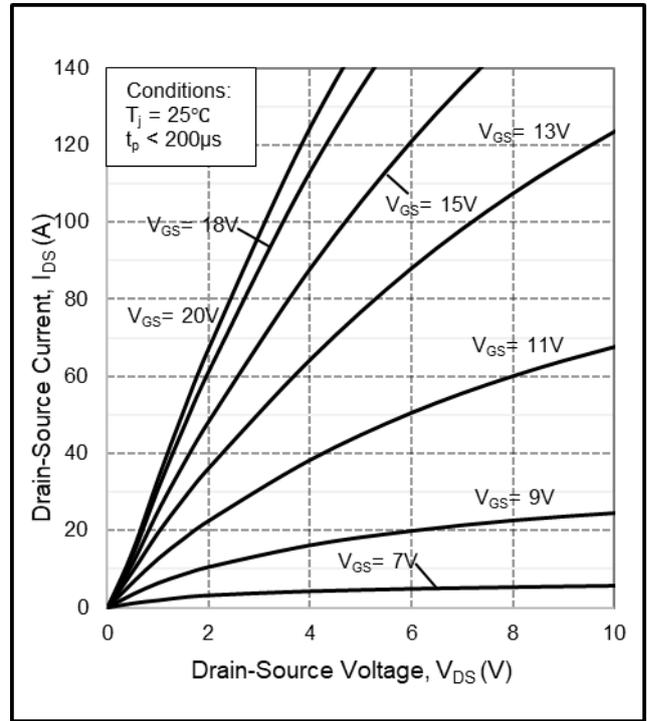


Figure 2. Output characteristics at  $T_j = 25^\circ\text{C}$

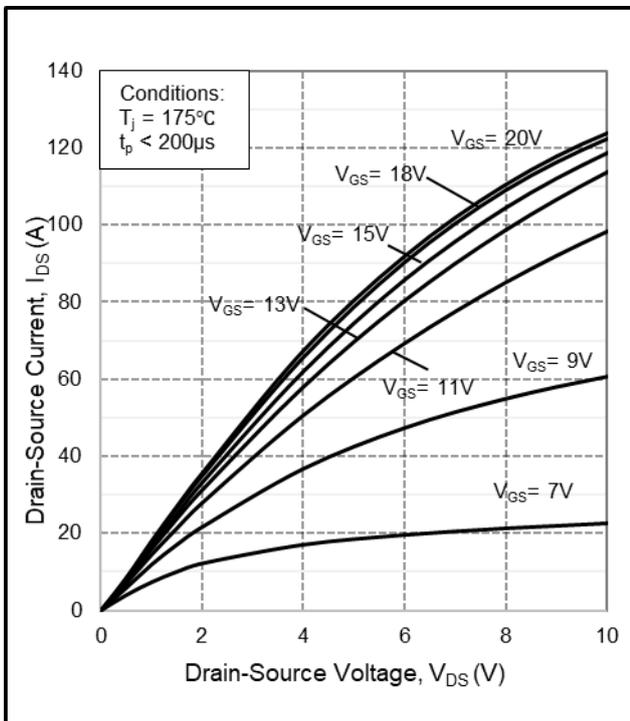


Figure 3. Output characteristics at  $T_j = 175^\circ\text{C}$

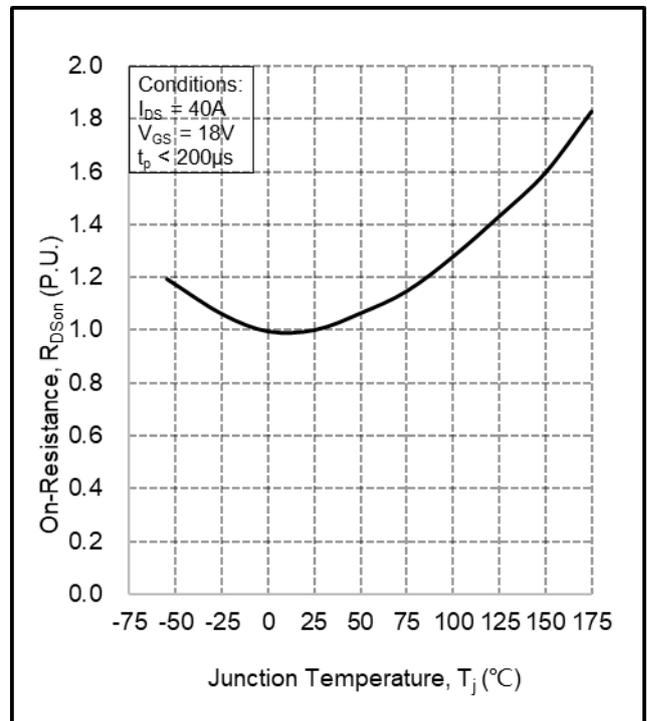


Figure 4. Normalized on-resistance vs. temperature

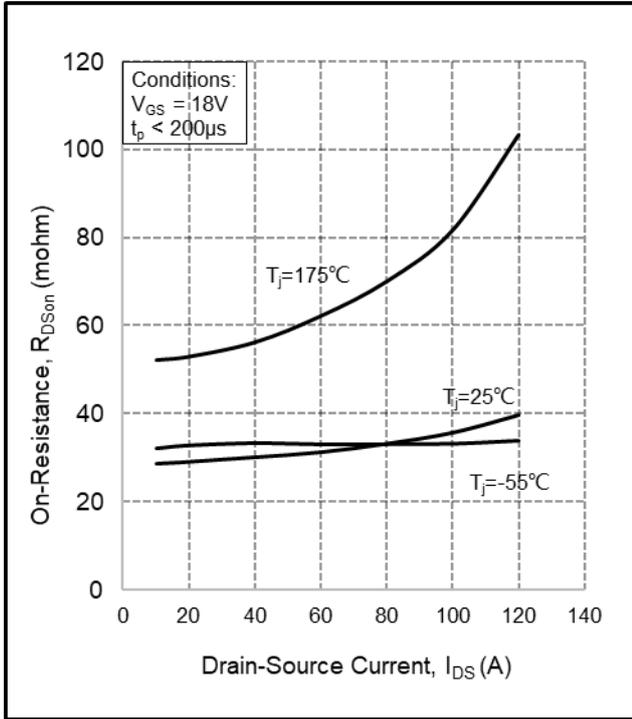


Figure 5. On-resistance vs. drain current for various temperatures

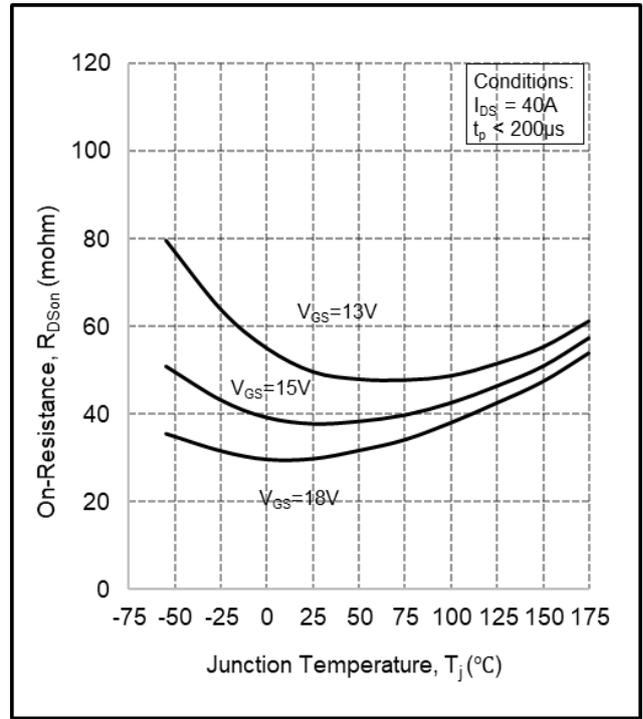


Figure 6. On-resistance vs. temperature for various gate voltages

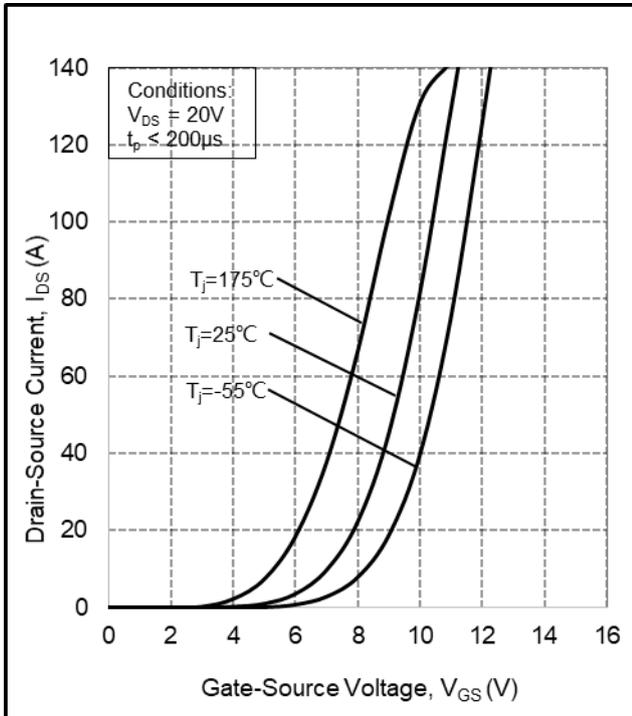


Figure 7. Transfer characteristic for various junction temperatures

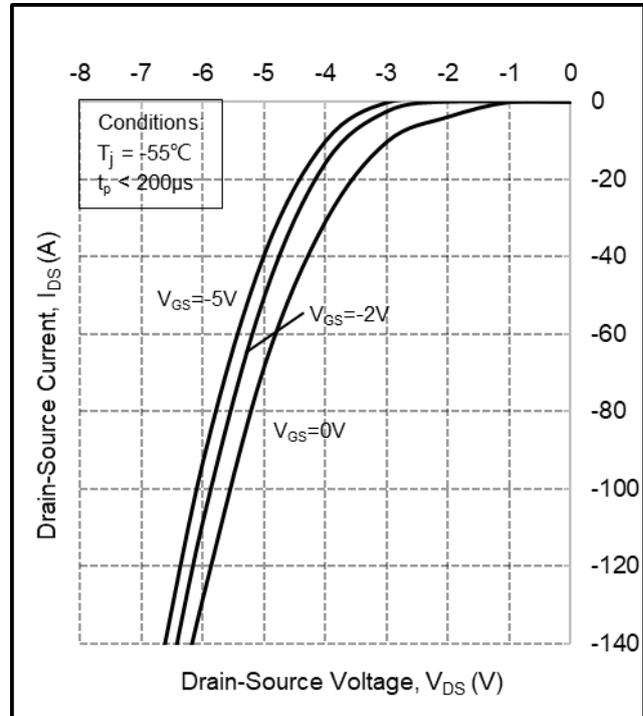


Figure 8. Body diode characteristic at  $T_j = -55^\circ\text{C}$

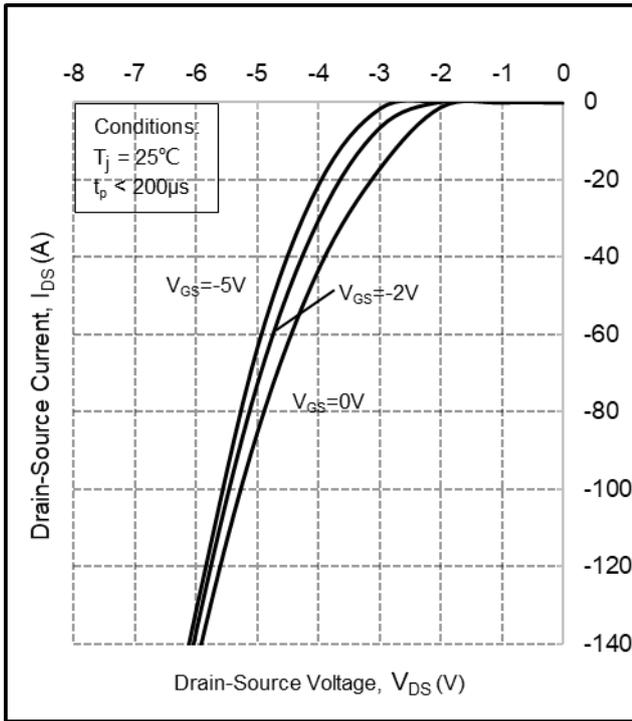


Figure 9. Body diode characteristic at  $T_j = 25^\circ\text{C}$

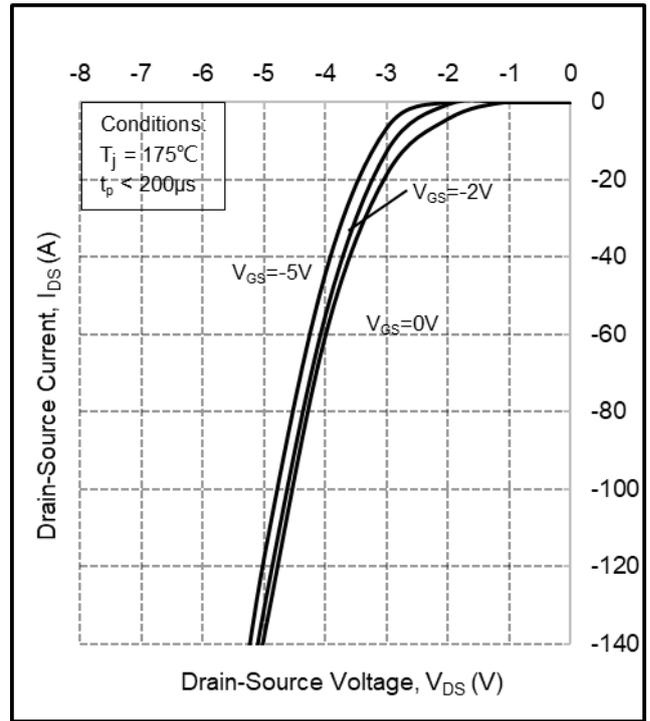


Figure 10. Body diode characteristic at  $T_j = 175^\circ\text{C}$

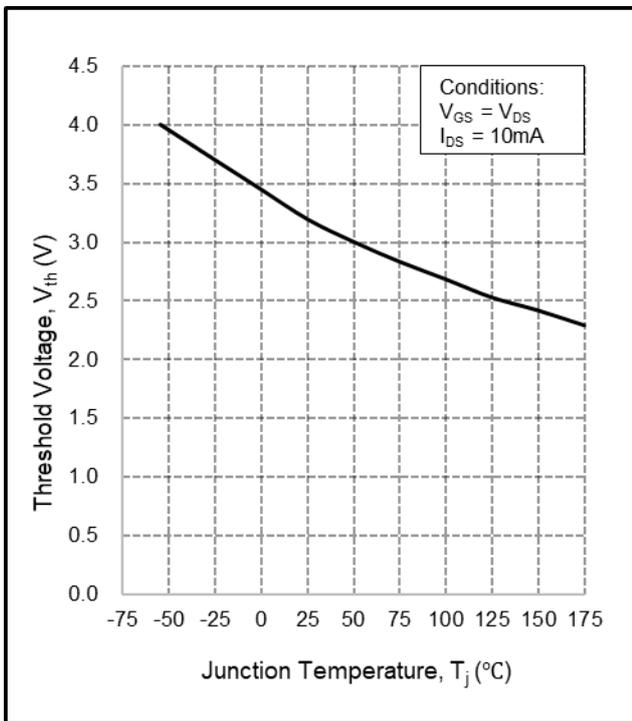


Figure 11. Threshold voltage vs. temperature

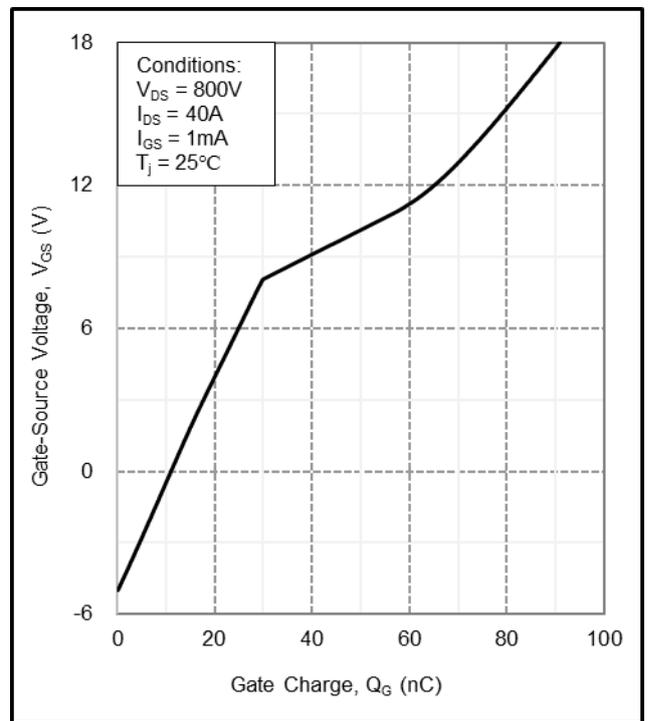


Figure 12. Gate charge characteristics

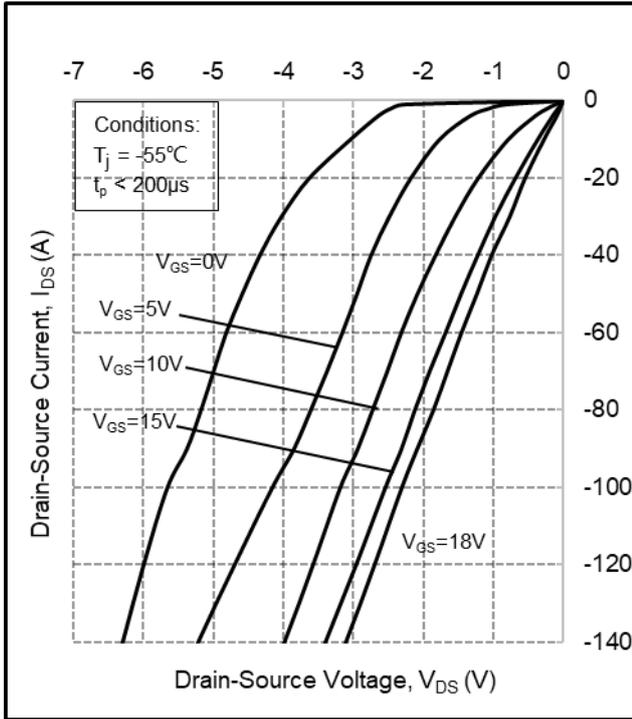


Figure 13. 3rd quadrant characteristic at  $T_j = -55^\circ\text{C}$

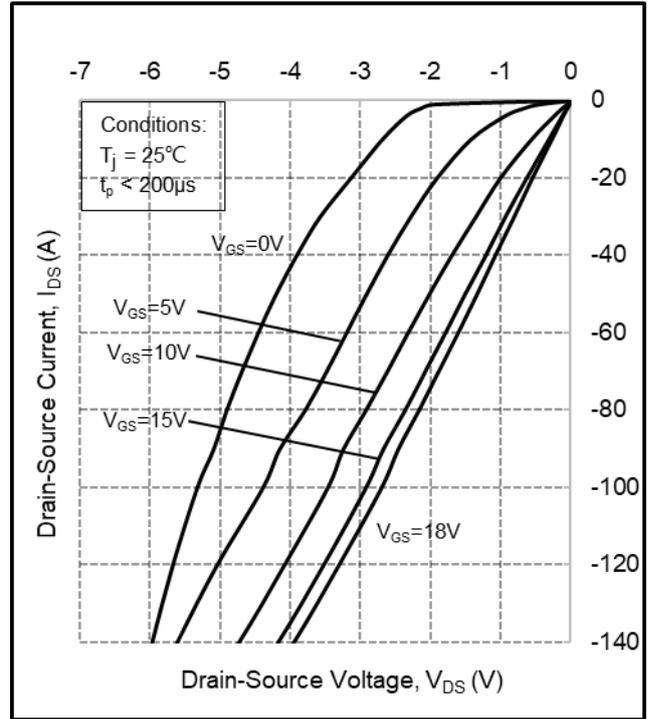


Figure 14. 3rd quadrant characteristic at  $T_j = 25^\circ\text{C}$

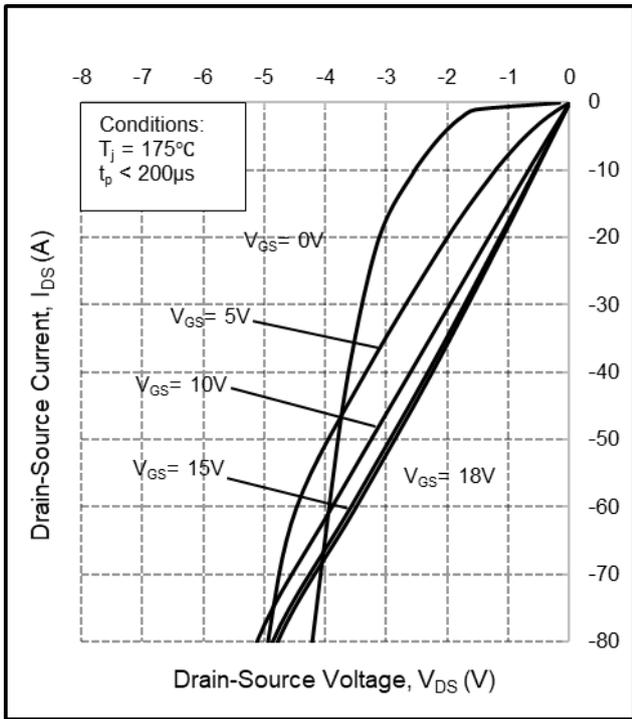


Figure 15. 3rd quadrant characteristic at  $T_j = 175^\circ\text{C}$

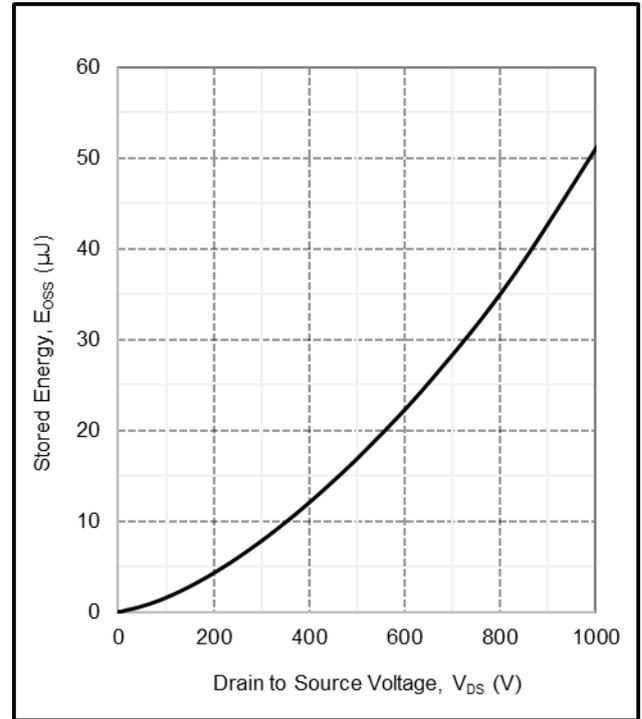


Figure 16. Output capacitor stored energy

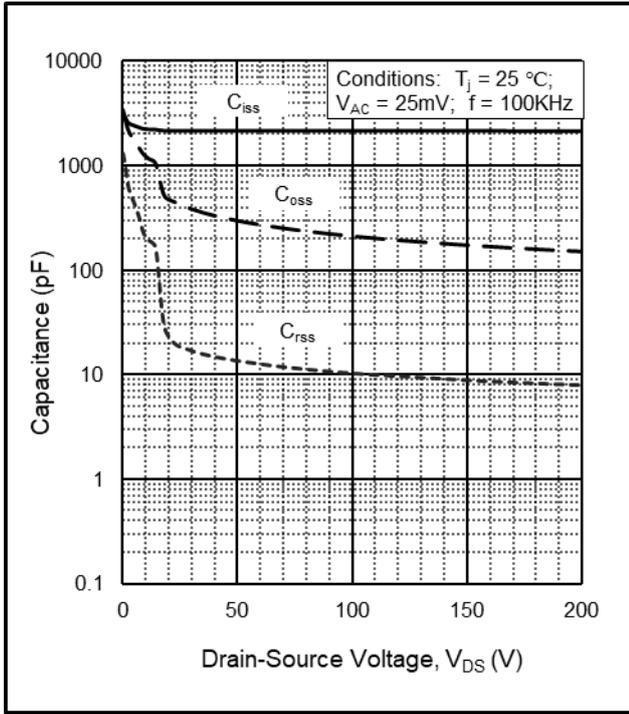


Figure 17. Capacitance vs. drain-source voltage (0 - 200V)

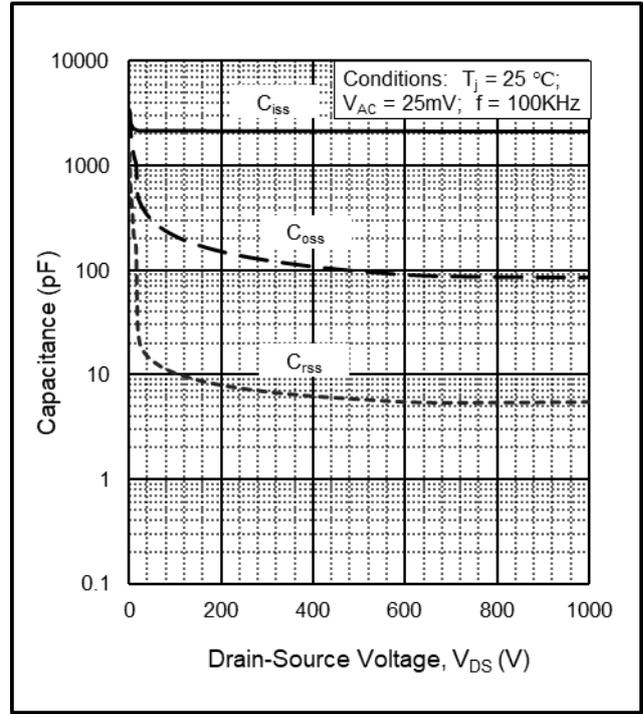


Figure 18. Capacitance vs. drain-source voltage (0 - 1000V)

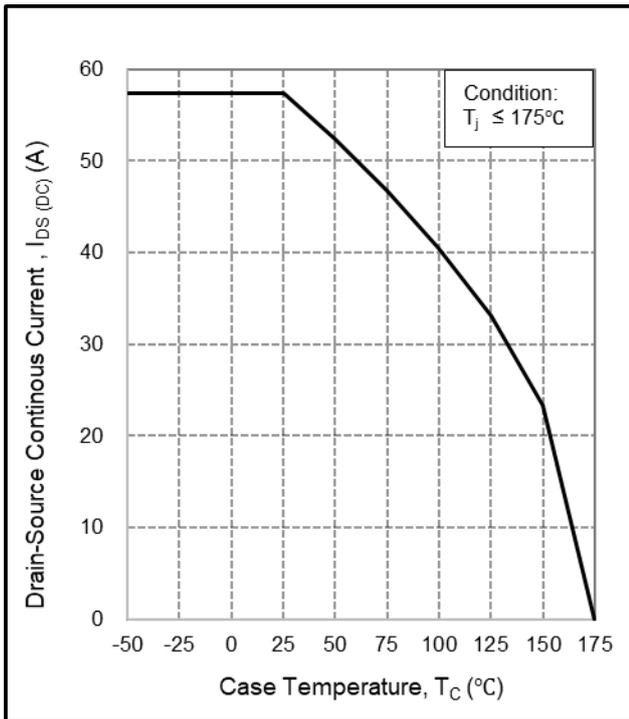


Figure 19. Continuous drain current derating vs. temperature

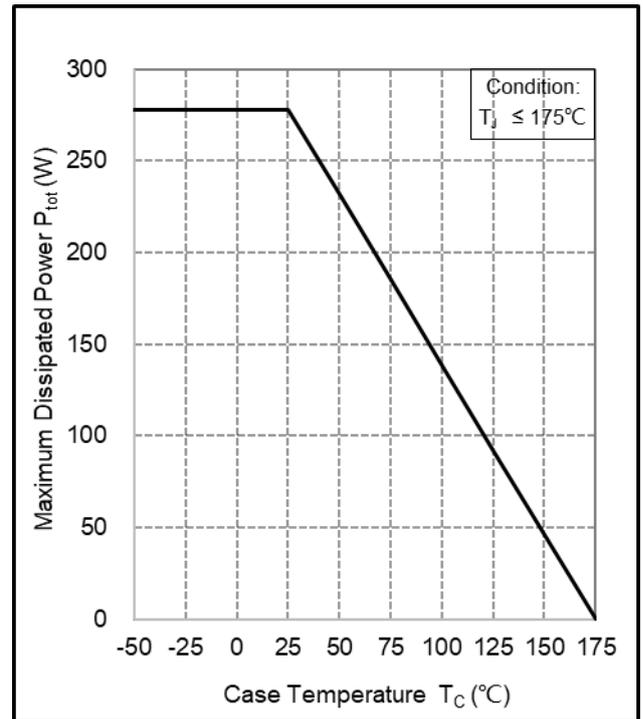


Figure 20. Maximum power dissipation derating vs. temperature

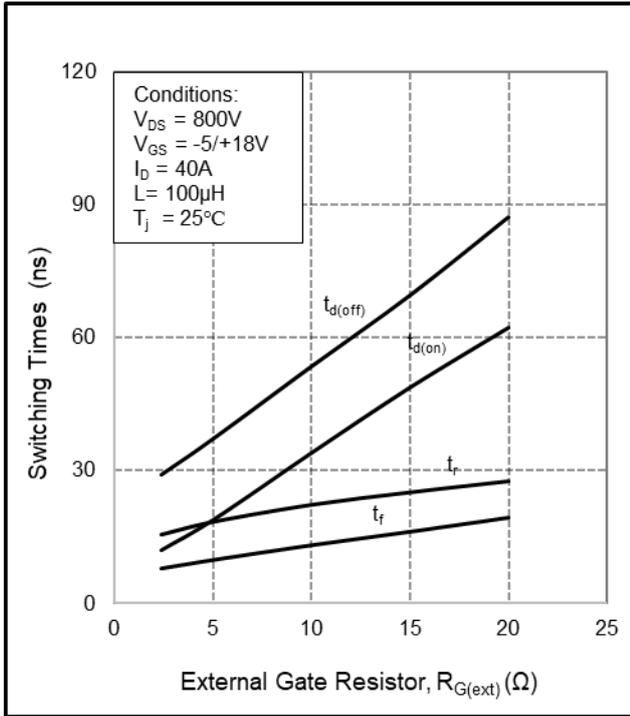


Figure 21. Switching Times vs.  $R_{G(ext)}$

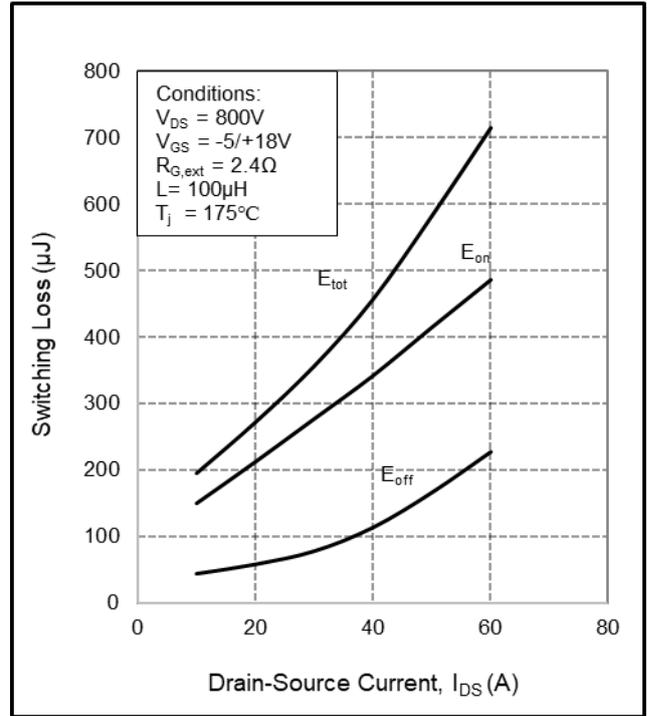


Figure 22. Clamped inductive Switching energy vs. drain current

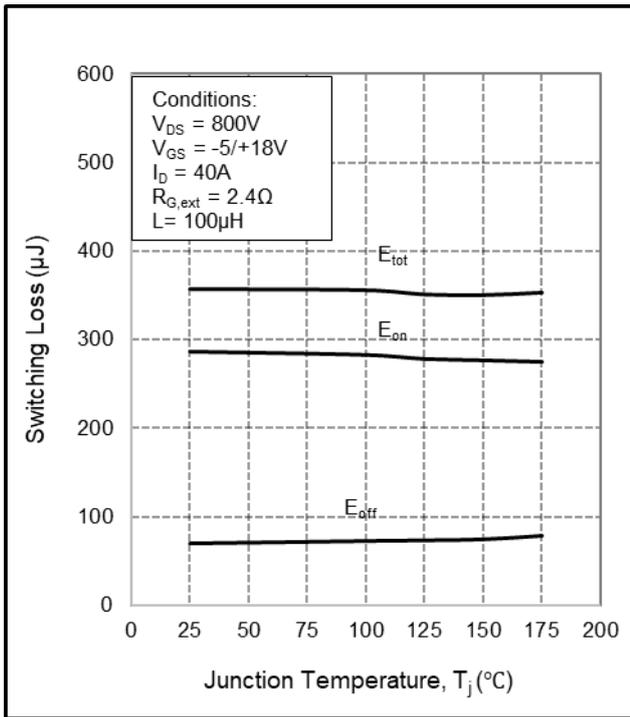


Figure 23. Clamped inductive Switching energy vs. temperature

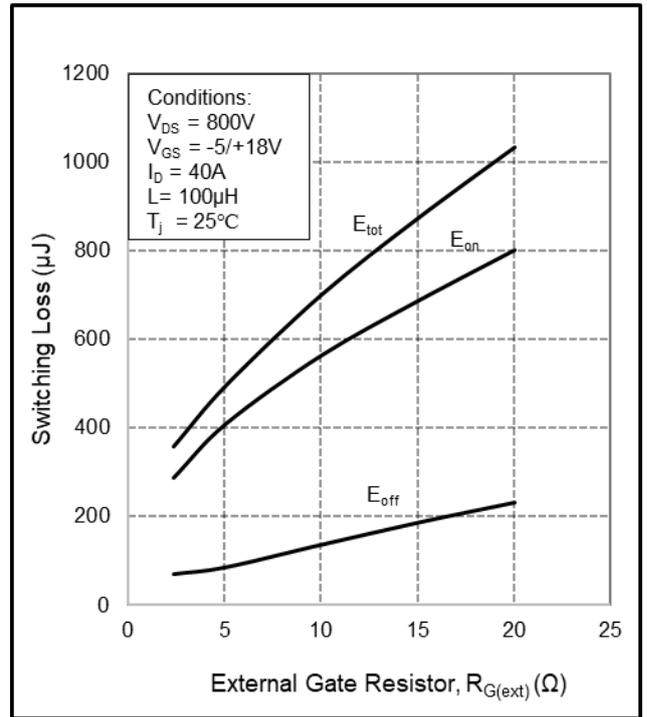


Figure 24. Clamped inductive Switching energy vs.  $R_{G(ext)}$

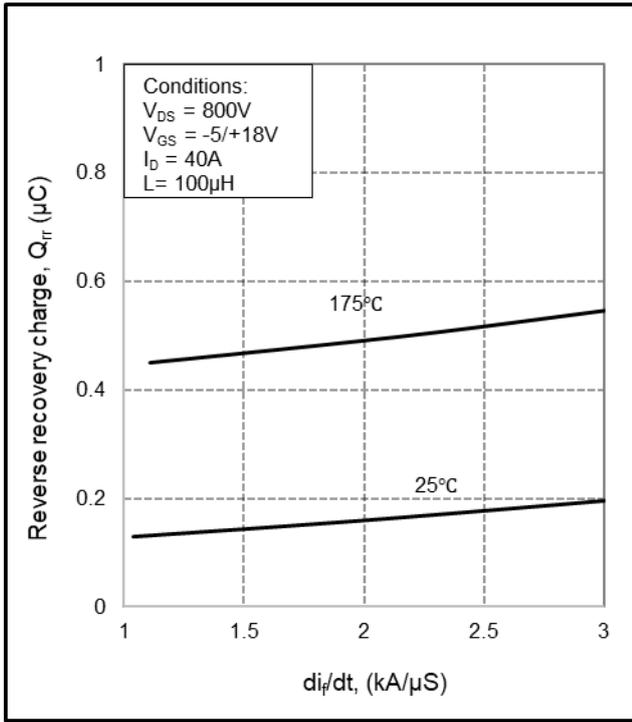


Figure 25. Reverse recovery charge vs.  $di/dt$

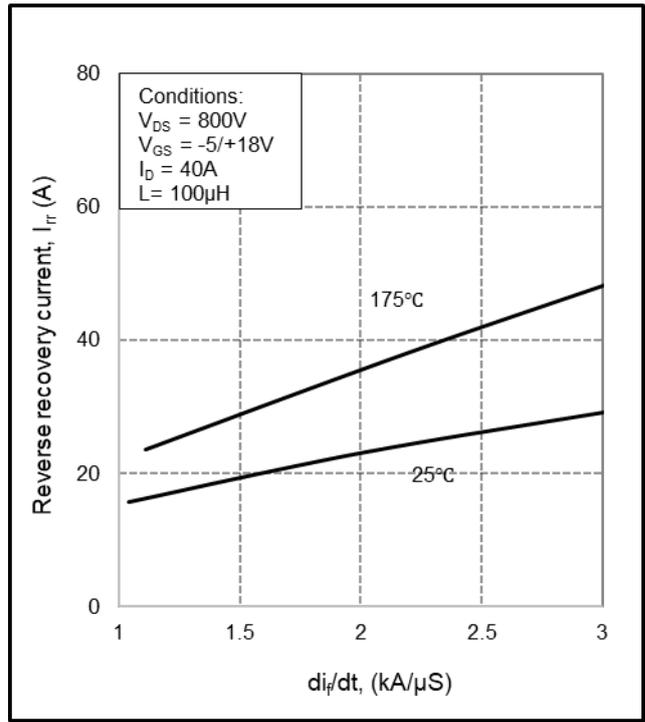


Figure 26. Reverse recovery current vs.  $di/dt$

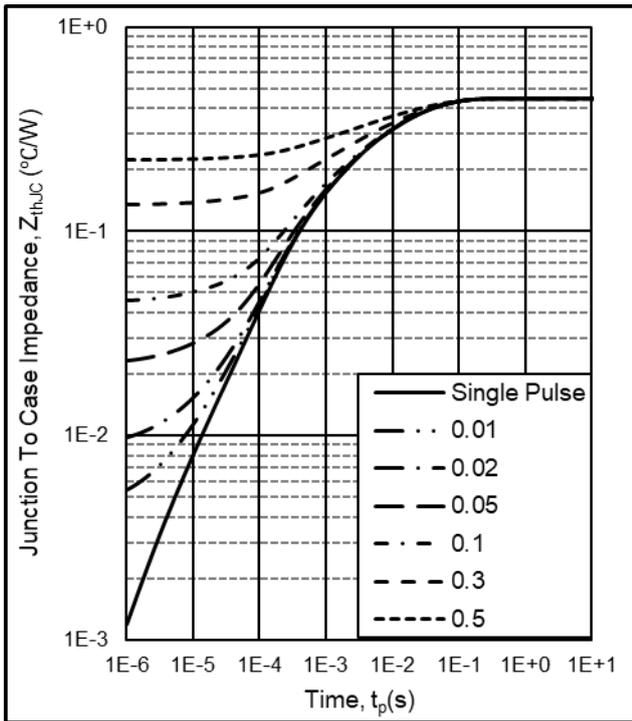


Figure 27. Transient thermal impedance (Junction - Case)

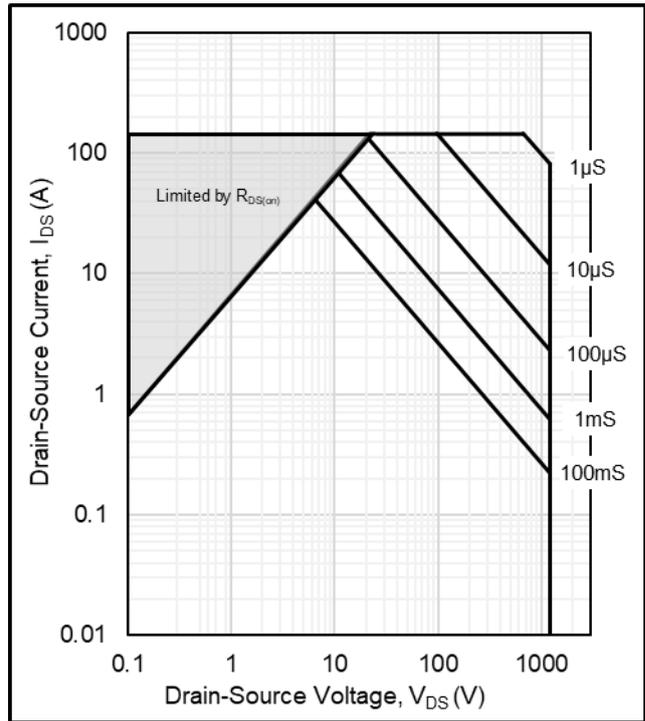


Figure 28. Safe Operating Area

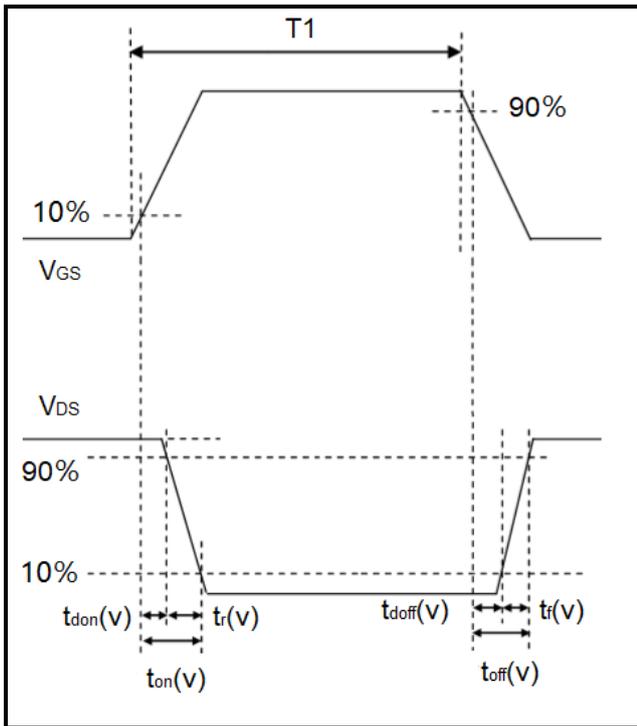


Figure 29. Switching times definition

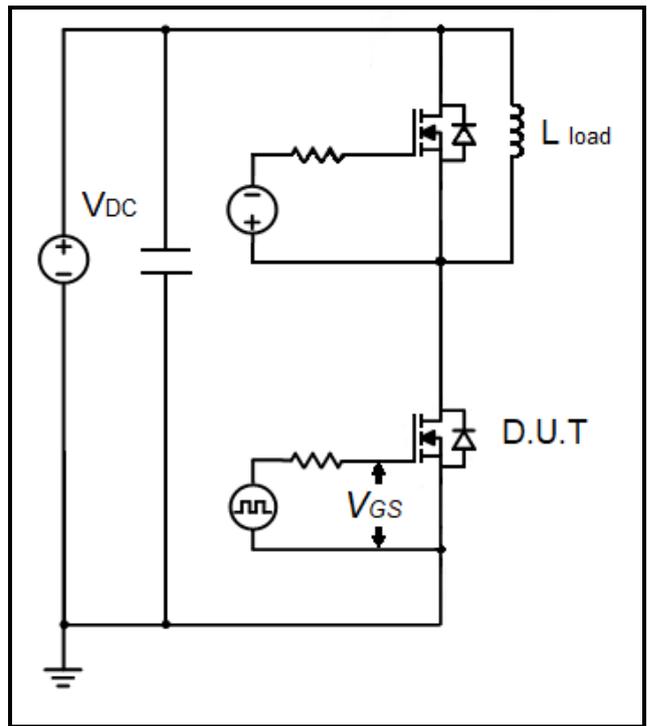
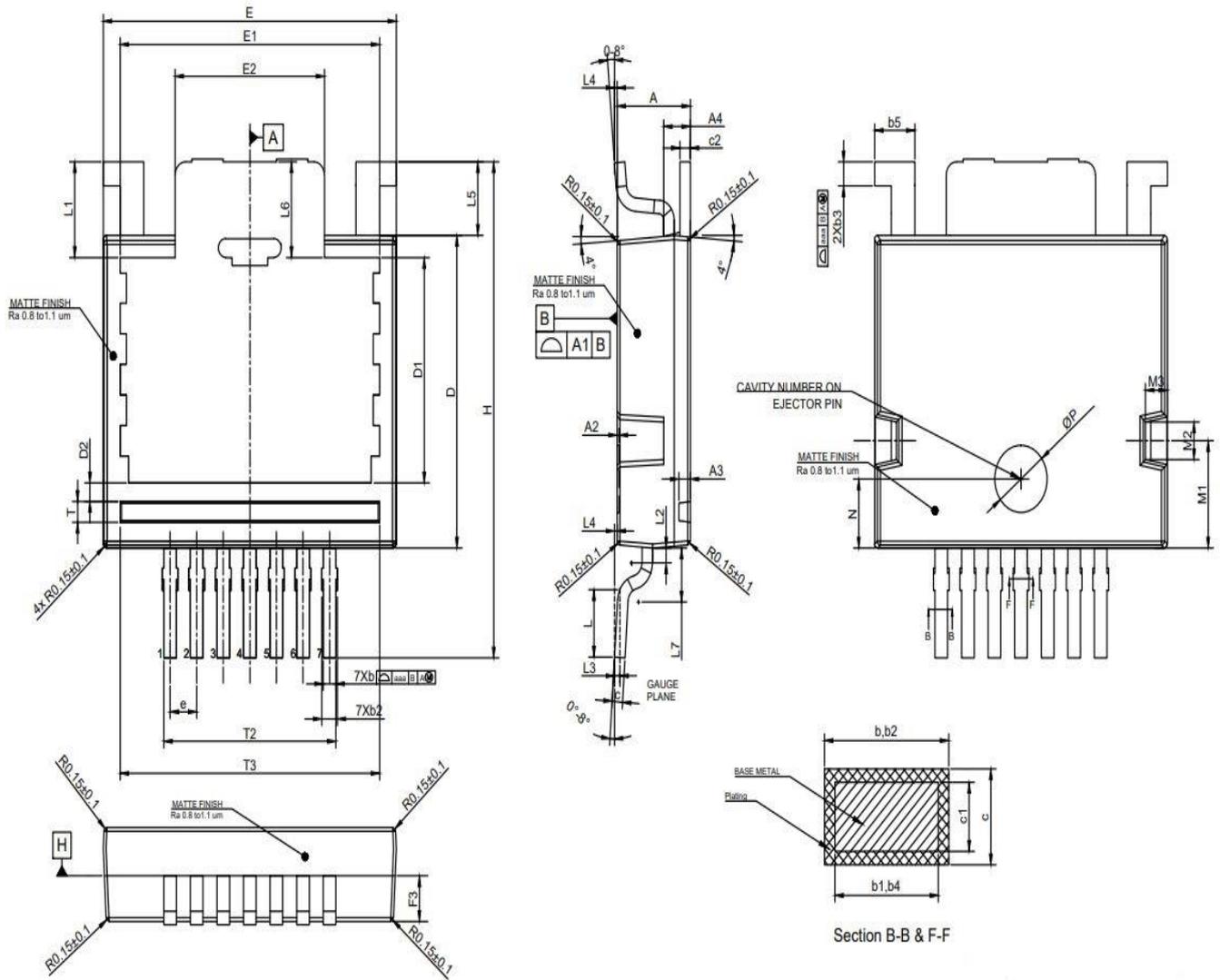


Figure 30. Clamped inductive switching waveform test circuit

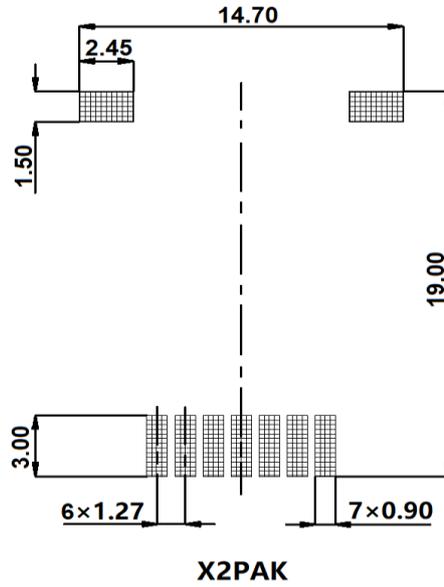
### Package Information



Dimension unit: [mm]					
Symbol	Min	Max	Symbol	Min	Max
A	3.40	3.60	T	0.58	0.98
A1	0.05		T2	8.12	8.32
A2	0.05	0.15	T3	12.30	12.50
A3	0.45	0.65	e	1.27 BSC.	
A4	1.27 REF.		H	18.00	19.00
b	0.50	0.70	L	2.40	2.60
b1	0.50	0.65	L1	3.47	3.67
b2	0.50	1.00	L2	0.45	0.65
b3	0.80	1.00	L3	0.26 BSC.	
b4	0.50	0.95	L4	0.08	0.18
b5	1.83	2.03	L5	2.65	2.85
c	0.40	0.60	L6	3.47	3.67
c1	0.40	0.55	L7	2.01 REF.	
c2	0.40	0.60	M1	4.00 REF.	
D	11.50	11.80	M2	1.31	1.51
D1	8.30	8.50	M3	0.95	1.15
D2	0.70 REF.		N	2.57 REF.	
E	13.90	14.10	ØP	2.50 REF.	
E1	12.30	12.50	aaa	————	0.10
E2	7.04	7.24			
F3	1.60	1.80			

## Recommended Solder Pad Layout

Note: All dimensions are in mm



## Ordering Information

Part number	AMS1200032VA2-ASARH
Package	X2PAK
Unit quantity	800 EA
Packing type	Tape & Reel

## Important Notices – Read Carefully

Before you use the products that made in Sanan, you are requested to carefully read this document and fully understand its contents. Sanan Semiconductor Co., Ltd shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of products that made in Sanan.

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