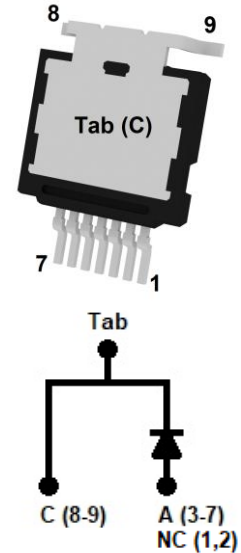


3rd Generation 1200V/50A SiC Schottky Barrier Diode

Features

- AEC-Q101 qualified
- Revolutionary semiconductor material - Silicon Carbide (SiC)
- No reverse recovery
- High-speed switching performance
- Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- Junction temperature range from -55°C to 175°C
- RoHS compliant



Package Type: SAPKG-9L

Potential Applications

- DC/DC converter for EV/HEV
- On board charger (OBC)

Description

The ADS120J050V3 SiC Schottky Barrier Diode (SBD) has been developed using Sanan's advanced 3rd generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 1200V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components and being AEC-Q101 qualified, it is qualified for use in automotive application.



Product Specifications

Device	V _{RRM}	I _F (135°C)	V _F (25°C)	Q _c	Marking
ADS120J050V3	1200V	69A	1.35V	304nC	DS120050V3

CONTENTS

Features.....	1
Potential Applications.....	1
Description.....	1
Product Specifications	1
Table 1 Maximum Ratings.....	3
Table 2 Thermal Resistance.....	3
Table 3 Static Electrical Characteristics.....	4
Table 4 Dynamic Electrical Characteristics	4
Electrical Characteristic Diagrams.....	5
Package Information	7
Recommended Solder Pad Layout.....	9
Ordering Information	9
Important Notices – Read Carefully	10
Warning	10

Table 1. Maximum Ratings

(T_C = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V _{RRM}	1200	V	T _C = 25°C
Surge peak reverse voltage	V _{RSM}	1200		T _C = 25°C
DC reverse voltage	V _{DC}	1200		T _C = 25°C
Continuous forward current	I _F	134	A	T _C = 25°C
		69		T _C = 135°C
		50		T _C = 153°C
Surge non-repetitive forward current	I _{FSM}	504	A	T _C = 25°C, t _p = 10ms, half sine pulse
		446		T _C = 150°C, t _p = 10ms, half sine pulse
Surge repetitive forward current	I _{FRM}	202	A	T _C = 25°C, t _p = 10ms, half sine wave D = 0.1
Power dissipation	P _{tot}	625	W	T _C = 25°C
i ² t value	∫i ² dt	1270	A ² s	T _C = 25°C, t _p = 10ms
Operating junction temperature	T _J	-55~175	°C	
Storage temperature	T _{stg}	-55~175	°C	

Table 2. Thermal Resistance

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	R _{th(j-c)}	/	0.24	/	°C/W	

Table 3. Static Electrical Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
DC blocking voltage	V_{DC}	1200	/	/	V	$I_R = 200\ \mu\text{A}$
Forward voltage	V_F	/	1.35	1.50	V	$I_F = 50\text{A}$, $T_j = 25^\circ\text{C}$
		/	1.75	2.20		$I_F = 50\text{A}$, $T_j = 175^\circ\text{C}$
Reverse current	I_R	/	5	120	μA	$V_R = 1200\text{V}$, $T_j = 25^\circ\text{C}$
		/	40	800		$V_R = 1200\text{V}$, $T_j = 175^\circ\text{C}$

Table 4. Dynamic Electrical Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Total capacitance	C	/	4547	/	pF	$V_R = 0\text{V}$, $f = 1\text{MHz}$
		/	285	/		$V_R = 400\text{V}$, $f = 1\text{MHz}$
		/	202	/		$V_R = 800\text{V}$, $f = 1\text{MHz}$
Total capacitive charge	Q_C	/	304	/	nC	$V_R = 800\text{V}$
Capacitance stored energy	E_C	/	86	/	μJ	$V_R = 800\text{V}$

Electrical Characteristic Diagrams

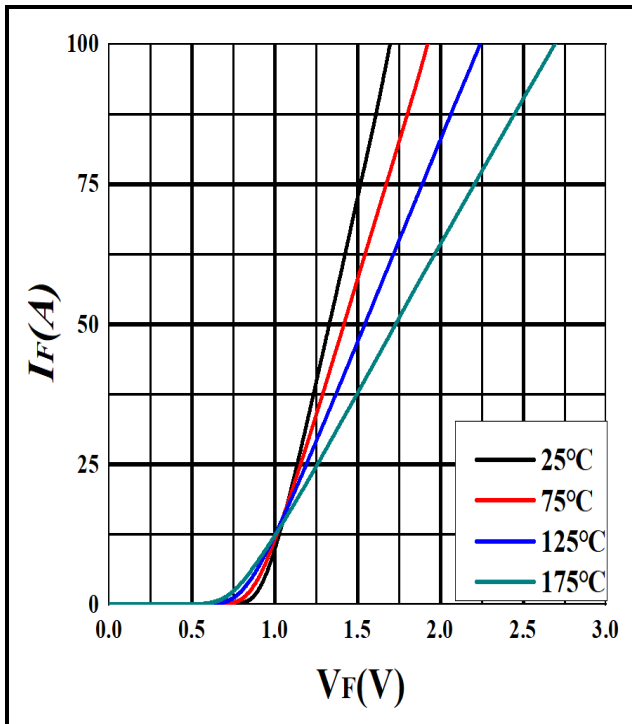


Figure 1. Forward characteristics

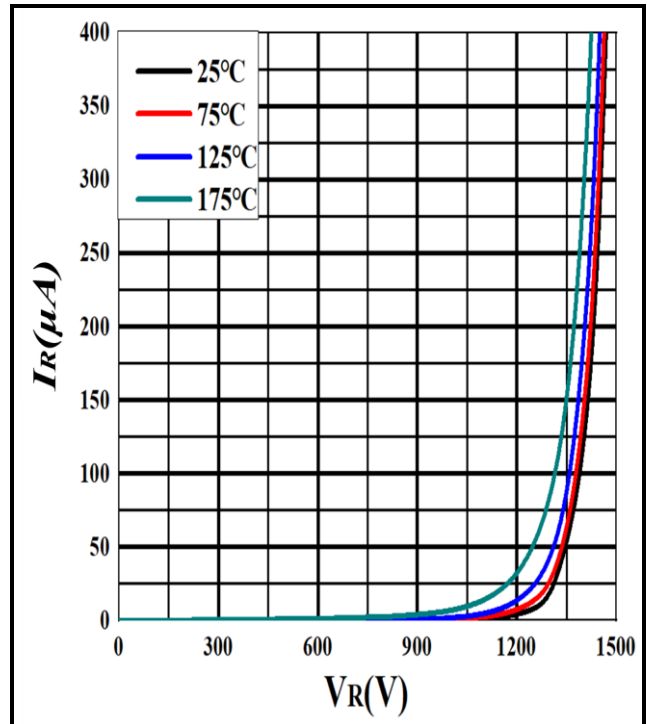


Figure 2. Reverse characteristics

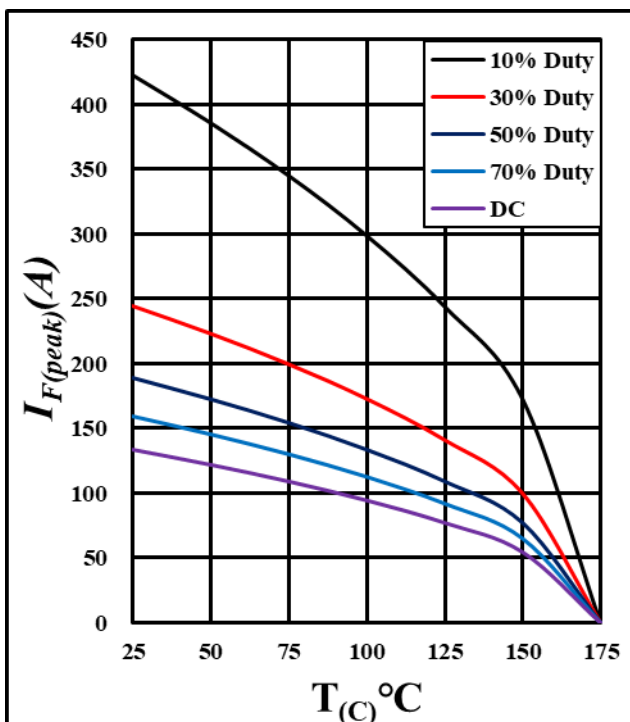


Figure 3. Current derating

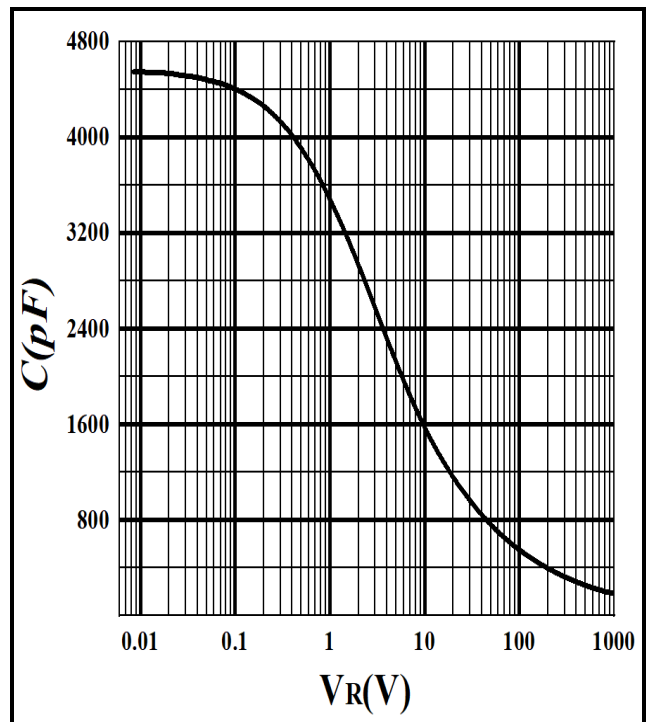


Figure 4. Capacitance vs. reverse voltage

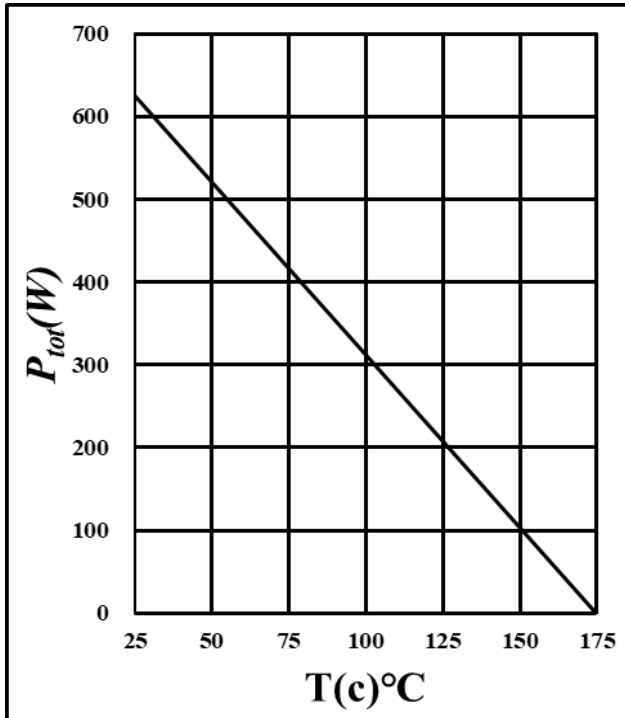


Figure 5. Power derating

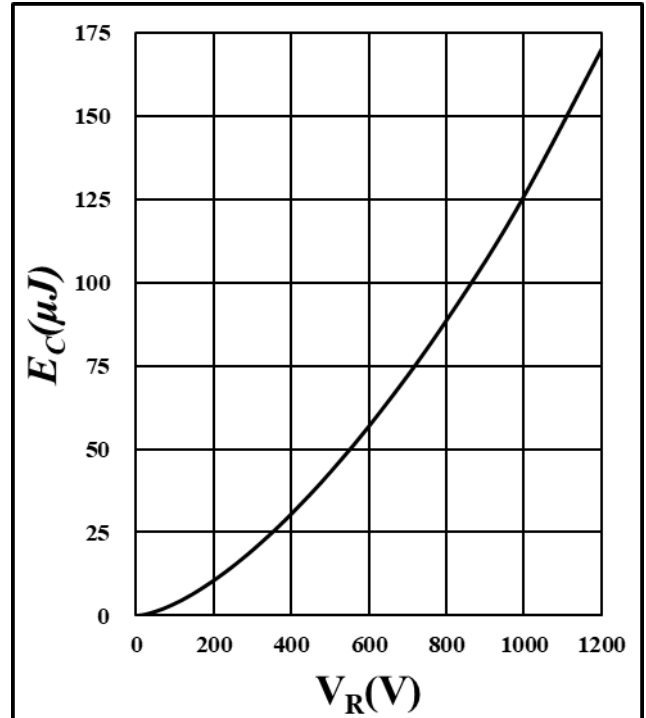


Figure 6. Capacitance stored energy

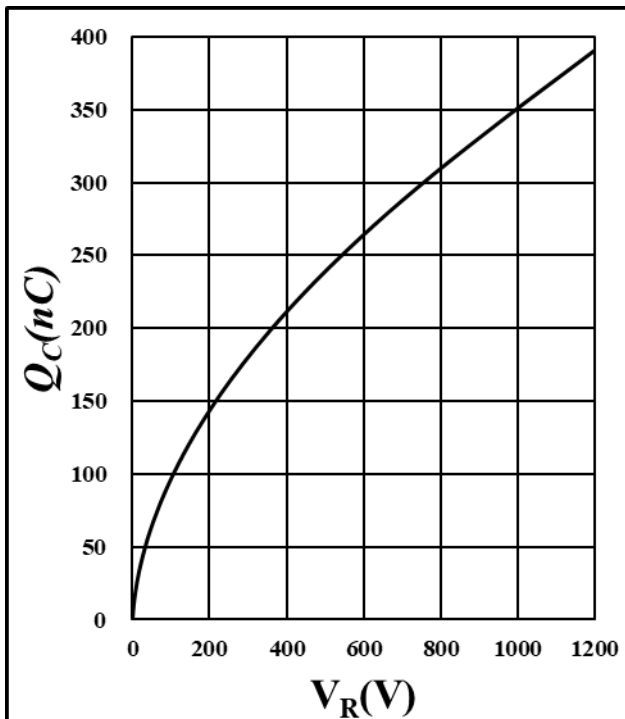


Figure 7. Total capacitance charge vs. reverse voltage

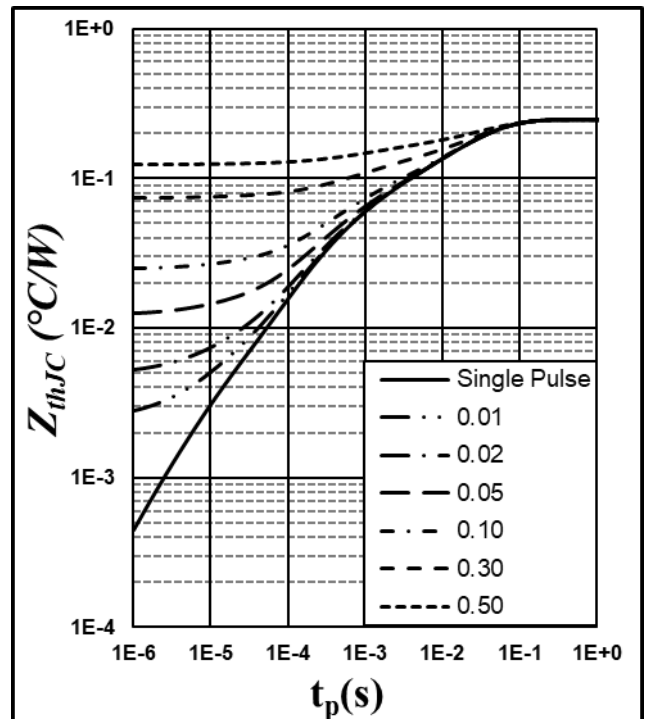
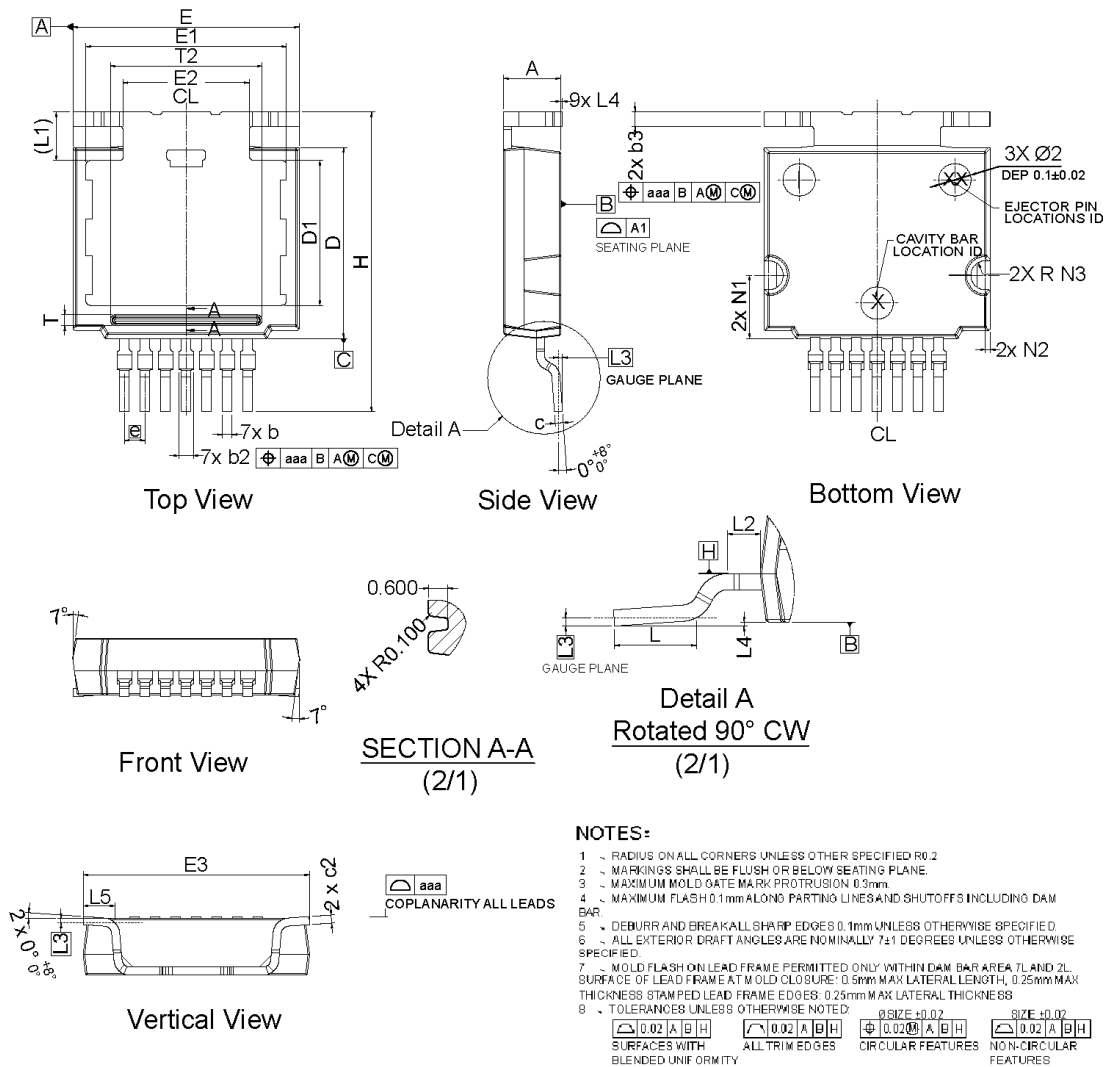


Figure 8. Transient Thermal Impedance
(Junction Case)

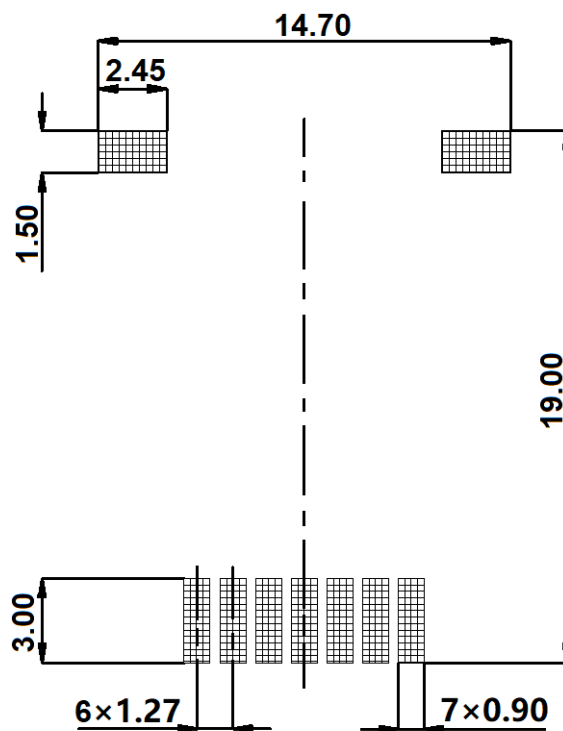
Package Information



Dimension unit: [mm]			
Symbol	Min	Nom	Max
A	3.42	3.52	3.62
A1	-	0.05	-
b	0.50	0.60	0.70
b2	0.50	0.75	1.00
b3	0.87	0.97	1.07
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	8.95	9.10
E	13.90	14.00	14.10
E1	12.30	12.40	12.50
E2	7.70	7.80	7.90
E3	13.90	14.00	14.10
e		1.27 BSC	
H	18.00	18.58	19.00
L	2.40	2.50	2.60
L1	-	3.00	-
L2	0.90	1.00	1.10
L3		0.25 BSC	
L4	0.02	0.12	0.22
L5	1.83	1.93	2.03
aaa	-	0.10	-
N1	3.80	3.90	4.00
N2	0.25	0.30	0.35
N3	0.75	0.85	0.95
T	0.55	0.65	0.75
T2	9.28	9.38	9.48

Recommended Solder Pad Layout

Note: All dimensions are in mm



SAPKG-9L

Ordering Information

Part number	ADS120J050V3-ASARR
Package	SAPKG-9L
Unit quantity	600 EA
Packing type	Tape & Reel

Important Notices – Read Carefully

Before you use our products, you are requested to carefully read this document and fully understand its contents. Sanan Semiconductor Co., Ltd. shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of Sanan's products.

Sanan Semiconductor Datasheets are subject to change. Information presented in this document is from the characterization of engineering lots. Sanan Semiconductor Co., Ltd reserves the right to change limits, test conditions, and dimensions without notice. Information contained in this document are typical values and shall in no event be regarded as a guarantee of characteristics. With respect to any information regarding the application of the product, Sanan hereby disclaims all warranties and liabilities of any kind. The information in this document is exclusively for trained technical staff. It is the responsibility of the customer's technical department to decide the suitability of the product in the customer's application and Sanan Semiconductor Co., Ltd assumes no responsibility or liability whatsoever for the use of the information contained in this document.

In case there is any clause in this document or in any other documents which is contradictory to this clause, this clause shall prevail. This clause shall survive after termination of this document.

Warning

Due to technical requirements, Sanan's products may contain dangerous substances. For detailed information about the substance(s), please contact the Sanan office. Sanan Semiconductor Co., Ltd bears no responsibility for any damage whatsoever due to the substance(s) used in Sanan's products.

Contact info

Website: <https://www.sanan-semiconductor.com/>
<https://www.sanan-semiconductor.com/en>