

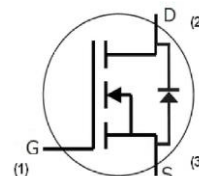
1700V 1000mΩ Silicon Carbide Power MOSFET

Features

- High blocking voltage with low on-resistance
- High switching speed with low capacitance
- Very fast and robust intrinsic body diode with low reverse recovery
- Very low switching losses
- Excellent avalanche ruggedness
- RoHS compliant

Benefits

- Greater system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency
- Easy to parallel and simple to drive



Potential Applications

- Solar inverters
- Uninterrupted power supplies
- Switch mode power supplies
- Motor drives

Package Type: TO-247-3L



Description

The Sanan Semiconductor 1700V/1000mΩ silicon carbide power MOSFET uses advanced SiC MOSFET technology with low on-resistance, low switching losses, and a high operation temperature of 175°C. It is suitable for use in high frequency circuits and provides a reduction in overall system size, increased efficiency and increased switching frequency. It has been widely used in applications including solar inverters, uninterrupted power supplies, switch mode power supplies, and motor drives. Using RoHS compliant components, it is qualified for use in industrial application.

Product Specifications

Device	V _{DS}	I _D (25°C)	R _{(DS)on}	Marking
SMS1701000K	1700V	5.7A	1000mΩ	MS1701000K

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Table 1. Maximum Ratings

($T_C = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit	Test conditions
Drain-source voltage	V_{DSmax}	1700	V	$V_{GS} = 0V, I_D = 100\mu A, T_C = 25^\circ\text{C}$
Gate-source voltage, max. transient voltage	V_{GSmax}	-10/+27		$t_p \leq 0.5\mu s, D < 1\%, T_C = 25^\circ\text{C}$
Gate-source voltage, max. static voltage	V_{GSmax}	-8/+24		$T_C = 25^\circ\text{C}$
Gate-source voltage	V_{GSop}	-5/+20		Recommended operation values, $T_C = 25^\circ\text{C}$
Continuous drain current	I_D	5.7	A	$V_{GS} = 20V, T_C = 25^\circ\text{C}$
		4		$V_{GS} = 20V, T_C = 100^\circ\text{C}$
Pulsed drain current	$I_{D(pulse)}$	14	A	Pulse width t_p limited by T_{jmax}
Power dissipation	P_{tot}	100	W	$T_C = 25^\circ\text{C}$
Operating junction temperature	T_j	-55~175	$^\circ\text{C}$	
Storage temperature	T_{stg}	-55~175	$^\circ\text{C}$	
Soldering temperature	T_L	260	$^\circ\text{C}$	1.6mm from case for 10s
Mounting torque	M	0.7	Nm	M3 screw

Table 2. Thermal Resistances

Parameter	Symbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Thermal resistance from junction to case	$R_{th(j-c)}$	/	1.25	/	$^\circ\text{C/W}$	
Thermal resistance from junction to ambient	$R_{th(j-a)}$	/	/	40	$^\circ\text{C/W}$	

Table 3. Static Electrical Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Drain-source breakdown voltage	$V_{(BR)DSS}$	1700	/	/	V	$V_{GS} = 0V, I_D = 100\mu A$
Gate threshold voltage	$V_{GS(th)}$	2	2.6	4		$V_{DS} = V_{GS}, I_D = 0.5mA$
		/	1.8	/		$V_{DS} = V_{GS}, I_D = 0.5mA, T_j = 175^\circ\text{C}$
Drain-source leakage current	I_{DSS}	/	1	100	μA	$V_{DS} = 1700V, V_{GS} = 0V$
Gate-source leakage current	I_{GSS}	/	1	250	nA	$V_{GS} = 20V, V_{DS} = 0V$
Drain-source on-state resistance	$R_{DS(on)}$	/	1.0	1.2	Ω	$V_{GS} = 20V, I_D = 2A$
		/	1.5	/		$V_{GS} = 20V, I_D = 2A, T_j = 175^\circ\text{C}$
Transconductance	g_{fs}	/	1.0	/	S	$V_{DS} = 20V, I_D = 2A$
		/	1.2	/		$V_{DS} = 20V, I_D = 2A, T_j = 175^\circ\text{C}$
Internal gate resistance	$R_{g(int)}$	/	6	/	Ω	$f = 1MHz, V_{AC} = 25mV$
Avalanche energy	E_{AS}	/	150	/	mJ	$L = 5mH, V_{DD} = 50V$

Table 4. Dynamic Electrical Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Input capacitance	C _{iSS}	/	227	/	pF	V _{GS} = 0V, V _{DS} = 1000V, f = 1MHz, V _{AC} = 25mV
Output capacitance	C _{oSS}	/	12.5	/		
Reverse transfer capacitance	C _{rSS}	/	2	/		
C _{oSS} stored energy	E _{oSS}	/	7.7	/	μJ	
Gate to source charge	Q _{gs}	/	1.67	/	nC	V _{DD} = 1200V,V _{GS} = -5/+20V, I _D = 2A,I _{GS} = 1mA
Gate to drain charge	Q _{gd}	/	9.2	/		
Total gate charge	Q _g	/	16.7	/		

Table 5. Switching Characteristics

($T_j = 25^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Turn-on delay time	t _{d(on)}	/	18	/	ns	V _{DD} = 1200V, V _{GS} = -5/+20V, I _D = 2A, R _{G(ext)} = 12Ω, L = 1364μH
Rise time	t _r	/	16	/		
Turn-off delay time	t _{d(off)}	/	24	/		
Fall time	t _f	/	79	/		
Turn-on switching energy	E _{on}	/	63	/	μJ	
Turn-off switching energy	E _{off}	/	29	/		
Turn-on delay time	t _{d(on)}	/	17	/	ns	V _{DD} = 1200V, V _{GS} = -5/+20V, I _D = 2A, R _{G(ext)} = 12Ω, L = 1364μH, T _j = 175°C
Rise time	t _r	/	16	/		
Turn-off delay time	t _{d(off)}	/	29	/		
Fall time	t _f	/	84	/		
Turn-on switching energy	E _{on}	/	88	/	μJ	
Turn-off switching energy	E _{off}	/	29	/		

Table 6. Reverse SiC Diode Characteristics

($T_j = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Values			Unit	Test conditions
		Min.	Typ.	Max.		
Diode forward voltage	V_{SD}	/	3.60	/	V	$V_{GS} = -5\text{V}$, $I_{SD} = 1\text{A}$
		/	3.20	/		$V_{GS} = -5\text{V}$, $I_{SD} = 1\text{A}$, $T_j = 175^\circ\text{C}$
Continuous diode forward current	I_S	/	/	5	A	$V_{GS} = -5\text{V}$, $T_C = 25^\circ\text{C}$
Diode pulse current	$I_{S, \text{pulse}}$	/	/	21	A	$V_{GS} = -5\text{V}$, pulse width t_p limited by $T_{j\text{max}}$
Reverse recovery time	t_{rr}	/	34	/	ns	$V_{GS} = -5\text{V}$, $I_{SD} = 2\text{A}$, $V_R = 1200\text{V}$, $di/dt = 0.19\text{kA}/\mu\text{s}$
Reverse recovery charge	Q_{rr}	/	0.04	/	uC	
Peak reverse recovery current	I_{rrm}	/	1.85	/	A	
Reverse recovery time	t_{rr}	/	39	/	ns	$V_{GS} = -5\text{V}$, $I_{SD} = 2\text{A}$, $V_R = 1200\text{V}$, $T_j = 175^\circ\text{C}$, $di/dt = 0.19\text{kA}/\mu\text{s}$
Reverse recovery charge	Q_{rr}	/	0.08	/	uC	
Peak reverse recovery current	I_{rrm}	/	3.57	/	A	

Electrical Characteristic Diagrams

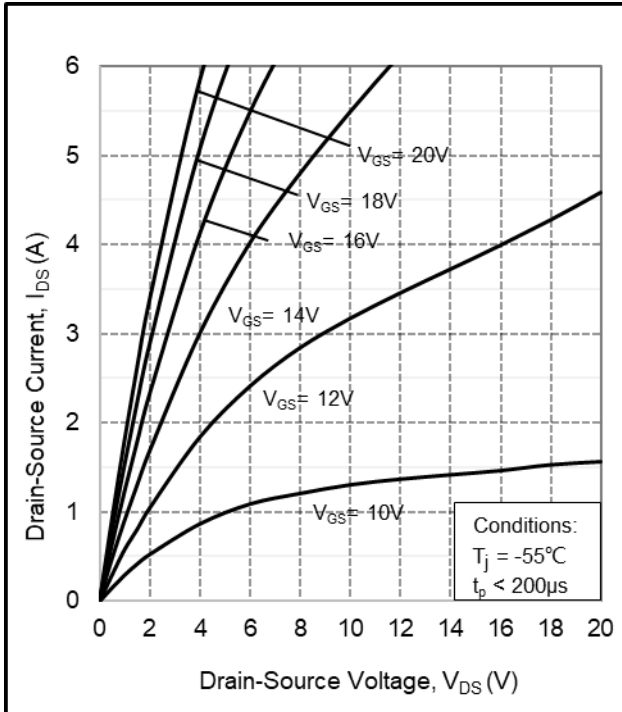


Figure 1. Output characteristics at $T_j = -55^\circ\text{C}$

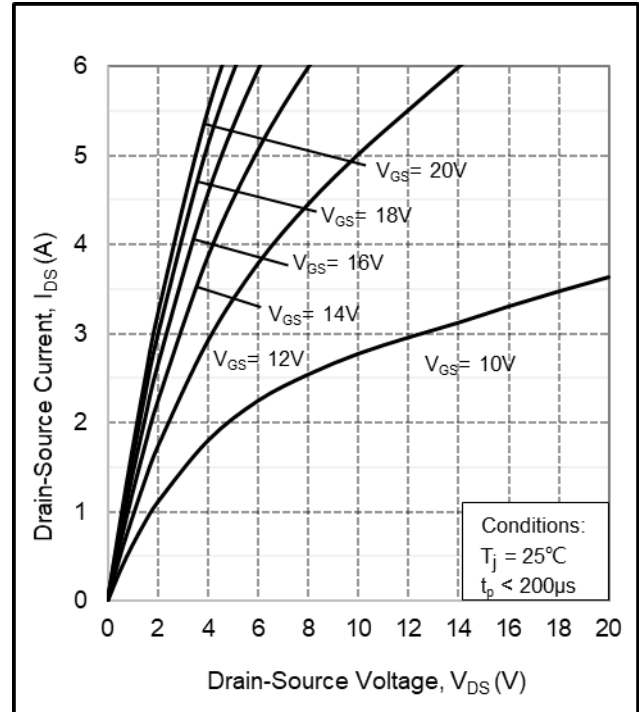


Figure 2. Output characteristics at $T_j = 25^\circ\text{C}$

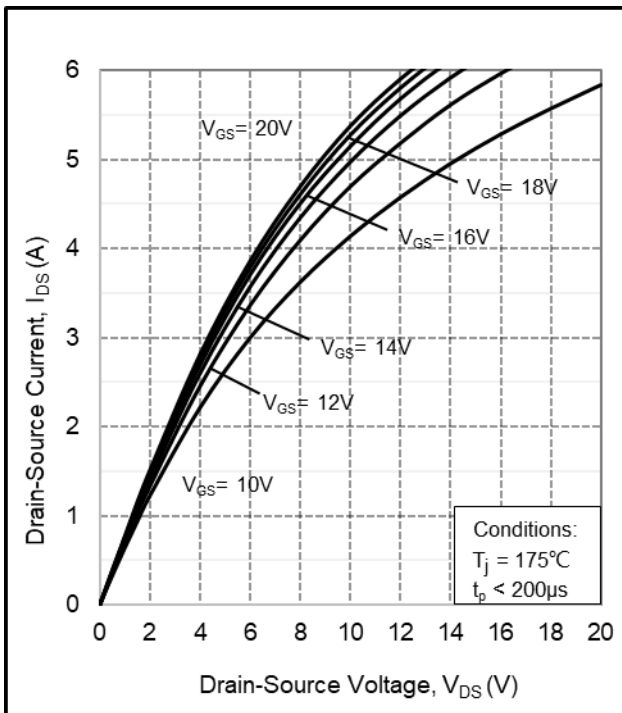


Figure 3. Output characteristics at $T_j = 175^\circ\text{C}$

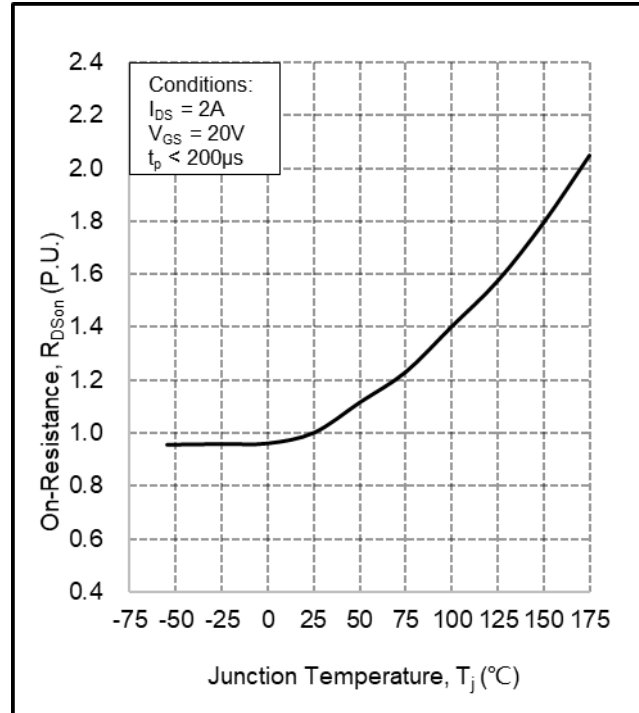


Figure 4. Normalized on-resistance vs. temperature

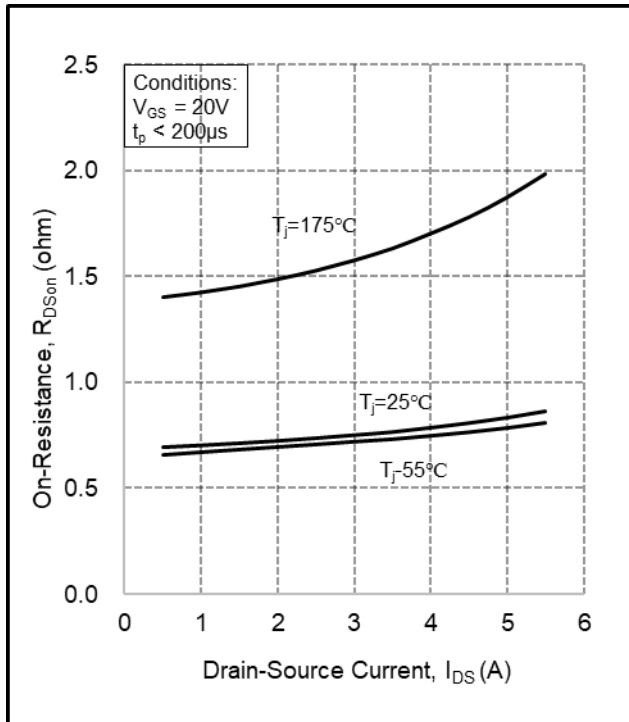


Figure 5. On-resistance vs. drain current for various temperatures

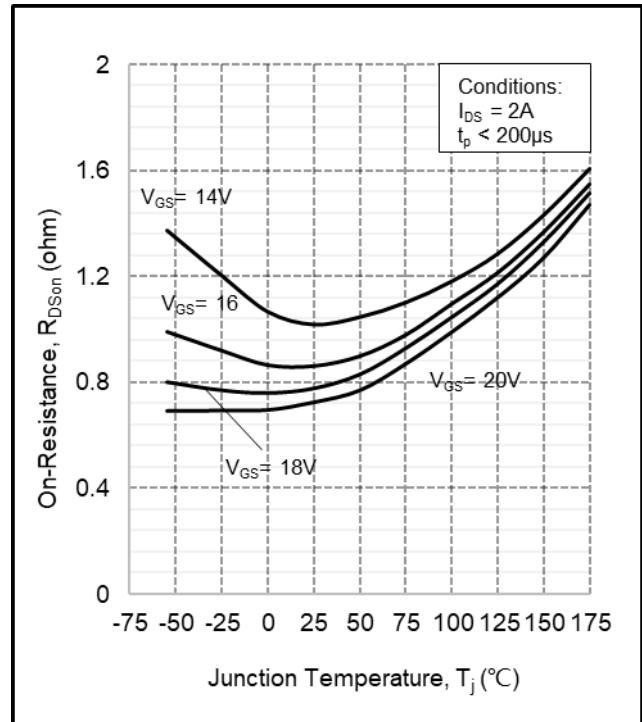


Figure 6. On-resistance vs. temperature for various gate voltages

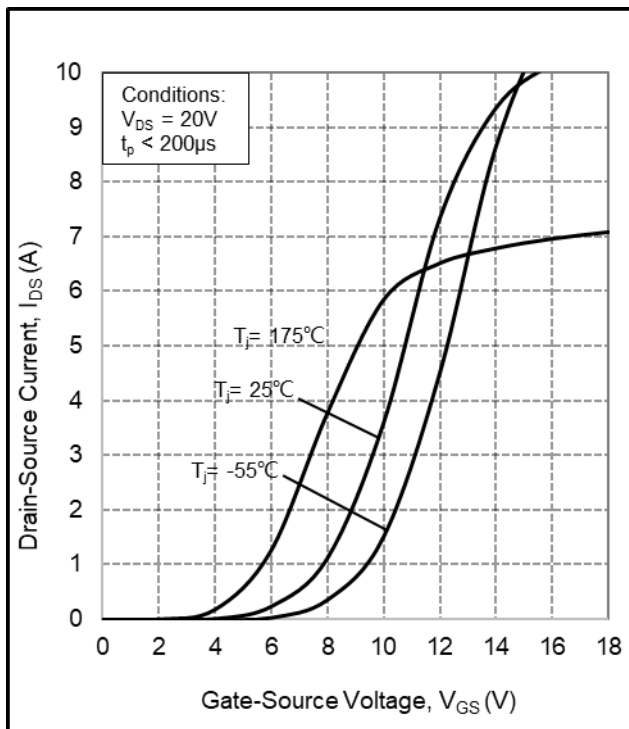


Figure 7. Transfer characteristic for various junction temperatures

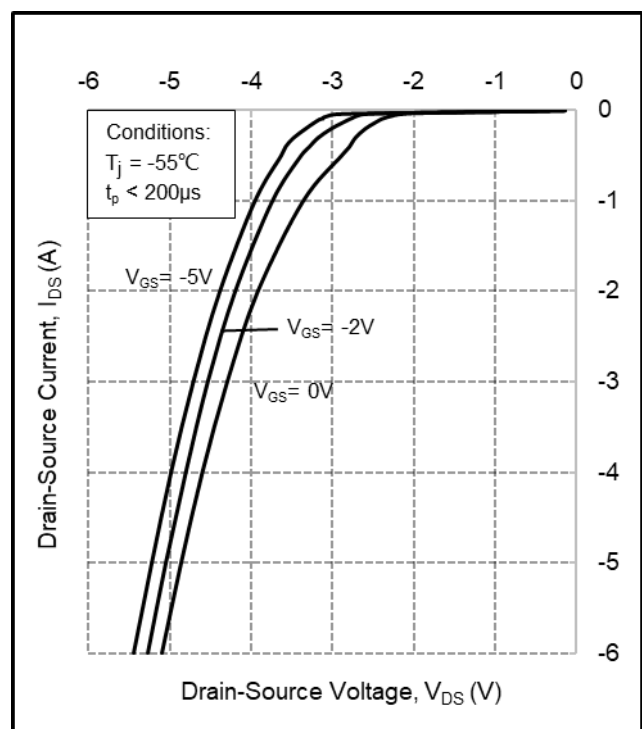


Figure 8. Body diode characteristic at $T_J = -55^\circ\text{C}$

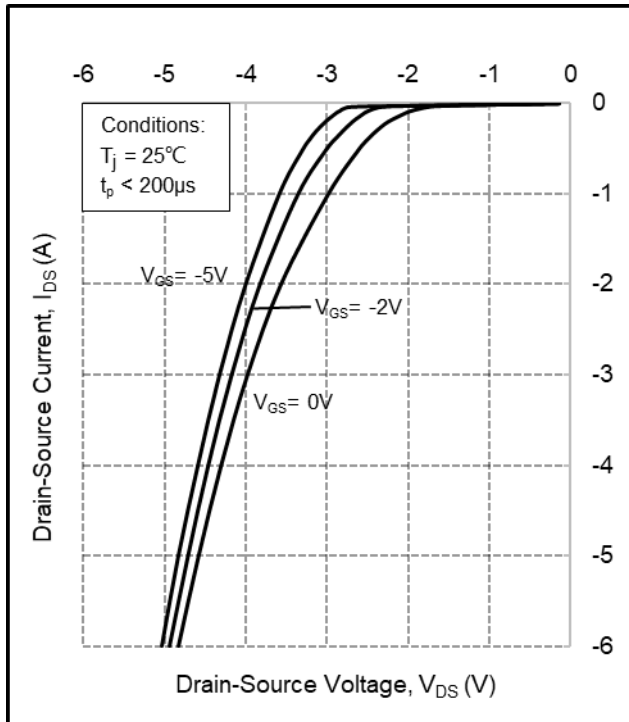


Figure 9. Body diode characteristic at $T_j = 25^\circ\text{C}$

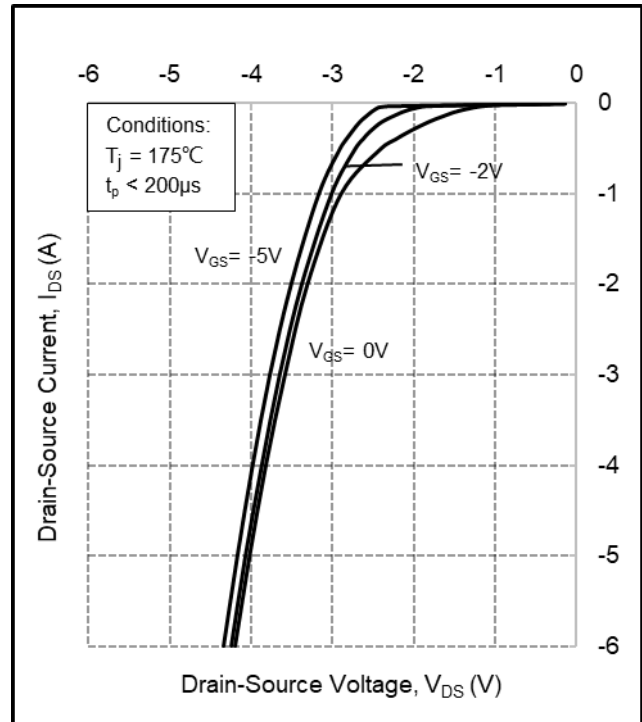


Figure 10. Body diode characteristic at $T_j = 175^\circ\text{C}$

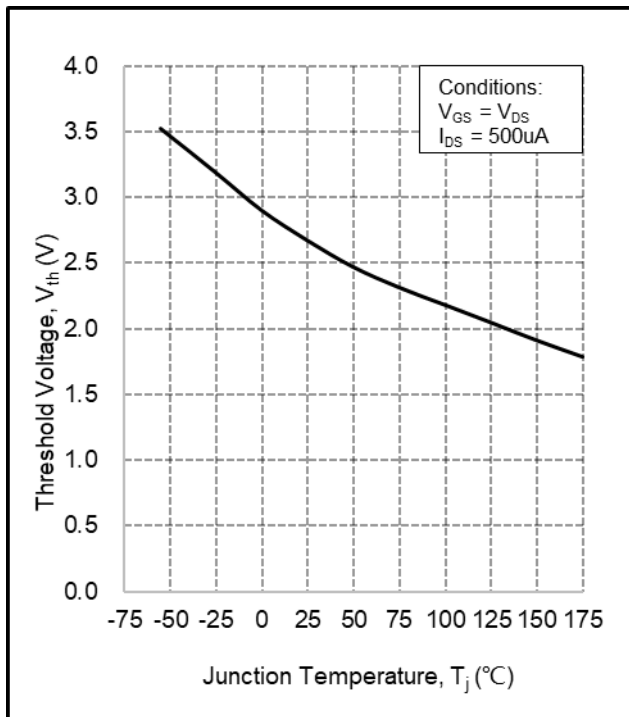


Figure 11. Threshold voltage vs. temperature

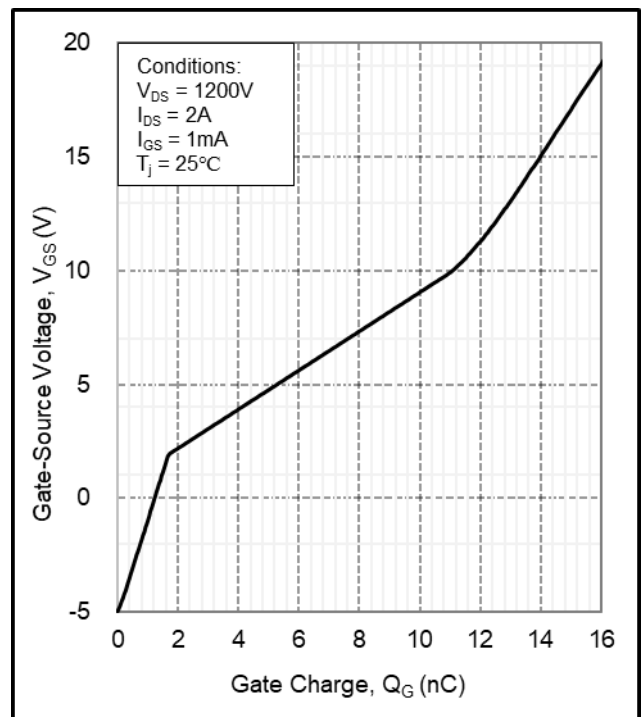


Figure 12. Gate Charge Characteristic

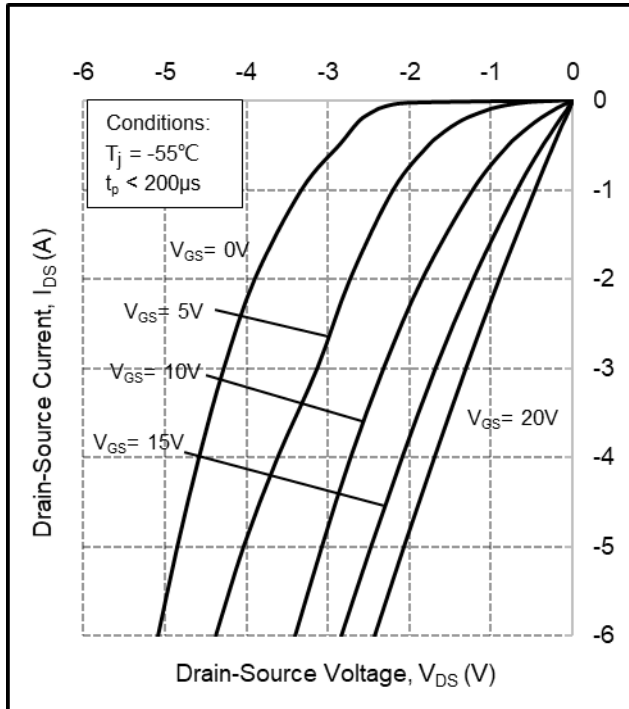


Figure 13. 3rd quadrant characteristic
at $T_j = -55^\circ\text{C}$

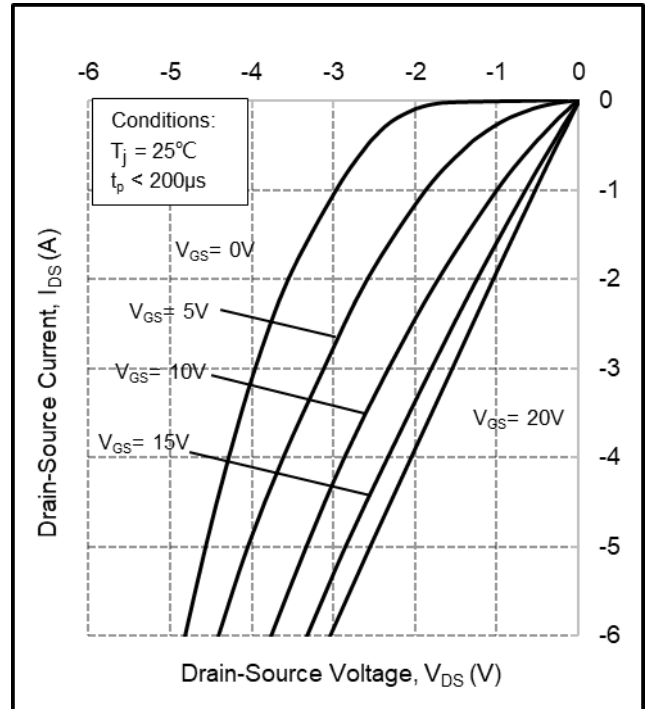


Figure 14. 3rd quadrant characteristic
at $T_j = 25^\circ\text{C}$

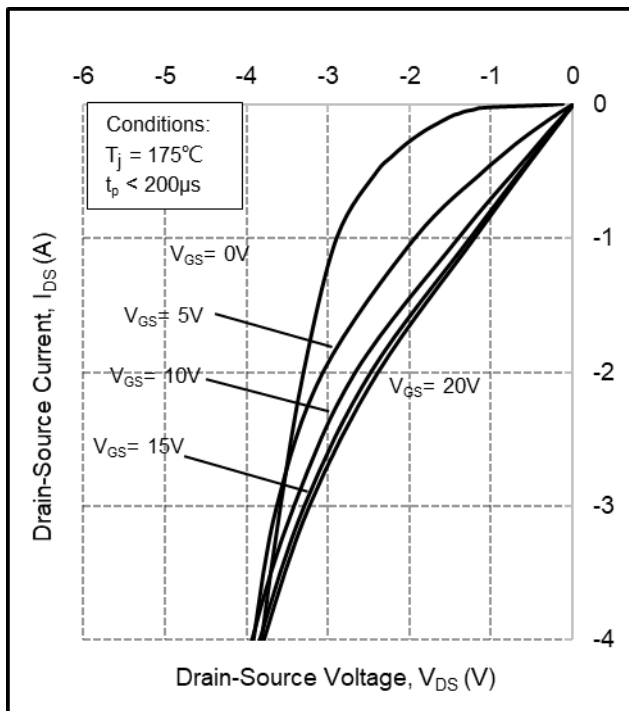


Figure 15. 3rd quadrant characteristic
at $T_j = 175^\circ\text{C}$

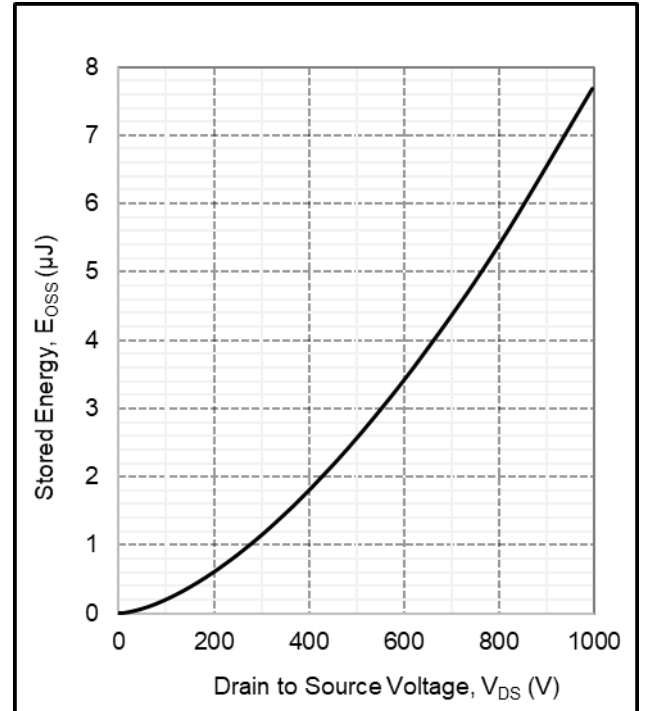


Figure 16. Output capacitor stored energy

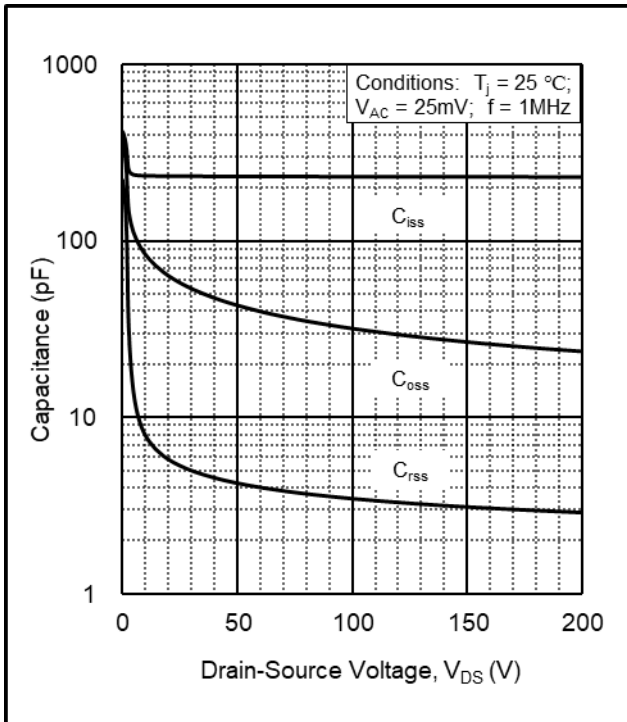


Figure 17. Capacitance vs. drain-source voltage
(0 - 200V)

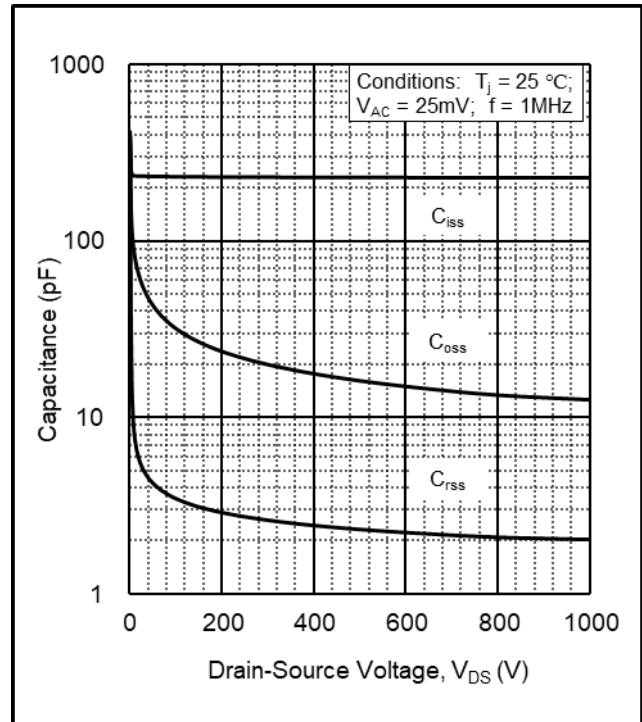


Figure 18. Capacitance vs. drain-source voltage
(0 - 1000V)

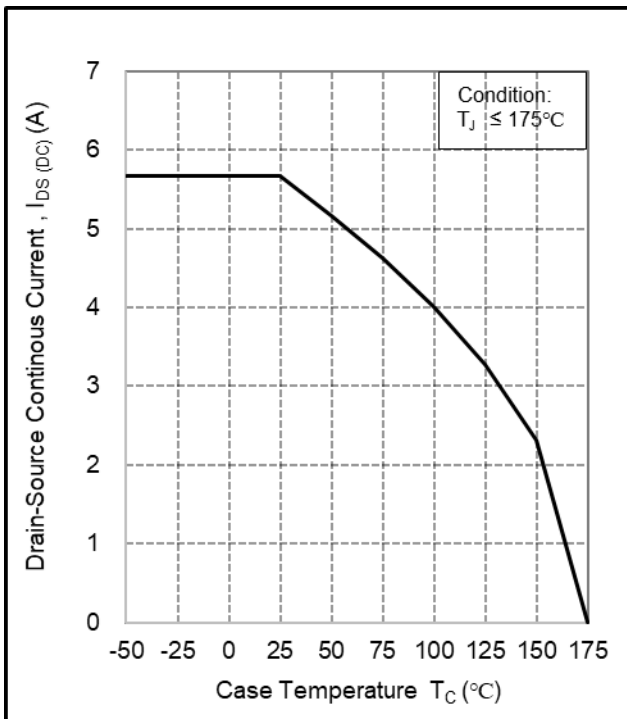


Figure 19. Continuous drain current derating
vs. temperature

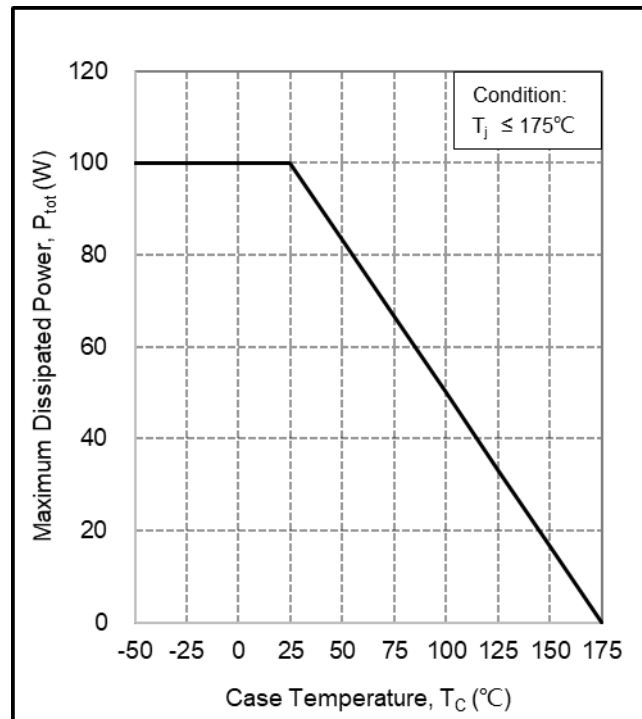


Figure 20. Maximum power dissipation derating
vs. temperature

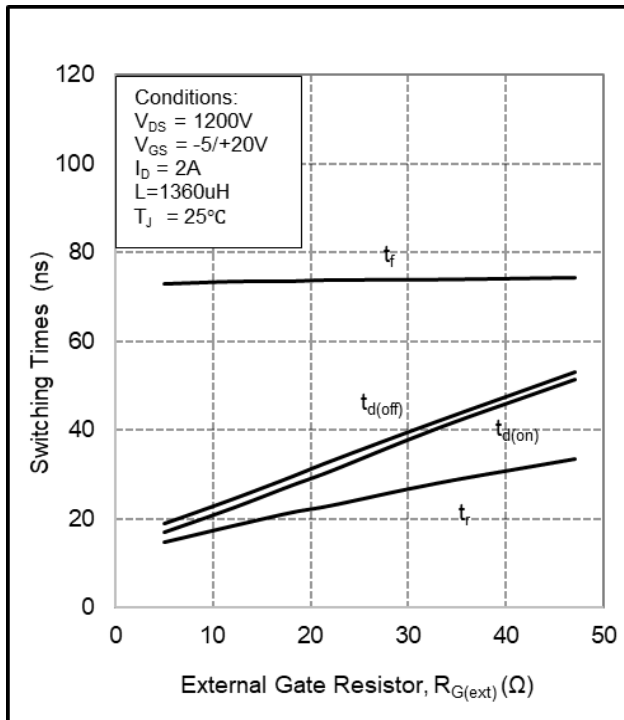


Figure 21. Switching Times vs. $R_{G(ext)}$

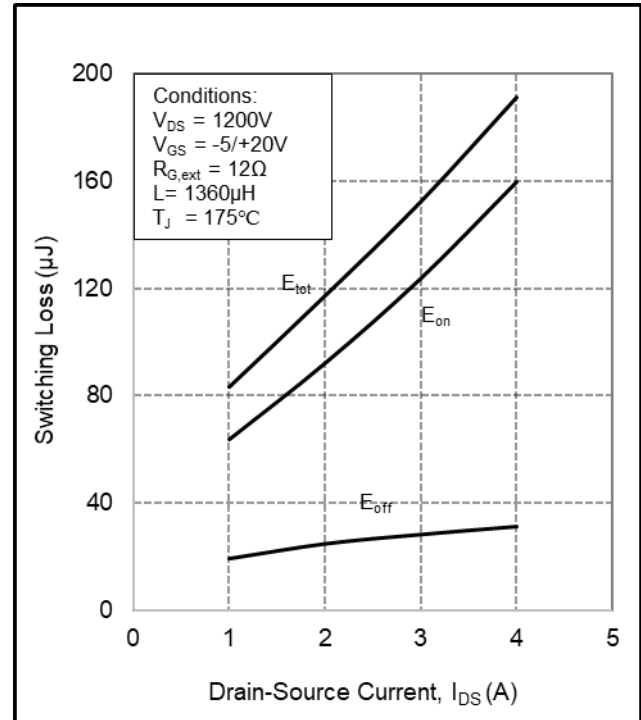


Figure 22. Clamped inductive Switching energy vs. drain current

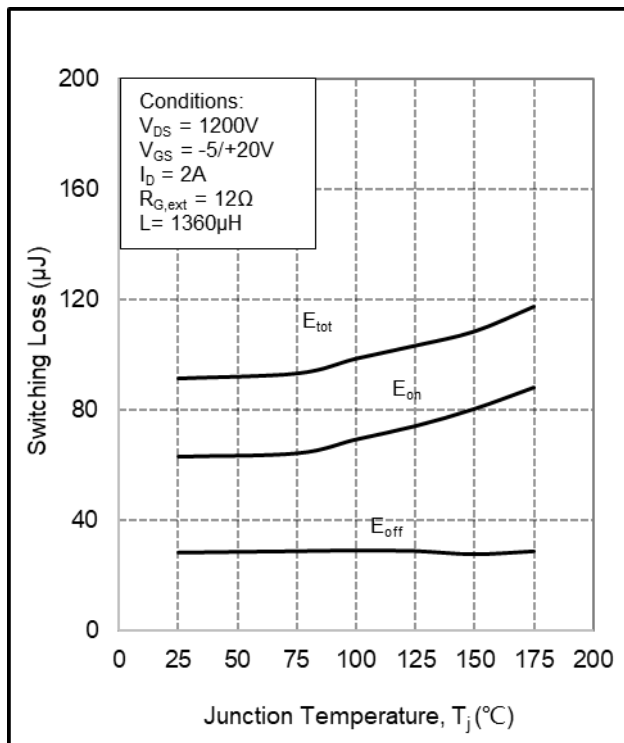


Figure 23. Clamped inductive Switching energy vs. temperature

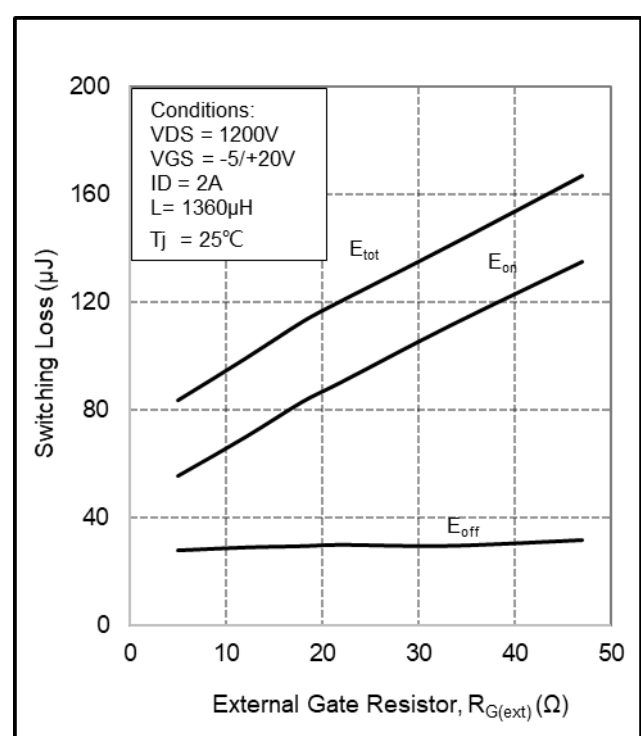


Figure 24. Clamped inductive Switching energy vs. $R_{G(ext)}$

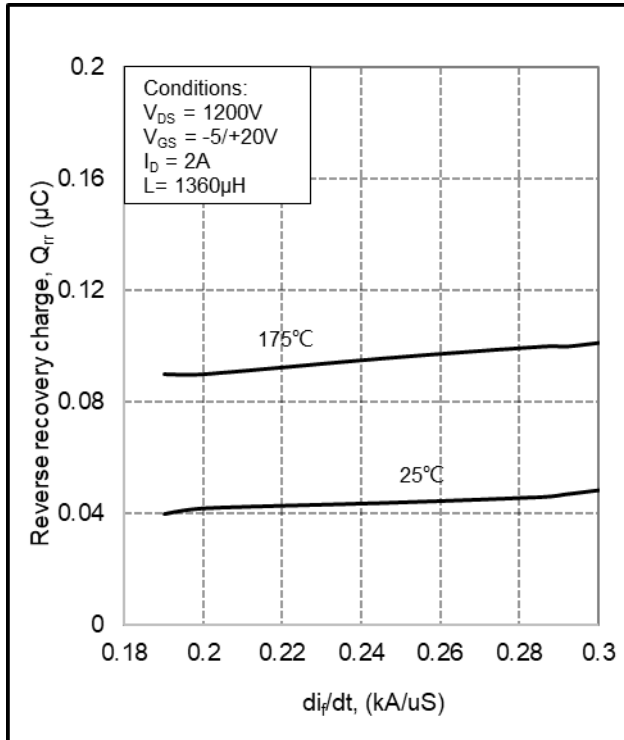


Figure 25. Reverse recovery charge vs. di/dt

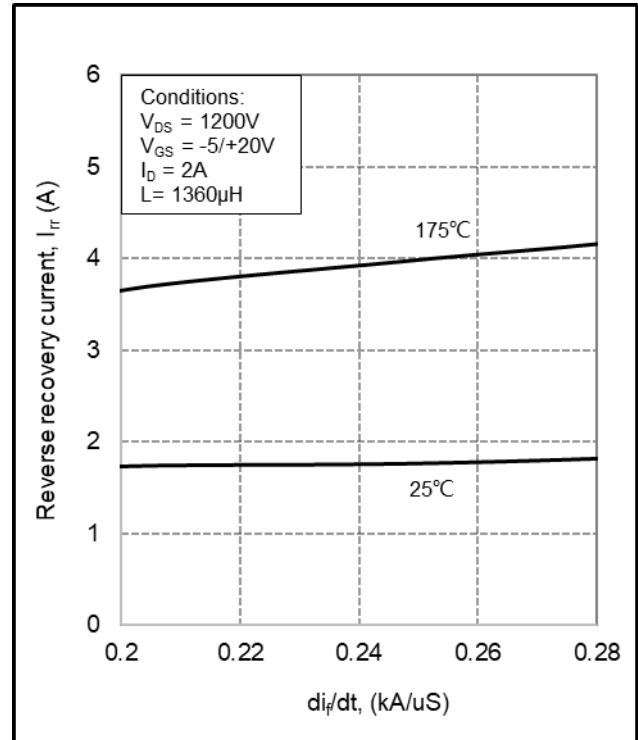


Figure 26. Reverse recovery current vs. di/dt

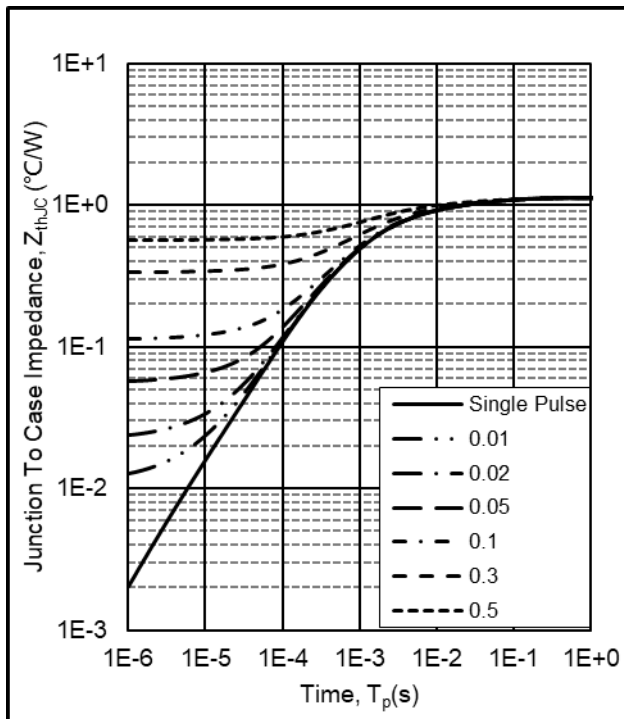


Figure 27. Transient Thermal Impedance
(Junction - Case)

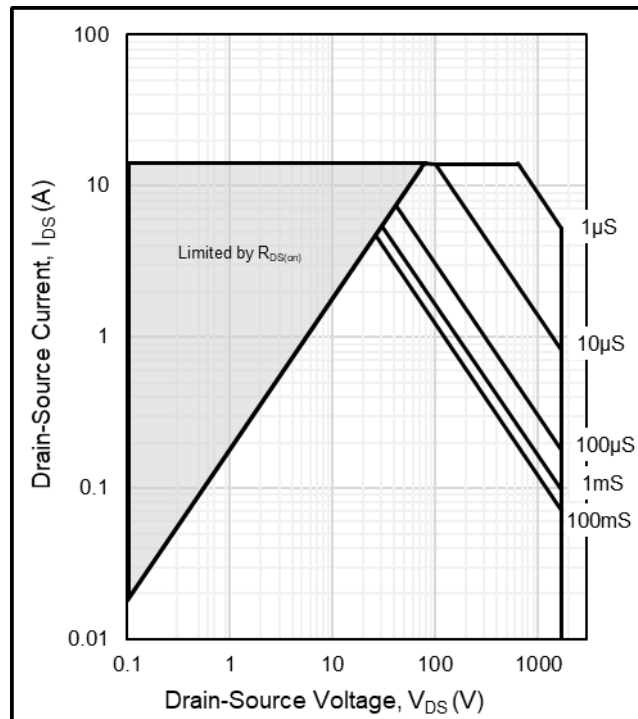


Figure 28. Safe Operating Area

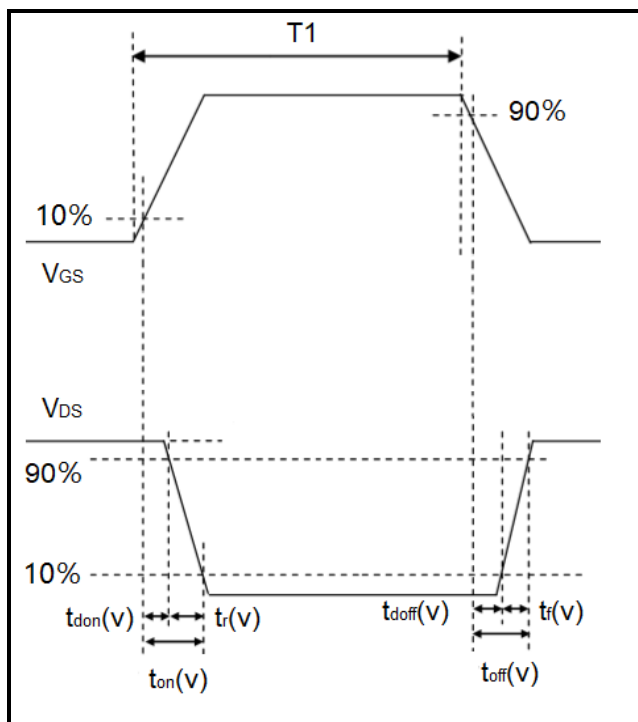


Figure 29. Switching times definition

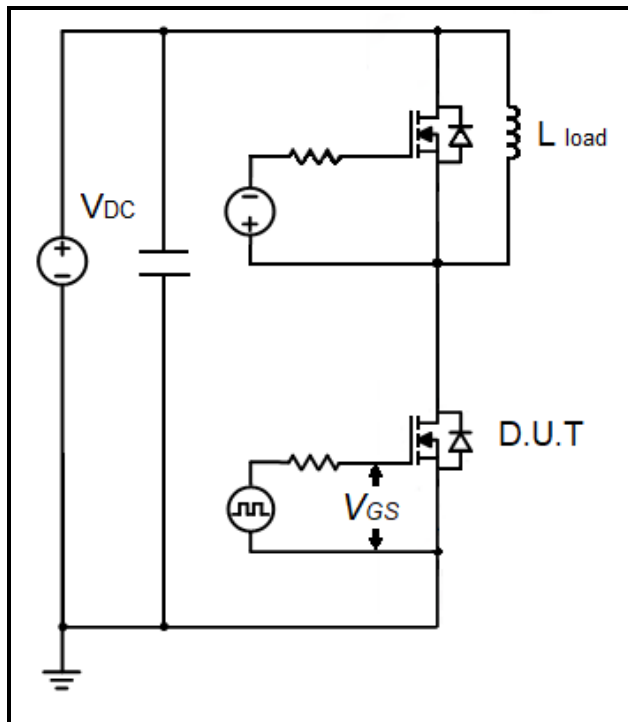
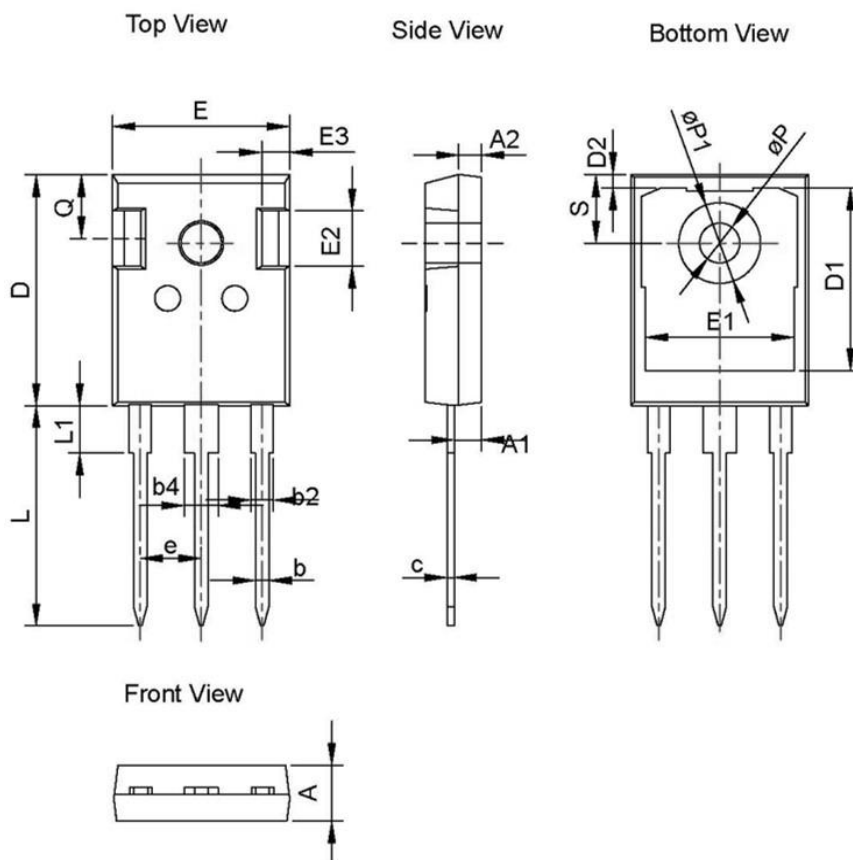


Figure 30. Clamped inductive switching waveform test circuit

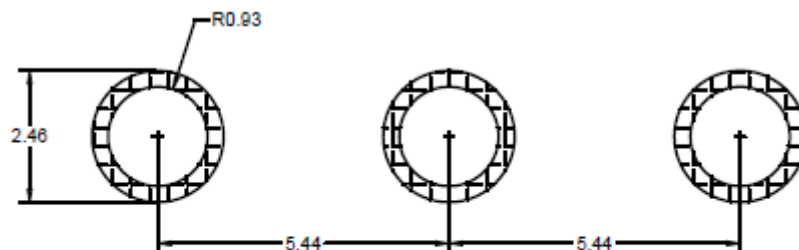
Package Information



Dimension unit: [mm]			
Symbol	Min	Nom	Max
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.60	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
D2	1.00	1.20	1.35
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44 BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
ϕP	3.40	3.60	3.80
$\phi P1$	-	-	7.30
Q	5.40	5.80	6.20
S	6.20 BSC		

Recommended Solder Pad Layout

Note: All dimensions are in mm



TO-247-3L

Ordering Information

Part number	SMS1701000K-ISATH
Package	TO-247-3L
Unit quantity	300 EA
Packing type	Tube

Important Notices – Read Carefully

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